Gate-tunable Hall sensors on large area CVD graphene protected by h-BN with 1D edge contacts

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Graphene is an excellent material for Hall sensors due to its atomically thin structure, high carrier mobility, and low carrier density. However, graphene devices need to be protected from the environment for reliable and durable performance in different environmental conditions. Here we present magnetic Hall sensors fabricated on large area commercially available chemical vapor deposited (CVD) graphene protected by exfoliated hexagonal boron nitride (h-BN). To connect the graphene active regions of Hall samples to the outputs, 1D edge contacts were utilized which show reliable and stable electrical properties. The operation of the Hall sensors shows the current-related sensitivity up to 345 V/(AT). By changing the carrier concentration and type in graphene by the application of gate voltage, we are able to tune the Hall sensitivity. Published by AIP Publishing.

I. INTRODUCTION

As our society becomes more integrated with information technology, sensors are getting increasingly important as can be seen from the consistent global market growth. A variety of different sensors based on magnetic field sensing are used, more than 50% of which exploit the Hall effect for their operation. These are utilized in many fields such as healthcare, automotive, industry, and consumer electronics for a broad spectrum of applications including position sensing, current monitoring, proximity detection, and others. Si is the most commonly used material in active regions of Hall sensors as the technology is highly mature with relatively low manufacturing costs and reasonable current-related sensitivities $S_H \sim 100$ V/(AT). For better sensitivity, Hall sensors based on high mobility compound semiconductor heterostructures are used, but yield an increased production price.

Fostered by the constant strive for performance improvements and market price reduction, new materials are being considered for utilization in Hall sensors. Graphene is a highly promising material for Hall sensing applications due to its beneficial properties such as low charge carrier concentration and high mobility. Recent experimental reports have already proven the feasibility of graphene Hall sensors with current-related sensitivity more than 60 times higher than in silicon-based sensors. Furthermore, graphene’s 2D nature allows its application in flexible and transparent electronics. However, the high-performance proof-of-concept devices demonstrated so far either use exfoliated graphene flakes, or are made of a manually selected single crystal CVD graphene area, or the graphene active layer is not protected from external influences required for robust device operation. To harness the advantages of industrially compatible large area CVD graphene in Hall sensors, it is necessary to protect the graphene layer for reliable device performance in different environmental conditions.

Here we report magnetic Hall elements fabricated on large area CVD graphene covered by h-BN for protection against the environmental influences to ensure reliable operation and long lifetime. The insulating and two-dimensional nature of the h-BN capping layer is expected to have a good interface with graphene, containing fewer dangling bonds and charge traps. Our devices incorporate 1D edge contacts to graphene/h-BN heterostructures, which circumvent the effects related to contact-induced doping. These factors are crucial for reliable performance of graphene Hall sensors in an ambient environment.

II. RESULTS AND DISCUSSIONS

The Hall sensor devices were fabricated on commercially available CVD graphene after it was transferred on a Si/SiO$_2$ substrate (Graphenea$_{20}$). The schematic illustration and an optical image of the investigated Hall samples are shown in Figs. 1(a) and 1(b), respectively. The microfabrication process steps are depicted in Fig. 1(c). The h-BN was exfoliated from the bulk crystal on top of CVD graphene by means of a regular scotch-tape method. Then the unprotected graphene regions were etched away with oxygen plasma. 1D edge contacts were fabricated by means of electron beam lithography and electron beam evaporation of metals (1 nm TiO$_2$/65 nm Co) followed by liftoff in acetone/isopropyl alcohol.

Figure 2 shows the characterization of the graphene/h-BN heterostructure and 1D edge contacts to graphene. The Raman spectra of CVD graphene and the graphene/h-BN heterostructure are shown in Figs. 2(a) and 2(b), respectively. The absence of a band splitting of the 2D peak and its higher intensity compared to the G peak indicate that the graphene in the heterostructure is monolayer. The CVD

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The graphene used here has grain sizes of 1–5 μm (Graphenea). The typical two-terminal current-voltage (I-V) characteristic at room temperature [Fig. 2(c)] shows a linear behaviour and the contact resistances, estimated from the analysis of data from local four- and two-terminal measurements, are reproducible in several devices. The field-effect mobility of the measured graphene at room temperature is found to be ~133 cm²/V s⁻¹ and the sheet resistance is ~17 kΩ/□.

Figure 3(a) shows the Hall measurements performed using the measurement configuration depicted in the inset. The Hall voltage ($V_H$) response of the investigated samples obtained during the magnetic field (B) sweep at applied current bias $I = 15 μA$ is fitted with\(^5\) \(\frac{\partial V_H}{\partial B} = \frac{1}{en_{2D}} I\),

\[ (1) \]

where $e$ is the elementary electron charge and $n_{2D}$ is the charge carrier density. Graphene was found to exhibit hole conduction at zero back gate voltage with a sheet charge carrier concentration of $n_{2D} = 1.75 \times 10^{12}$ cm⁻² and a background voltage offset of 3 mV, which has been subtracted from the measured raw data. The linearity errors\(^5\) \(^9\) \(^12\) were found to be within ±3.1% with an average absolute value of 1.3% over a large magnetic field range from −760 mT to 780 mT at room temperature. From the measured Hall voltage response as a function of time at different perpendicular magnetic fields [Fig. 3(b)], one can estimate the noise level and the minimum resolvable magnetic field of ~20 mT at room temperature. From the Hall measurements, the calculated current-related Hall sensitivity

\[ S_I = \frac{1}{I} \left. \frac{\partial V_H}{\partial B} \right|_{I=\text{const}} \]

\[ (2) \]

did not show significant bias-related change in the bias current range from 15 μA to 50 μA at room temperature [Fig. 3(c)].

Next, we investigated the Hall sensitivity of graphene for different carrier concentrations in electron- and hole-doped regimes at room temperature (Fig. 4). Graphene has the unique property that its charge carrier type and concentration can be tuned continuously by applying gate voltage. Figure 4(a) shows the Hall measurements with application of gate voltages $V_g = ±40 \text{ V}$, where a sign change is observed in the slope of Hall response for electron- and hole-doped regimes. From the back gate dependence of the graphene resistance the charge neutrality point was found at $V_g = 26 \text{ V}$. Such full gate-dependent Hall effect measurements were performed by sweeping the back gate voltages from electron to hole type of conduction across the Dirac point in the presence of different perpendicular magnetic fields [bottom panel of Fig. 4(b)]. To reduce the influence of
the device geometry, the Hall response at different magnetic fields ($V_{H}(B)$) is subtracted from measured response at 0 T magnetic field ($V_{H0}$): $\Delta V_{H} = V_{H}(B) - V_{H0}$. Here, we observe a change in the amplitude and sign of $\Delta V_{H}$ by sweeping the gate voltage. Using Eq. (2), we extracted the current-related sensitivity by fitting the $\Delta V_{H}$-$B$ dependencies. It is plotted in Fig. 4(c) as a function of gate voltage at room temperature. This dependence reveals the gate tuning of the sensitivity with maxima (up to 345 V/(AT)) close to the graphene Dirac point. Such tunability of Hall response stems from gate voltage-induced change of the carrier concentration in the graphene sheet yielding a change of the current-related sensitivity and the output Hall voltage [Eqs. (1) and (2)].

These sensitivities of the large area CVD graphene/h-BN heterostructure-based Hall sensors are at least three times higher than current silicon-based devices. Previous studies reported on the performance of unencapsulated graphene Hall sensors with sensitivities up to 2093 V/(AT). Such unprotected devices are known to be extremely sensitive to environmental parameters and degrade rapidly. Consequently, an insulating barrier is required to protect the graphene layer. Even though recent studies on CVD graphene with Al2O3 encapsulation show very promising results on easily scalable and reproducible device fabrication techniques, the 2D and atomically flat nature of insulating h-BN demonstrated outstanding electronic properties when encapsulating graphene. Such exfoliated h-BN/graphene/h-BN heterostructure Hall devices were demonstrated with sensitivities 15 times higher than in our h-BN capped CVD graphene. This can be attributed to the growth quality of CVD graphene, presence of grain boundaries, and charge doping from the SiO2 substrate as well as contaminations introduced during wet transfer of CVD graphene. Contaminations and grain boundaries are also likely reasons behind the observed low mobility and high sheet resistance of graphene. The measured Hall sensitivities could be further increased by improving the CVD graphene mobility by reducing the SiO2 substrate-induced doping and graphene-substrate interactions by fully encapsulating CVD graphene in h-BN. Utilizing high-quality, large-grain graphene growth and Ohmic instead of tunnel contacts would also allow to significantly improve graphene characteristics and sensing.

FIG. 3. Operation of CVD graphene/h-BN Hall elements. (a) Output Hall voltage as a function of perpendicular magnetic field measured at $I = 15 \mu A$ at room temperature (circles) with linear fitting (solid line) according to Eq. (1). Inset: Hall measurement configuration. (b) Output Hall voltage as a function of time at different applied magnetic fields measured at $I = 15 \mu A$ and at room temperature. (c) Current-related sensitivity as a function of current bias.

FIG. 4. Gate tunability of CVD graphene/h-BN Hall sensors at room temperature. (a) Output Hall voltage as a function of perpendicular magnetic field at gate voltages of ±40 V. (b) Back gate dependence of graphene resistance (top) and Hall voltage response at different perpendicular magnetic fields (bottom). $V_{D}$ is the gate voltage corresponding to the Dirac point. (c) Absolute value of current-related sensitivity calculated from Hall voltage response as a function of gate voltage according to Eq. (2). The measurements at 75 K will be published elsewhere.
performance. Furthermore, as a next step forward a fully scalable fabrication approach should be considered with utilization of only all-CVD h-BN/graphene/h-BN stacks patterned on a large area.

III. CONCLUSIONS

In summary, we demonstrated the operation of graphene magnetic Hall elements fabricated on large area CVD graphene on a Si/SiO$_2$ substrate with h-BN capping and 1D edge contacts. Such heterostructure devices showed reliable contact properties and Hall sensor performance. The samples showed a constant bias dependence of the current-related sensitivity at room temperature. Gate voltage-induced tunability of the Hall response was observed with a maximum current-related sensitivity of 345 V/(AT) close to the Dirac point. These results obtained in graphene Hall sensors with h-BN protection are promising for operation in ambient conditions and for potential applications in transparent and flexible electronics.

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