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## Metal-Free Graphene as Transparent Electrode for GaN-Based Light-Emitters

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Graphene contacts to p-GaN are considered as an alternative to indium-tin-oxide transparent electrodes in GaN based vertical-cavity surface-emitting lasers (VCSELs). Contact properties were investigated on light-emitting diode and p-GaN test structures, where dielectric apertures were used to eliminate the influence of the metal pads used to bias the contacts. Using single layer graphene we were able to operate light emitting diodes with current densities of 300 A/cm<sup>2</sup>. Addition of a second layer of graphene increased the maximum bias current to 1 kA/cm<sup>2</sup>. However, the contacts are non-linear and cannot withstand high current densities for a long time. The results are promising but further investigation and improvement is needed for graphene to be a viable alternative to indium-tin-oxide for blue VCSELs.

### 1. Introduction

In GaN-based vertical-cavity surface-emitting lasers (VCSELs) there is a need for a transparent current spreading layer. So far, indium-tin-oxide (ITO) has been the material of choice for the transparent electrode.<sup>1-5)</sup> However, graphene has been suggested as an alternative to ITO providing lower optical absorption in the ultraviolet-blue regime for a similar sheet resistance,<sup>6)</sup> and potential cost reductions.<sup>7)</sup> The work functions of ITO and graphene are similar, but the Fermi level of graphene could be affected by charge carrier transport due to the low density of states in graphene and result in a lowered barrier between graphene and metals.<sup>8)</sup> Recent experimental results support these findings for graphene on p-GaN.<sup>9)</sup> Thus utilizing graphene instead of ITO could possibly reduce the contact resistivity to p-GaN, which is a major concern in GaN-based VCSELs.

Graphene as a transparent conductive layer has in the literature been explored primarily for use in light emitting diodes (LEDs).<sup>10-13)</sup> The main focus of the research has been on proof-of-concept. A few reports<sup>14,15)</sup> indicate specific contact resistivity of 1-5 Ω cm<sup>2</sup> between graphene and p-GaN, which is too large for device applications. Furthermore, in most of these investigations the test structure consists of pads with p-GaN/graphene/metal contact. In graphene-LEDs most of the contact area is the graphene/p-GaN contact, without any metal. Also in VCSELs metals should be avoided due to the high absorption loss in them; it is thus important to investigate the graphene/p-GaN contacts without metals.

In this work, we have designed and fabricated test structures dedicated to the investigation of the pure, metal-free, graphene/p-GaN contact on p-GaN samples as well as full light-emitting diode (LED) structures with current confining apertures similar to those used in GaN-based VCSELs.

### 2. Experimental Procedure

The epitaxial structures used for evaluating the graphene / p-GaN contacts were a p-GaN structure consisting of (15 nm pp-GaN) / (750 nm p-GaN) / (2 μm nid-GaN) and a LED structure consisting of (50 nm pp-GaN) / (150 nm p-GaN) / (20 nm p-AlGaIn) / (92 nm nid-GaN with five 4 nm wide In-GaN quantum wells with 12 nm barriers) / (1.5 μm n-GaN) / (3 μm nid-GaN), both grown by metal-organic chemical vapour deposition (MOCVD) on sapphire substrate.

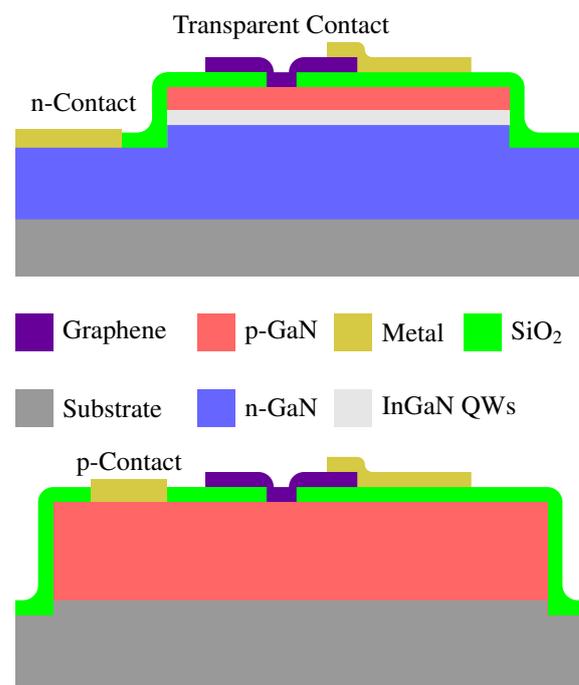


Fig. 1. (Color online) The SiO<sub>2</sub> dielectric insulates the metal bondpad and ensures that the only current path is through the graphene/p-GaN interface. Test structures were fabricated on both LED (above) and p-GaN (below) samples.

A schematic view of the p-GaN and LED test structures is shown in Fig. 1. The fabrication started with etching mesas using Cl<sub>2</sub>/Ar reactive ion etching into the nid-GaN and n-GaN layers, respectively. The samples were cleaned in BASF Sioetch MT 06/01, buffered oxide etch (BOE), for 5 min before a 50 nm thick SiO<sub>2</sub> layer was sputtered on top. Openings for metal contacts (aperture diameter of 2, 4, 6, 8, 10, and 12 μm) in the SiO<sub>2</sub> were etched for 30 s in BOE. Pd/Au (30/200 nm) and Ti/Al/Ti/Au (5/10/15/200 nm) contacts were deposited by evaporation, on the p-GaN and LED structures, respectively. Rapid thermal annealing in N<sub>2</sub> atmosphere was then done for 1 minute at 800°C for the n-contacts on LED samples and for 15 minutes at 550°C for the p-contacts on p-GaN. Holes in the SiO<sub>2</sub> for the graphene contacts were opened by etching in BOE for 30 s. The graphene was grown on Cu substrates by chemical vapour deposition (CVD); for growth details see 16. The growth gives large uniform single layer

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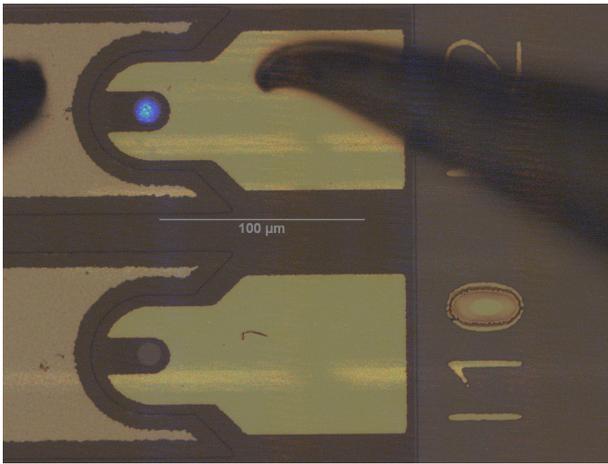


Fig. 2. (Color online) Spontaneous emission from LED with 12  $\mu\text{m}$  diameter aperture graphene contact during I-V-scan.

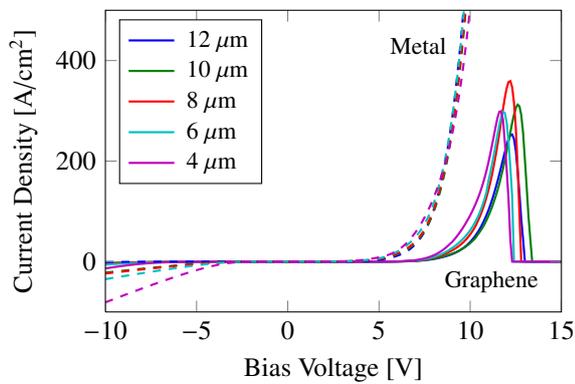


Fig. 3. (Color online) Comparison of current density for metal (dashed) and graphene (solid) contacts to LEDs with different aperture diameters.

graphene, verified by Raman spectra. The graphene is lifted-off from the Cu substrate by electrolytic delamination,<sup>17,18)</sup> and is then transferred to the p-GaN. The transfer process was repeated to achieve not only single but also dual layer graphene. The graphene was then patterned using  $\text{O}_2$  plasma and bond pads of Ti/Pd/Au (5/30/200 nm) were electron-beam evaporated. On the LED chips, reference components were also fabricated with Ti/Pd/Au contacts instead of graphene.

The graphene/p-GaN contacts were evaluated using the Keithley 4200-SCS parameter analyzer. The maximum applied voltage during current-voltage (I-V)-scans was gradually increased to examine both repeatability and breakdown conditions. Contact stability over time was evaluated by applying a fixed voltage and monitoring the current.

### 3. Results and Discussion

The imaged spontaneous emission from a 12  $\mu\text{m}$  diameter LED is shown in Fig. 2. It confirms current confinement by the aperture and current injection through the graphene to the top p-GaN layer of the LED.

The I-V characteristics for LEDs are shown in Fig. 3. The turn-on voltage for the LEDs with graphene contacts is about 3 V higher than for the LEDs with Ti/Pd/Au contacts. The Ti/Pd/Au contacts are not conventional contacts to p-GaN, and give a slightly larger turn-on voltage compared to ohmic contacts. The metal contacts to n-GaN were ohmic. The LEDs

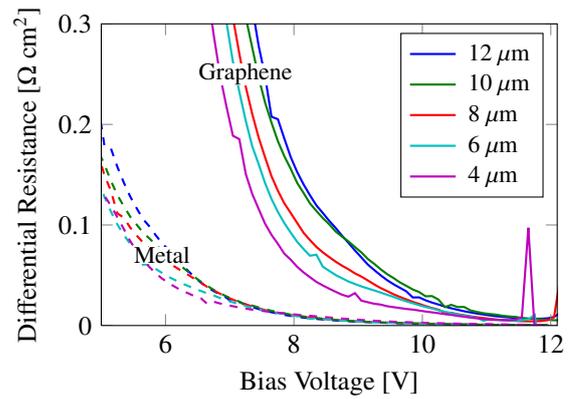


Fig. 4. (Color online) Comparison of differential specific resistance for metal (dashed) and graphene (solid) contacts to LEDs with different aperture diameters.

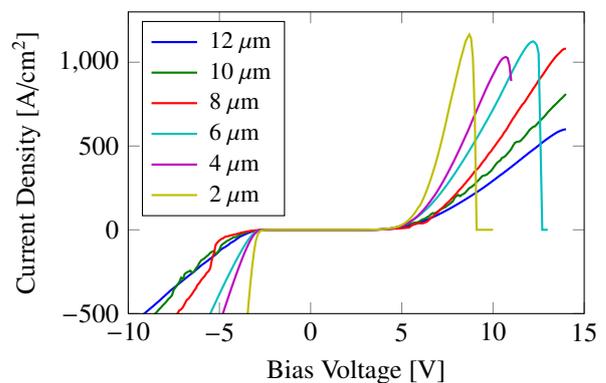


Fig. 5. (Color online) Current density as a function of voltage for dual layer graphene on p-GaN, for contacts with different aperture diameters.

with graphene contacts break-down after a current density of around 300  $\text{A}/\text{cm}^2$  has been reached, independent of aperture diameter, and the damage is irreversible. After turn-on voltage has been reached the differential resistance of the LEDs with graphene contacts is less than  $0.1 \Omega \text{ cm}^2$ , i.e., approaching that of the LEDs with metal contacts, Fig. 4.

Single and dual layer graphene contacts were fabricated on p-GaN samples. The single layer graphene has a sheet resistance of  $1000 \Omega/\square$  and the dual layer graphene has  $500 \Omega/\square$ . Figure 5 show I-V-characteristics of dual layer contacts. The I-V-curves are not symmetric, and show a forward voltage barrier that is larger than the reverse voltage barrier. As seen in the figure, the slopes of the I-V-curves are different, which is due to the differential resistance being dominated by the lateral resistance in the p-GaN layer. This effect was also observed in the I-V-characteristics for the LEDs, but less pronounced due to the larger conductivity of the n-GaN layer. The dual layer graphene contacts were able to withstand current densities of around  $1 \text{ kA}/\text{cm}^2$ , 2-3 times more than that of single layer graphene contacts.  $1 \text{ kA}/\text{cm}^2$  is close to the threshold requirements of  $3 \text{ kA}/\text{cm}^2$  for GaN-based VCSELs.<sup>1)</sup>

The stability of the graphene contacts at low forward bias is shown in Fig. 6. The contacts are degraded after a few minutes of operation, and the degradation is likely caused by the thermal oxidation of the graphene layer. Similar degradation effects have been observed by other groups.<sup>12)</sup>

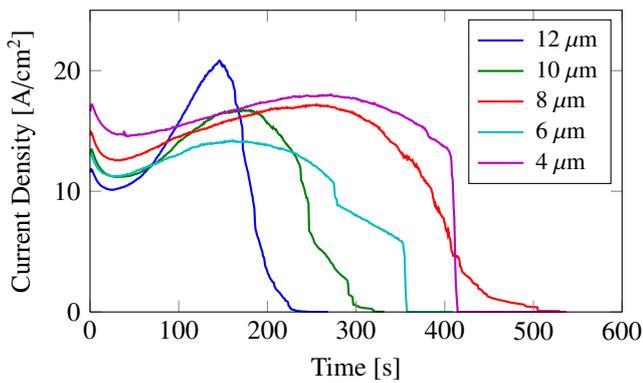


Fig. 6. (Color online) Current density as a function of time with a forward bias of 5 V on for dual layer graphene on p-GaN showing degradation of the dual layer graphene contacts with different aperture diameters.

The graphene contacts may have been negatively affected by plasma damage and oxidation of the thin pp-GaN contacting layer during sputtering of SiO<sub>2</sub>. Circular transmission line measurements show that the Pd/Au contacts on p-GaN were slightly nonlinear and have three times larger contact resistivity than samples where Pd/Au contacts were applied without SiO<sub>2</sub> deposition and removal.

#### 4. Conclusions

We have demonstrated the use of graphene as a transparent electrode on GaN-based LEDs without metal near the transparent contact. A dual layer graphene was able to withstand current densities of 1 kA/cm<sup>2</sup>, which is close to the required threshold current density in blue GaN-based VCSELs. However, the graphene/p-GaN contacts are not ohmic and degrade irreversibly under forward bias, also at low current densities. For graphene to be viable as a transparent contact for GaN based VCSELs, the contact stability must be improved. Efforts towards reducing the contact resistivity are needed, where direct growth of graphene<sup>19)</sup> on p-GaN could be an interesting option to explore. The use of coating layers blocking oxygen from reaching the contact interface could also potentially improve stability.

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