Reverse Schottky gate current in AlGaN-GaN high-electron-mobility-transistors

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Two theoretical one-dimensional models are developed for reverse currents through Schottky gate contacts on AlGaN-GaN high-electron-mobility-transistors (HEMTs) and compared with measurement data. One model covers ideal triangular and square junction barriers and contains current contributions of thermionic emission, thermionic field emission, and tunneling of electrons from the contact metal towards the two dimensional electron gas (2-DEG) at the AlGaN-GaN interface. The second model describes the contribution of trap assisted tunneling through a Schottky barrier. Both models are compared with measurements done on AlGaN-GaN diodes at reverse voltages between threshold voltage and zero volt, which is the regime in which the current flow can be described by one-dimensional models. The trap assisted tunneling model cannot explain the data. The first model agrees with the measurements only if it is assumed that the barrier is triangular and that the current only flows through a fraction (2×10^{-4}) of the junction area, probably through defect patches. The triangular barrier in the defects has a barrier height of 0.58 eV. This result is consistent with previously reported findings of defect patches at the AlGaN surface. (@ 2012 American Institute of Physics. [http://dx.doi.org/10.1063/1.4764866]

I. INTRODUCTION

Several models have been proposed to explain reverse leakage currents in Schottky gates on AlGaN-GaN high-electron-mobility-transistor (HEMT) structures. Essentially, two different groups can be distinguished: direct current models that are based on a combination of tunneling, thermionic field emission (TFE), and thermionic emission (TE)¹ and models that next to the direct current also include a current contribution from trap assisted tunneling (TAT).² All models depend strongly on the Schottky barrier shape, i.e., the energy of the conduction band in the AlGaN as function of depth beneath the metal-AlGaN interface.

Direct and TAT current models have been published before in AlGaN-GaN Schottky structures^{1,3} in MOS structures⁴ and capacitors.⁵ There are, however, several problems with the published models that justify a new approach. The direct current models are conflicting with each other in the electron energy that is used in calculating the probability of transmission through the barrier. Some authors only take the kinetic energy directed transversal to the Schottky interface,⁶ while others take the total electron energy.¹ We will show that both approaches are incorrect and that an energy somewhere in between these values has to be used, depending on the ratio of the effective masses of the electron in the Schottky metal and in the AlGaN. In many TAT models, fitting parameters are used in the expressions for the trapping and de-trapping rates.^{3,5,7} These models only consider the TAT current component. In this paper, however, we are treating both the direct current and the TAT current in the same Schottky junction. Therefore, we need a unified theory that treats direct and TAT currents on equal footing deriving both from the same first principles.

In this paper, we study a model for the direct current based on tunneling, TFE, and TE reverse leakage through triangular and square barriers in Sec. II. In Sec. III, we derive a model for the TAT current in a triangular barrier. As will be shown in Sec. IV, none of these models agree well with experiments if it is assumed that the total Schottky surface area contributes to the current. Some authors have suggested that the current is concentrated in numerous very small defect patches, sometimes associated with screw dislocations in the AlGaN, that have a low barrier height and carry almost all current.⁸⁻¹¹ The rest of the surface area would have a high barrier height such that its contribution to the current can be ignored. We can use the ideal barrier model to calculate the current in these patches if we assume that there is an effective total patch area and an effective barrier height. This patch model therefore introduces one extra parameter: the effective surface area. Since we do not know how the potential and the electric field are shaped inside these patches, we have to consider various possibilities and compare the results with the measurements. In Sec. II, we describe a few possible field distributions in the patches.

The calculations of the equations that are derived in the theoretical models are performed numerically (MATHCAD).

II. DIRECT REVERSE CURRENT THROUGH TRIANGULAR AND SQUARE SCHOTTKY BARRIERS

A. Triangular barrier

Fig. 1 shows the triangular Schottky barrier between gate metal and AlGaN-GaN 2-DEG in case of a negative applied voltage V_r . The gate current due to tunneling and

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FIG. 1. Leakage current mechanisms for triangular Schottky barrier under reverse bias. Reverse bias voltage is V_r . The three current contributions, tunneling, TFE, and TE are indicated. The main contribution by tunneling takes place at the metal Fermi level.

thermionic field emission can be derived using the Tsu-Esaki model.^{12,13} We follow the approach given by Gehring.⁶ The current going from metal to semiconductor carried by electrons that have a wave vector component in the x-direction perpendicular to the interface between k_x and k_x+dk_x is

$$dJ_{m\to s} = q\Gamma(\phi_{tun})v_x g(k_x)f_{FDm}(1-f_{FDs})dk_x.$$
 (1)

The velocity in the x-direction: $v_x = \frac{\hbar k_x}{m_m}$, where m_m is the electron mass in the metal.

The density of states in the metal in *k*-space is $g(k_x) = 2 \int_{0}^{\infty} \int_{0}^{\infty} \frac{1}{4\pi^3} dk_y dk_z$. The factor 2 arises from spin degeneracy.

We use the notations: $\phi_x = \frac{\hbar^2 k_x^2}{2m_m}$ and $\varphi_{//} = \frac{\hbar^2 k_{//}^2}{2m_m} = \frac{\hbar^2}{2m_m} \frac{\hbar^2 k_{//}^2}{2m_m}$ $(k_y^2 + k_z^2)$ for the energies in the x-direction and parallel to the gate metal surface, respectively.

The Fermi-Dirac distribution functions for the metal and the AlGaN semiconductor are f_{FDm} and f_{FDs} , respectively,

$$f_{FDm} = \frac{1}{1 + \exp\left\{\frac{q(\phi_x + \phi_{//} - \varphi_F)}{kT}\right\}}.$$
 (2)

For the AlGaN semiconductor region, we assume that $l_{f_{FDs}} \approx 1$.

The quantum mechanical tunneling probability through the barrier can be calculated in the case of a triangular barrier as¹⁴

$$\Gamma(\phi_{tun}) = \exp\left\{-\frac{\alpha_s}{E}(\varphi_F + \varphi_B - \phi_{tun})^{3/2}\right\},\qquad(3)$$

where $\alpha_s = \frac{4\sqrt{2m_sq}}{3\hbar}$ and m_s is the electron mass in the semiconductor. *E* is the electric field. The metal Fermi level is given by φ_F and the barrier height, i.e., the distance between top of the barrier and the metal Fermi level, is given by φ_B .

Very often, a wrong expression is used for the electron energy ϕ_{tun} in Eq. (3). Some authors take $\phi_{tun} = \phi_x$, while others take $\phi_{tun} = \phi_x + \phi_{//}$.^{1,6} However, it is easy to see that the electron wave function in the metal can be described by

$$\psi_m = \{A\exp(ik_x x) + B\exp(-ik_x x)\}\exp(ik_y y + ik_z z).$$

Note that this equation contains a wave in the negative *x*-direction, which is the reflected wave at the metalsemiconductor interface. The electron wave in the semiconductor is given by

$$\psi_s = C \exp(-\kappa_x x) \exp(ik_y y + ik_z z),$$

where κ_x described the damping of the wave in the semiconductor.

This is only valid very close to the metal interface, such that we may approximate the barrier potential by a fixed value $\varphi_F + \varphi_B$ and we can ignore the triangular nature of the barrier shape. Using the appropriate boundary conditions and Schrödinger equation, we can calculate

$$\begin{aligned} \kappa_x^2 &= \frac{2m_s}{\hbar^2} (\varphi_F + \varphi_B - \phi) + k_y^2 + k_z^2 \\ &= \frac{2m_s}{\hbar^2} \left(\varphi_F + \varphi_B - \phi_x - \frac{m_s - m_m}{m_s} \phi_{//} \right) \end{aligned}$$

where ϕ is the total electron energy $\phi = \phi_x + \phi_{//}$. Therefore, energy ϕ_{tun} is

$$\phi_{tun} = \phi_x + \frac{m_s - m_m}{m_s} \phi_{//}.$$
(4)

Inserting Eq. (4) into Eq. (2) and changing the integral of Eq. (1) over k_x into an integral over ϕ_x gives

$$J_{m \to s} = \frac{4\pi m_m q^3}{h^3} \int_{0}^{\phi_F + \phi_B} \int_{0}^{\phi_F + \phi_B - \phi_x} \int_{0}^{\phi_F + \phi_B - \phi_x} (5)$$

$$\times \frac{\exp\left\{-\frac{\alpha_s}{E}(\phi_F + \phi_B - \phi_{tun})^{3/2}\right\}}{1 + \exp\left\{\frac{q(\phi_s + \phi_{//} - \phi_F)}{kT}\right\}} d\phi_{//} d\phi_x.$$

In deriving Eq. (5), we have transformed the integral over $dk_y dk_z$ using $dk_y dk_z = \frac{2\pi m_m}{\hbar^2} d\varphi_{//}$. The thermionic emission can be found from Eq. (5) by

The thermionic emission can be found from Eq. (5) by taking the integral boundaries for $\phi_{//}$ from 0 to infinity and those for ϕ_x from $\varphi_F + \varphi_B$ to infinity and approximating the Fermi-Dirac distribution by the Boltzmann distribution. Then Eq. (5) can be calculated analytically

$$J_{m\to s} = \frac{4\pi m_m q k^2 T^2}{h^3} \exp\left\{\frac{-q\varphi_B}{kT}\right\},\tag{6}$$

which is the standard expression for thermionic emission. Note that we did not take barrier lowering effects, e.g., due to image charge, into account, which is allowable since the barrier height will in practice be used as a fitting parameter.⁴

B. Square barrier

In case of a square barrier, i.e., a barrier with a constant potential $\varphi_F + \varphi_B$, irrespective of position *x*, the expression for the current changes by using a modified equation for Eq. (2). In this case,¹⁴

$$\Gamma_{sq}(\phi_{tun}) = \exp\left\{-2d\frac{\left[2m_s(\phi_F + \phi_B - \phi_{tun})\right]^{1/2}}{\hbar}\right\}.$$
 (7)

We will use the summation of Eqs. (5) and (6) to describe the direct current through an ideal barrier. There is also an electron flow $J_{s\to m}$ from the semiconductor to the metal. This current is, however, so low that it is only of importance at very low bias voltages that we will not consider.

C. Patch models

Patch models assume that the junction is for the larger part ideal and has a high barrier height φ_B . A barrier height in the order of 1.5–2 eV would fit to the material parameters and the measured threshold voltage. Such a barrier would hardly conduct any current. Numerous very small defect patches carry almost all current. We can use the ideal barrier model to calculate the current if we assume that there is an effective total patch area and an effective barrier height. However, since we do not know how the potential or the electric field is shaped inside such patch, we have to consider various possibilities and compare the results of these with the measurements.

1. Patch model 1 (the constant field patch model)

This model assumes that the electric field in the patch is constant between the metal and the 2-DEG. See Fig. 2(a) where the potential inside the patch is displayed by the thick line. This is the simplest approximation for the field that can be made.

2. Patch model 2 (the zero field patch model)

This model assumes that the patches have a reduced barrier height but constant potential (zero electric field) in the patch up to a depth d_{patch} where the patch potential equals the potential of the surrounding ideal barrier. See Fig. 2(b) where the potential inside the patch is displayed by the thick line. The current can be calculated by multiplying the transmission of the constant potential part of the barrier ($0 < x < d_{patch}$) with that of the triangular barrier ($x > d_{patch}$). This can be done by using Eq. (7) and modifying Eq. (5) to

$$J_{m\to s} = \frac{4\pi m_m q^3}{h^3} \int_0^{\varphi_F + \varphi_B} \int_0^{\varphi_F + \varphi_B - \phi_x} \frac{\Gamma_{sq}(\phi_{tun})\Gamma(\phi_{tun})}{f_{FDm}(\phi_x)} d\phi_{//} d\phi_x.$$

3. Patch model 3 (the zero potential patch model)

This model looks much like the zero field model with the modification that the flat potential is taken to be zero. In fact, it assumes that the patch acts as an extension of the metal up to the point $x = d_{patch}$. This gives the electrons a free ride up to a certain depth inside the barrier. This means that we can calculate the current by assuming an ideal triangular barrier with reduced barrier height but with an electric field equal to the electric field in the surrounding area.



FIG. 2. (a) The constant field patch model with triangular potential barrier. (b) Zero field patch model. The potential is constant to a depth d_{patch} and equal to the surrounding potential for $x > d_{patch}$. In both models, the potential inside the patch is indicated by the thick lines.

III. TRAP ASSISTED TUNNELING

We will first give an expression for trapping of an electron from the metal by a trap in the AlGaN semiconductor when the barrier has a square potential. This has been described by Lundström and Svensson¹⁵ in a way fully consistent with the theory in Sec. II. We will extend the theory from the square potential case to that of a triangular potential barrier. If the barrier has a square potential, the total capture probability for electrons with an energy equal to the trap energy is¹⁵

$$\wp = \int_{0}^{k_{0}} p dk_{//} = \frac{4\hbar m_{s}}{m_{m}^{2}} \frac{\alpha}{\alpha^{2} + k_{0}^{2}} \times \int_{0}^{k_{0}} \exp\left\{-2\sqrt{\alpha^{2} + k_{//}^{2}}W\right\} \sqrt{k_{0}^{2} - k_{//}^{2}} k_{//} dk_{//}.$$
 (8)

In this expression, *W* is the distance of the trap from the gate metal and $\alpha^2 = \frac{2m_r\varphi_t}{\hbar^2}$ where φ_t is the trap potential measured from the bottom of the conduction band. The electron energy equals the trap potential. This assumption is justified because zero phonon transitions are dominating the capture process.¹⁶ Hence,

$$k_x^2 + k_y^2 + k_z^2 = k_x^2 + k_{//}^2 = k_0^2 = \frac{2m_m(\varphi_F + \varphi_B - \varphi_t)}{\hbar^2}.$$

In the case of a triangular barrier, the trap assisted tunneling model, in its simplest form, assumes that there are traps uniformly distributed throughout the AlGaN layer that have an energy level φ_t below the conduction band. Electrons from the metal may tunnel to the traps with a probability P_1 and from there tunnel to the semiconductor with a probability P_2 . An illustration of this process is shown in Fig. 3.

The Schrödinger equation for the triangular barrier is

$$\nabla^2 \psi = \frac{2m_s}{\hbar^2} \{ \varphi_F + \varphi_B - E \cdot x - \phi \} \psi.$$

There is no analytical solution to the Schrödinger equation of a triangular barrier, but we can get an approximate solution by splitting the variables (for simplicity, only the *x*-



FIG. 3. Tunneling by electrons having energy ϕ assisted by traps at energy level ϕ_t below the conduction band in AlGaN.

and *y*-components are taken into account. It is straightforward to extend the final result also to the *z*-direction)

$$\psi(x, y) = \xi(x)\chi(y),$$

$$\frac{1}{\xi}\frac{\partial^2 \xi}{\partial x^2} + \frac{1}{\chi}\frac{\partial^2 \chi}{\partial y^2} = \frac{2m_s}{\hbar^2} \{\varphi_F + \varphi_B - E.x - \phi\}$$

We now assume that $\chi = \exp(ik_y y)$, so the Schrödinger equation reduces to:

$$\frac{1}{\xi}\frac{\partial^2 \xi}{\partial x^2} = \frac{2m_s}{\hbar^2} \{\varphi_F + \varphi_B - E.x - \phi\} + k_y^2$$
$$= \frac{2m_s}{\hbar^2} \{\varphi_F + \varphi_B - \phi_{\text{tun}} - E.x\}. \tag{9}$$

This is an equation of the shape: $\frac{1}{\xi} \frac{\partial^2 \xi}{\partial x^2} = A - Bx$ with $A = \frac{2m_s(\varphi_F + \varphi_B - \phi_{Ium})}{\Lambda^2}$ and $B = \frac{2m_s E}{\hbar^2}$.

Although there is no general analytical solution, we can try a function like

$$\xi = \exp\left\{-\int_{0}^{x} (A - Bx')dx'\right\}.$$
(10)

Then,

$$\frac{1}{\xi}\frac{\partial^2 \xi}{\partial x^2} = A - Bx + \frac{B}{2\sqrt{A - Bx}}.$$
(11)

This is a Schrödinger equation of a barrier that is deviating from a triangular shape by the last term on the right hand side. If we normalize Eq. (11), we get

$$\frac{1}{A - Bx} \frac{1}{\xi} \frac{\partial^2 \xi}{\partial x^2} = 1 + \frac{B}{2(A - Bx)^{1.5}}$$
$$= 1 + \frac{E}{2\sqrt{\frac{2m_x}{\hbar^2}}} \{\varphi_F + \varphi_B - \phi_{tun} - E.x\}^{1.5}$$
$$= g(x, par).$$
(12)

The right hand side deviates most from 1 when $\varphi_F + \varphi_B - \phi_{tun}$ is minimal and Ex is high. From Fig. 3, it is clear that $Ex = \varphi_F + \varphi_B - \varphi_t - \phi \le \varphi_F + \varphi_B - \varphi_t - \phi_{tun} \Rightarrow \varphi_F + \varphi_F$

 $\varphi_B - \phi_{tun} - Ex \ge \varphi_t$. So the right hand side of Eq. (12) is always finite and the largest deviation from a triangular barrier occurs for low values of φ_t . Let us assume that φ_t $\ge 0.1 \text{ eV}$. The electric field is at maximum 3 V over 20 nm before complete depletion of the 2-DEG takes place and the vertical field cannot be further increased, so E_{max} $\approx 1.5 \times 10^8 \text{ V/m}$. In Fig. 4, we have plotted the right hand side of Eq. (12) g(x, par) as function of depth for several values of $par = \varphi_F + \varphi_B - \phi_{tun}$: 0.1, 0.2, 0.5, 1.0, and 3.0 eV, with $E = E_{max}$. We can make the following observations.

The curves tend to infinity when $E_{max}x$ approaches $\varphi_F + \varphi_B - \phi_{nun}$. As said, this will never occur. The curve for the case of 0.1 eV rises already considerably above 1 for low values of *x*, so in this case Eq. (10) is not a good wave function to use.

For values of $\varphi_F + \varphi_B - \phi_{tun} \ge 0.2 \text{ eV}$, the deviation of Eq. (12) from unity at low values of *x* is minimal. Therefore, we can conclude that Eq. (10) is a good approximation for the wave function for traps deeper than 0.2 eV below the conduction band. This solution can also be used for slightly shallower traps without significant error, especially at lower values of *E*.

This leads to a modified version of Eq. (8) for the triangular barrier

$$\wp_{1} = \frac{4\hbar m_{m}}{m_{s}^{2}} \frac{\alpha}{\alpha^{2} + k_{0}^{2}} \\ \times \int_{0}^{k_{0}} \exp\left\{-2\int_{0}^{W} \sqrt{\frac{2m_{s}}{\hbar^{2}}\{\varphi_{F} + \varphi_{B} - \phi_{tun} - Ex\}}dx\right\} k_{x}k_{//}dk_{//}$$
(13)

If we replace k_x by $k_x = \sqrt{k_0^2 - k_{//}^2}$ and ϕ_{tun} by $\phi_{tun} = \phi - \frac{m_m}{m_s} \phi_{//} = \phi - \frac{\hbar^2 k_{//}^2}{2m_s}$, we can integrate Eq. (13) over $k_{//}$ and calculate \wp_1 .

An equation for the de-trapping probability can be derived analogous to the derivation of \wp_1 . Referring to Fig. 3 at reverse bias voltage V_r , the electron Fermi level in the semiconductor is V_r below φ_F . We can calculate the probability \wp_2 of a trap located at W for capturing an electron from



FIG. 4. Deviation of triangular barrier as function of depth for different trap energy levels φ_i .

the semiconductor that has the right energy, i.e., an energy $\varphi_B - \varphi_t - EW + V_r$ above the conduction band minimum in the semiconductor. Using detailed balance considerations, this probability equals the de-trapping probability of an electron from the trap into the semiconductor. Assuming that the conduction band minimum is very close to the Fermi level, this means

$$k_0^2 = \frac{2m_s(\varphi_B - \varphi_t - EW + V_r)}{\hbar^2}$$

We can calculate \wp_2 simply by using Eq. (13), taking φ_t as barrier height and replacing W by φ_t/E . This gives

$$\wp_{2} = \int_{0}^{x_{0}} p dk_{//} = \frac{4\hbar}{m_{s}} \frac{\alpha}{\alpha^{2} + k_{0}^{2}}$$

$$\times \int_{0}^{k_{0}} \exp\left\{-2 \int_{0}^{\varphi_{t}/E} \sqrt{\frac{2m_{s}}{\hbar^{2}} \{\varphi_{t} - Ex\} + k_{//}^{2}} dx\right\} k_{x} k_{//} dk_{//}.$$
(14)

The term with $k_{//}$ in the exponent arises because there are multiple orbits available for de-trapping into the semiconductor with different $k_{//}$ levels. The argument is completely analog to the reverse case in which free electrons from the semiconductor are trapped into the barrier.

The next step is to calculate the gate current from \wp_1 and \wp_2 . Suppose at a certain depth *W*, there is a trap concentration N_t . Suppose further that a fraction *n* of these traps are occupied by electrons and therefore do not trap more electrons. The concentration of empty traps that are capable of capturing electrons is $N_t - n$. The capture rate of the traps is given by $f_{FDm}\wp_1(N_t \cdot n)$, while the de-trapping rate is $\wp_2 n$. In equilibrium, these rates must be equal, so

$$\frac{\wp_2}{f_{FDm}\wp_1} = \frac{N_t - n}{n}$$

The capture rate therefore is: $R = \frac{N_d f_{FDm} \wp_1 \wp_2}{f_{FDm} \wp_1 + \wp_2} = N_t$ $(\frac{1}{f_{FDm} \wp_1} + \frac{1}{\wp_2})^{-1}$.

The current contribution of these traps is $\Delta J_{TAT} = qR$. The total TAT current is the integral of ΔJ_{TAT} over all traps, so over all values of W running from zero for energies equal to $\varphi_F + \varphi_B - \varphi_t$ to very large values for very low energies. In practice, energies below the Fermi level will hardly contribute anymore. So we can write for the total TAT current

$$J_{TAT} = q N_t \int_0^\infty \left(\frac{1}{f_{FDm} \wp_1} + \frac{1}{\wp_2} \right) \quad dW.$$
(15)

The only variables depending on *W* in this expression are \wp_1 and f_{FDm} . Integration has to be done numerically after substituting Eqs. (13) and (14) into Eq. (15).

The expression (15) describes the current contribution caused by one trap level with an energy φ_t below the conduction band that is uniformly distributed throughout the semiconductor barrier. If there are several trap levels available, each of them will contribute according to Eq. (15) and all contributions have to be summed.

IV. MEASUREMENTS

Measurements were done of the gate to source current in a Schottky diode with a square gate area minimizing the influence of the gate edge. Voltages are applied between the negative threshold voltage and zero volt. In that case, the electric field at the gate edge is low because the 2-DEG is only (partly) depleted underneath the gate and not next to the gate. The current flow as well as the electric field can essentially be treated as one-dimensional and be compared to the models developed above. The diodes were processed by United Monolithic Semiconductors (UMS: www.ums-gaas.com) in Germany.¹⁷ The process technology has been described before, including TEM pictures of the layer structure.¹⁸ The HEMT structure is grown on SI-SiC substrate by MOVPE. Device isolation was done by ion implantation. The ohmic contact was made by evaporating Ti/Al/Ni/Au, which was annealed at 870 °C by RTA. A SiN layer was deposited on the AlGaN and a gate opening was etched in the SiN layer using a CF₄ based ICP process. After that a NiPtAu layer was deposited as Schottky contact and annealed at 400 °C. The resulting sheet resistance of the 2-DEG was 550 Ω / square. We used the following material parameter settings: ε_r = 9.5 for the AlGaN, $m_m = 0.1m_0$, $m_s = 0.23m_0$ with m_0 the vacuum electron mass.¹⁹ Since the calculated results are not very sensitive to the metal Fermi level, we take $\varphi_F = 5 \,\mathrm{eV}$ more or less arbitrarily, which is a suitable value for most gate metals. The parameters describing the diode structure are $d_{eff} = 25 \text{ nm}$ as effective barrier thickness, $A = 10^{-8} \text{ m}^2$ as gate area, and $\Delta V = 0.4 \text{ V}$ as the sum of the conduction band offset between AlGaN and GaN and the GaN flatband voltage, see Fig. 5. The factor ΔV contributes to the total electric field across the barrier. From Fig. 5, the electric field in the barrier is

$$E = \frac{V_r + \varphi_B - \varphi_{flatband} - \Delta \varphi_C}{d_{eff}} \text{ and } \Delta V = \varphi_{flatband} + \Delta \varphi_C.$$

In practice, ΔV is used as a fitting parameter that shifts the *I-V* characteristics along the voltage axis.



FIG. 5. The influence of the flatband voltage $\varphi_{flat \ band}$ and conduction band offset $\Delta \varphi_C$ between AlGaN and GaN on the total electric field in the barrier. φ_{fn} is the electron Fermi level in the semiconductor.

A comparison is made between the various theoretical models and the measured data in the following sections.

A. The trap assisted tunneling model

This model assumes that each type of trap is distributed uniformly throughout the AlGaN region and can be characterized by only two parameters: the concentration N_t and the energy level φ_t . Each trap type will contribute a current J_{TAT} according to Eq. (15). Every trap contribution is added to the direct current through the barrier given by the sum of Eqs. (5) and (6). Having many traps and their contributions to the total current raises the possibility that the model can become Ptolemaeic, i.e., that we introduce so many traps and associated fitting parameters, that we can fit any data without obtaining a model that makes physically sense. Therefore, we limit the number of traps in the model to two, which gives us four extra fitting parameters, next to the ones we already have for the direct current. Fig. 6 shows the measured and calculated data. Note that the threshold voltage of the device is -2.5 V. At lower voltages, the vertical field underneath the gate cannot increase any further because the 2-DEG is completely depleted. The measured data therefore become independent of voltage below the threshold voltage and the theories developed in this paper lose their validity. At higher temperatures, the direct current dominates. This allows us to accurately extract the barrier height from the data at 175 °C. This results in $\varphi_B = 0.97$ eV. The trap parameters are optimized to give the best fit to the data at 25 °C. One trap is at an energy level $\varphi_{tl} = 0.58 \text{ eV}$ and at a concentration of $N_{t1} = 6 \times 10^{14} \,\mathrm{cm}^{-3}$ and the other trap has φ_{t2} = 0.35 eV at a concentration of $N_{t2} = 1.5 \times 10^{15} \text{ cm}^{-3}$.

As can be seen in Fig. 6, the traps add very distinct humps in the current curves, especially at lower temperatures where TAT dominates the total current. However, the measured data at 25 °C do not show these typical trap signatures, suggesting that TAT does not occur in our samples. Of course, it is possible to add enough trap types in the theory, each with its own energy level and concentration, to obtain also a smooth *I-V* curve at 25 °C, but it is very unlikely that such a model would represent reality.

 10^{-5}

 10^{-8}

 10^{-9}

10-10

10-11

75C

25C

-3.5

 $I_r[A]$



-3

-2.5

 $V_r[V]$

-2 -1.5 -1

-0.5

0



FIG. 7. Comparison between measurements and calculations for the case of an ideal barrier with a height of 0.95 eV.

B. The ideal barrier

In this model, we only use the direct current through the defect-free barrier given by the sum of Eqs. (5) and (6). We treat the barrier height φ_B as a fitting parameter that we fit to the high temperature measurements at 175 °C, which results in a barrier height value of 0.95 eV. The comparison between measured and calculated values is shown in Fig. 7.

From Fig. 7, we conclude that, although the high temperature data can be reproduced, the model clearly fails at the lower temperatures. Therefore, we will evaluate the patch models as described in Sec. II.

C. The constant field patch model

In Fig. 8, the calculations are compared with the measured data for a patch area that is 2×10^{-4} times the total area with an effective patch barrier height of 0.58 eV (see Sec. II C 1). Note that the calculated current includes the thermionic emission current. This gives a good fit with the measured data, indicating that indeed this patch model may explain the data.

D. The zero field patch model

Calculations show that if $\varphi_{B,patch}$ is low, the current is dominated by thermionic emission and does not depend on voltage (see Sec. II C 2). If, on the other hand, $\varphi_{B,patch}$ is taken high enough such that thermionic emission does not



FIG. 8. Measured data and calculations using a defect patch model with triangular barrier, i.e., constant electric field throughout the patch.



FIG. 9. Measured data and calculations using zero potential defect patch model.

dominate, the total current is far too low to account for the observations. Therefore, the zero field patch model cannot explain the data.

E. The zero potential patch model

We can calculate the current by assuming an ideal triangular barrier with reduced barrier height and reduced thickness but with an electric field equal to the electric field in the surrounding area (see Sec. II C 3). We can obtain a perfect fit at the highest temperature by setting the patch area to the total area ratio at 10^{-5} and the patch barrier height to 0.60 eV. The calculations, however, deviate dramatically from the data at lower temperature (see Fig. 9). It is clear that the zero potential patch model also cannot explain the measured data.

V. CONCLUSIONS

We have developed theoretical models for currents in triangular and square Schottky barriers caused by tunneling, thermionic field emission, thermionic emission, and trap assisted tunneling. The models are compared with measurements on a large area Schottky diode on an AlGaN-GaN structure as used in high electron mobility transistors. Comparison was done over a voltage range between the negative threshold voltage and zero volt and over a temperature range from 25 °C to 175 °C. The measured data can be understood best by assuming that the current flows through only a very small fraction of the diode area, possibly through defect patches caused by epitaxial growth or gate processing. These defect patches have a low Schottky barrier height. Best agreement with measurements was obtained assuming that the defect area is only a fraction of 2×10^{-4} of the total diode area and the defect barrier height is 0.58 eV in the samples. A constant electric field in the defect patches is assumed to obtain the best fit with the data. Future research is needed to identify the nature and cause of the defects in order to reduce the Schottky current.

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