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Semi-physical nonlinear circuit model with device/physical parameters for HEMTs

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A nonlinear circuit model (NCM) with physical parameters is proposed for direct simulation of the RF characteristics of GaN high-electron-mobility transistors (GaN HEMTs) on the basis of device structure. The physical equations are used for the construction of the model in order to connect strongly the model parameters with the device/physical parameters. Hyperbolic tangent functions are used as the model equations to ensure good model convergence and rapid simulation (short simulation time). The usefulness of these equations is confirmed by technology computer aided design (TCAD) simulation. The number of model parameters for the nonlinear components (I_{ds} , C_{gs} , C_{gd}) is reduced to 17 by using common physical parameters for modeling the drain current and capacitance. The accuracy of this model is verified by applying to GaN HEMTs. The modeled I–V and capacitance characteristics agree well with the measurement data over a wide voltage range. Furthermore, this model can be used for the accurate evaluation of S-parameters and large-signal RF characteristics.

Keywords: Transistor models, GaN HEMT, Simulation, Power amplifiers, Physical modeling, Microwave

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I. INTRODUCTION

High-power amplifiers (HPAs) that include GaN high-electron-mobility transistors (GaN HEMTs) have potential applications in radar systems, satellite communication systems, cellular base stations, etc. Efforts have been made to improve the performance of GaN field effect transistor (FETs) on the basis of the vast experience accumulated over the years [1-17]. While several high-power, high-efficiency HPAs have been reported in the past [1-5], there is considerable scope for improvement of the HPA performance; this is because of the immaturity of the existing GaN technologies. Therefore, concurrent development of device and circuit design methods is desired. Empirical nonlinear circuit models (ENCMs) are widely used in circuit design. ENCMs are fast and converge well and give an accurate fit for DC and RF characteristics [6-9]. However, such circuit models are not suitable for use in device design because there is no explicit relationship between the model parameters and the device/ physical parameters. Moreover, a relatively large number of parameters must be extracted. On the other hand, physical models involving device/physical parameter calculations are too slow to be used in circuit simulations; further, these models do not converge well during RF simulation because of the complicated and exhaustive calculation for some boundary regions [10-13]. Therefore, simple ENCMs constructed using device/physical parameters are preferred. These models may help improve the device performance by contributing to an

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increase in the FET efficiency in practical circuit applications and facilitating device performance optimization in specific applications.

It is a widely accepted fact that hyperbolic tangent (tanh) functions can be used for a satisfactory description of the I-V and capacitance characteristics of HEMT devices [15, 16]. Such tanh functions have been used to describe the I-V characteristics of GaN HEMTs as well [8, 9]. The large-signal (LS) HEMT model [7] is a highly accurate model whose convergence is sufficiently high for it to be used in circuit design simulation. However, the model parameters, as in the case of every empirical device model, are used for reproducing the shape of the I-V curves, gm, capacitance characteristics, etc., rather than for device design.

In this paper, we propose a new model based on ENCM equations designed using device/physical parameters. We modified the model to obtain a direct connection with the device/ physical parameters. The model parameters are similar to the parameters used in device design. Furthermore, the number of model parameters for the nonlinear components ($I_{dsr} C_{gsr} C_{gd}$) can be reduced to 17 because the same physical parameters are used to describe the I-V and capacitance characteristics. This model is extended to a GaN HEMT, and the model parameters are extracted; the modeled I-V and capacitance characteristics are in excellent agreement with the measurement results. Moreover, with this model, accurate determination of the S-parameters and LS RF characteristics is possible.

II. MODEL

A) Drain-current model

To construct a drain-current model, we started from the following physical equation:

$$I_{ds} = qnv_e \tag{1}$$

where I_{ds} , q, n, and v_e are the drain current, electron charge, electron concentration of the two-dimensional electron gas (2DEG) and electron velocity, respectively. Figure 1 shows a schematic cross section of an HEMT. The electrons with n and v_e travel from the source to the drain in the channel.

Next, we need to find a suitable function to model n and v_e . It is known that n is controlled mainly by the gate voltage (V_{gs}) , and the tanh function was successfully used to model $n = f(\tanh(V_{gs}))$ [15, 16]. In order to make it possible to adjust the model for various cases, the tanh function for n was expressed as

$$n = A\left(\frac{1 + \tanh B}{2}\right) \tag{2}$$

$$B = C_1(V_{gs} - D) + C_2(V_{gs} - D)^2 + C_3(V_{gs} - D)^3$$
(3)

where A, B, C_1 , C_2 , C_3 , and D are constant. C_2 and C_3 are polynomial coefficients related to the carrier density.

For v_e , we adopted the following equation [18], which depends only on the drain voltage (V_{ds}) , that is, $v_e = g(\tanh(V_{ds}))$, because the electron accelerates owing to the laterally electronic field between the gate and drain, as shown in Fig. 1:

$$v_e = E \tanh \frac{V_{ds}}{F} \tag{4}$$

where *E* and *F* are constants. We confirmed the effectiveness of equations (2)–(4) by technology computer aided design (TCAD) simulation, where we employed the 2D device simulator (Silvaco ATLAS [19]). Figures 2 and 3 show the curves of the tanh functions fitted to the TCAD simulation results. *A*, *C*₁, *C*₂, *C*₃, and *D* in equations (1) and (2) were 1.35e12 cm⁻², 0.4 V⁻¹, 0.0 V⁻², 0.02 V⁻³, and 0.8 V, respectively. The values of *E* and *F* in equation (4) are 2.52e7 cm/s and 3.5 V, respectively. For both *n* and v_e , the model using the tanh function agreed very well with the TCAD simulation results. Therefore, equations (2)–(4), which are designed on the basis of tanh functions, can be used to express the characteristics of *n* and v_e .

Finally, we represented the drain-current model by substituting equations (2)-(4) in equation (1) and assigning the



Fig. 1. Schematic cross section of HEMT model.



Fig. 2. Curve of tanh function fitted to TCAD simulation data for *n*.



Fig. 3. Curve of tanh function fitted to TCAD simulation data for v_e .

parameters A-F to the device parameters:

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$$I_{ds} = I_{\max}\left(\frac{1 + \tanh\psi}{2}\right) \tanh\frac{2V_{ds}}{V_{knee}} \tag{5}$$

$$\psi = P_1 \left(V_{gs} - V_p - \frac{1}{P_1} \right) + P_2 \left(V_{gs} - V_p - \frac{1}{P_1} \right)^2 + P_3 \left(V_{gs} - V_p - \frac{1}{P_1} \right)^3$$
(6)

$$P_1 = \frac{2g_{mpk}}{I_{\max}} \tag{7}$$

where I_{max} , V_{knee} , V_p , and g_{mpk} are the maximum drain current, knee voltage, pinch-off voltage, and maximum transconductance, respectively. P_2 and P_3 are polynomial coefficients related to the carrier density and will allow us to adjust the current shape according to the channel profile. P_1 consists of two device parameters (I_{max} , g_{mpk}). Four of the input parameters (I_{max} , V_{knee} , V_p , and g_{mpk}) except P_2 and P_3 can be used as the device parameters, which are normally used in the device design and are familiar to the device engineers.

B) Capacitance model

The gate–source capacitance (C_{gs}) model is constructed from the following physical-based equation:

$$C_{gs} = \frac{\partial Q_{gs}}{\partial V_{gs}} = q \frac{\partial n}{\partial V_{gs}} (L_g^m + \Delta L_g^m)$$
(8)

where Q_{gs} and L_g^m are the gate charge and metallurgical-gate length, respectively. ΔL_g^m is defined as the extension length of the depletion region, as shown in Fig. 1. Usually, Q_{gs} corresponds to the charge of the parallel plate with an effective gate length L_g^m . However, an extension of the depletion region from the gate edge to the drain should be considered, because a large V_{gd} was applied to the GaN HEMTs. We consider an extension of the depletion region, expanding L_g^m to $(L_g^m + \Delta L_g^m)$. We used the tanh function, which limits the maximum value to ΔL_g^m , represented by

$$\Delta L_{\sigma}^{m} = G \tanh\left(HV_{gd}\right) \tag{9}$$

where *G* and *H* are constants. ΔL_g^m depends only on V_{gd} , because the depletion region extends mainly laterally. The shape of the $\Delta L_g^m - V_{gd}$ curve obtained by the TCAD simulation can be fitted well by equation (9), as shown in Fig. 4. *G* and *H* are 1.05 μ m and 110 V, respectively. For *n* in equation (8), the same expression as that for n in the drain-current model in equation (5) can be used. This reduces the number of model parameters.

Finally, the model C_{gs} is represented by

$$C_{gs} = C_{gspi} + C_{gso} I_{\max} L_g \left(\frac{d\psi/dV_{gs}}{2\cosh^2 \psi} \right)$$
$$\times \left[1 + R_{Lg} \tanh\left(\alpha_{Lg} V_{ed}\right) \right] \tag{10}$$

where C_{gso} , R_{Lgo} , α_{Lgo} , and C_{gspi} are the gate–source capacitance, the ratio of $L_g^m + \Delta L_g^m$ to L_g^m , gate extension coefficient, and the parasitic capacitance between gate and source, respectively.

In the case of the gate–drain capacitance (C_{gd}) , the charge model (Q_{gs}) is not sufficient to produce a useful C_{gd} expression from the physics. Because the C_{gd} is smaller than one-tenth of



Fig. 4. Curve of the tanh function fitted to TCAD simulation data for ΔL_g^m .

 C_{gs} , and more accuracy model of the Q_{gs} is necessary. So we used the conventional expression of the inversely proportional function dependent only on V_{gd} [13] instead, which yields

$$C_{gd} = \frac{C_{gdo}}{\left(1 - \left(V_{gd}/V_{bi}\right)\right)^{MSCGD}} + C_{gdpi} \tag{11}$$

where V_{bi} , C_{gdpi} , and *MSCGD* are the built-in voltage, parasitic capacitance, and capacitance coefficient for C_{gd} , respectively. C_{gdo} is the gate-drain capacitance at $V_{gd} = 0$.

C) Relationship between model parameters and physical parameters

Figures 5(a) and 5(b) show the device structure for GaN HEMTs and the band diagram under the gate electrode. In Fig. 5(a), L_{sg} and L_{gd} are the gate–source distance and the gate–drain distance, respectively. σ , v_{s} , and μ are the polarization charge, saturation electron velocity, and electron mobility, respectively. R_c , R_{Lsg} , R_{Lg} , and R_{Lgd} are the contact resistance, source–gate resistance, gate resistance, and gate–drain resistance, respectively. In Fig. 5(b), Φ_b , d, and ΔE_C are the gate work function, AlGaN layer thickness, and conduction band difference between the AlGaN layer and the GaN channel. These physical parameters are useful for device design, particularly in terms of performance optimization.

This model can be connected easily to the physical parameters due to the explicitly defined expressions of the device parameters. Device parameters can be estimated from physical parameters by using relational equations. Equations (19) and (20) used in this model are simple and make it easy to understand the relation between device parameters and physical parameters. The equations can be also easily implemented in standard, default CAD LS models.



Fig. 5. (a) Device structure for GaN HEMTs and (b) band diagram under gate electrode.

The maximum drain current I_{max} is expressed with respect to σ and v_{s} , as follows [20]:

$$I_{max} = q\sigma v_s \tag{12}$$

The knee voltage V_{knee} is expressed with respect to I_{max} and the on-state resistance R_{on} , which indicates the slope of the drain current in the linear region:

$$V_{knee} = R_{on}I_{max} \tag{13}$$

where

$$R_{on} = R_{Lsg} + R_{Lg} + R_{Lgd} + 2R_c$$
$$= \frac{(L_{sg} + L_{gd} + L_g^m)}{q\sigma\mu} + 2R_c$$
(14)

In equation (14), the product $q\sigma\mu$ represents the electrical conductivity of 2DEG.

Accounting for the source resistance R_{s} , the maximum transconductance g_{mpk} is expressed as [20]

$$g_{mpk} = \frac{g_{mi}}{1 + g_{mi}R_s} \tag{15}$$

where g_{mi} is the intrinsic transconductance without parasitic elements. g_{mi} is [20] given by

$$g_{mi} = \frac{\nu_s \varepsilon_G \varepsilon_0}{d} \tag{16}$$

where ε_G and ε_o are the GaN permittivity and vacuum permittivity, respectively. Similar to equation (14), R_s in equation (15) is expressed as

$$R_s = \frac{(L_{sg} + (1/2)L_g^m)}{q\sigma\mu} \tag{17}$$

The pinch-off voltage, V_p , is defined based on [21], as shown in Fig. 5(b):

$$V_p = \phi_b - \Delta E_c - \frac{q\sigma}{\varepsilon_{AG}\varepsilon_0}d\tag{18}$$

In this way, it is easy to relate the model parameters to the physical parameters in this model. The device parameters can be obtained by substituting the values of the physical parameters in equations (12)-(18). In addition, the drain current can be calculated by using the device parameters and equations (5)-(7). In other words, this model can calculate directly the *I*-*V* characteristics from the device structure.

Figure 6 shows the I-V characteristics of this model and the TCAD simulation. In this model, the self-heating effect was accounted for by substituting the following equations in equation (5):

$$I_{max} = I_{maxo}(1 - T_{cIpk}\Delta T_{chan})$$
(19)

$$\Delta T_{chan} = (T_{amb} - 25) + R_{therm}(I_{ds}V_{ds})$$
(20)

 I_{maxo} is the maximum drain current in the absence of the selfheating effect. T_{clpk} is the temperature coefficient of the drain current. ΔT_{chan} , T_{amb} , and R_{therm} are the channel temperature, ambient temperature, and thermal resistance, respectively. In the TCAD simulation, Poisson's equation, the current continuity equation, and the heat-transfer equation were solved simultaneously for self-heating effect. The physical parameters used in this simulation are summarized in Table 1. The simulation conditions are $V_{gs} = -5$ to 2 V (1 V steps) and $V_{ds} =$ o-30 V (1 V steps). The models agreed well with the TCAD simulation. This result shows that this model accurately predicts the *I-V* characteristics from physical parameters.

III. EXPERIMENTAL VERIFICATION

Figure 7 shows an equivalent circuit for GaN HEMT that implements the drain-current model and the capacitance model. The equivalent circuit consists of linear elements and nonlinear elements. The linear elements and the nonlinear elements represent a parasitic part and an intrinsic part in a transistor, respectively. The nonlinear elements are divided into three components which are the drain current (I_{ds}), nonlinear capacitances (C_{gs} , C_{gd} , C_{ds}), and a diode (D_{gs} , D_{gd}). The linear circuits are made up of parasitic inductances (L_g , L_d , L_s), parasitic capacitances (C_{pg} , C_{pd}), and parasitic resistances (R_g , R_d , R_s). This model accounts for the self-heating effect and the charge-trapping effect. A simple RC network is employed as a



Fig. 6. Simulated and modeled I-V characteristics of GaN HEMT: (a) $I_{ds}-V_{ds}$ and (b) $I_{ds}-V_{gs}$ curves.

No.	Name	Description	Value	Unit
1	Al_x	Al content of AlGaN layer	0.2	
2	d	AlGaN layer thickness	27	nm
3	V_{bi}	Built-in voltage	1.3	V
4	σ	Polarization charge	6.6e12	cm ⁻²
5	$V_{\rm s}$	Saturation electron velocity	1e7	cm/s
6	L_{SG}	Gate-source distance	1	μm
7	L_{GD}	Gate-drain distance	2	μm
8	$L^m g$	Gate length	1	μm
9	R_c	Contact resistance	2.3	$\Omega \ \mathrm{mm}$
10	μ	Electron mobility	1400	cm ² /V/s
11	κ_{GaN}	Thermal conductivity of GaN at 300 K	1.25	W/cm/K
12	$\kappa_{\rm SiC}$	Thermal conductivity of SiC at 300 K	4.9	W/cm/K

Table 1. Physical parameters.



Fig. 7. Equivalent circuit of HEMT.

thermal circuit. We use a series RC network to account for the charge-trapping effect [8]. The expressions for I_{ds} , C_{gs} , and C_{gd} in equations (5)–(7), (10), and (11) are applied to the draincurrent model, gate–source capacitance, and gate–drain capacitance.

The model was verified by using a GaN HEMT including an ion implantation doping of Si under a source/drain electrode for low ohmic contacts [14]. The epitaxial layers consisted of AlGaN/GaN layers grown on semi-insulating SiC substrates.

The parameter-extraction sequence of our model is simple and straightforward. For I_{ds} parameters (I_{max} , V_{knee} , V_p , g_{mpk}), the initial values were extracted from the measurement of I-V characteristics. The values can also be calculated from equations (12)-(18). The other parameters were set to zero. Next, the parameters were optimized by a CAD tool.

Parameters C_{gs} and C_{gd} are extracted from multibias S-parameter data. Firstly, intrinsic S-parameters were obtained by de-embedding parasitic elements. Parasitic elements were extracted by means of the cold FET method [17]. Next, converting the S-parameters to Y-parameters, C_{gs} and C_{gd} were obtained [22]. Finally, the parameters were optimized in order to minimize the error between the measurement and model data. In the optimization, the parameters related to the carrier density were the same as the values used in parameter I_{ds} . We can also use the values estimated



Fig. 8. Measured and modeled I-V characteristics of GaN HEMT: (a) I_{ds} - V_{ds} and (b) I_{ds} - V_{gs} curves.



Fig. 9. Measured and modeled capacitance characteristics of GaN HEMT: (a) $C_{gs}-V_{ds}$ and (b) $C_{gd}-V_{ds}$ curves.



Fig. 10. Measured and modeled multibias S-parameter: (a) S11, (b) S22, (c) S12, and (d) S21 of the 0.5-mm GaN HEMT for 1-10 GHz at $V_{gs} = -2-1$ V (1 V steps) and $V_{ds} = 20$ V.



Fig. 11. Measured (symbols) and modeled (solid lines) (a) output power, (b) gain, (c) power-added efficiency, and (d) drain current of the 0.5-mm GaN HEMT for 9.6 GHz at $V_{dsq} = 30$ V and $I_{dsq} = 50$ mA.

Table 2. Model parameters for nonlinear components.

No.	Name	Description	Value	Unit
1	I _{max}	Maximum drain current	1.1	А
2	V_{knee}	Knee voltage	6.8	V
3	G_{mpk}	Maximum transconductance	0.34	S
4	V_p	Pinch-off voltage	-0.91	V
5	P_2	Polynominal coefficient for carrier density	-0.035	$1/\mathrm{V}^2$
6	P_3	Polynominal coefficient for carrier density	0.093	$1/V^3$
7	R _{therm}	Thermal resistance	10	°C/W
8	T_{clpk}	Temperature coefficient of drain current	-0.003	
9	C_{gso}	Gate source capacitance coefficient	5.38	pF
10	C_{gspi}	Parasitic gate source capacitance	0.94	pF
11	$L^m g$	Metallurgical gate length	1	μm
12	R_{Lg}	Ratio of effective gate length to $L^m g$	0.41	
13	α_{Lg}	Coefficient for gate extension	0.07	
14	Cgdo	Gate drain capacitance coefficient	1.54	pF
15	C_{gdpi}	Parasitic gate drain capacitance	1e – 9	pF
16	V_{bi}	Built in voltage	1.3	V
17	MSCGD	Capacitance coefficient for C_{gd}	0.68	

from the physical parameters as the initial values for the optimization.

The models agreed very well with the measurement data. Figure 8 shows (a) $I_{ds}-V_{ds}$ and (b) $I_{ds}-V_{gs}$ curves. The measured bias conditions were over V_{gs} from -2 to +2 V (0.2 V steps) and V_{ds} from 0 to 30 V (2 V step). The agreement was obtained over a wide range of V_{ds} from 0 to 30 V.

Figure 9 shows (a) $C_{gs}-V_{ds}$ and (b) $C_{gd}-V_{ds}$ curves. The measured bias conditions were over V_{gs} from -2 to 0 V (0.2 V step) and V_{ds} from 0 to 30 V (2 V steps). The model reproduced the tendency of the measurements. The model parameters C_{gs} were equivalent to the model parameters I_{ds} related to carrier density n.

Figure 10 shows the measured and modeled multibias S-parameter. The solid line and the dotted line represent the model simulation and the measurement, respectively. Bias conditions were over V_{gs} from -2 to 1 V (1 V steps) at $V_{ds} = 20 \text{ V}$. The frequency range is from 1 to 10 GHz. The agreement was obtained over a wide range of frequencies and bias values. The model accurately simulated small-signal characteristics.

Figure 11 shows the measured and modeled LS RF characteristics of a GaN HEMT with a gate width of 0.5 mm at 9.6 GHz. The measured bias conditions were at $V_{dsq} = 30$ V and $I_{dsq} = 100$ mA/mm. The optimum input and output impedances at the fundamental frequency *f*0 and at 2*f*0 were determined based on a load-pull measurement. The model accurately predicted LS RF characteristics. The model parameters for nonlinear components are summarized in Table 2. This model can describe I_{ds} , C_{gs} , and C_{gd} based on 17 parameters. R_{therm} and T_{cIpk} are the thermal resistance and temperature coefficient of the drain current, respectively. A self-heating effect was included by decreasing I_{ds} in the high-current and high-voltage region by these parameters.

IV. CONCLUSION

We proposed a semi-physical nonlinear circuit model with device/physical parameters. The model equations were

derived from the physical ones. A hyperbolic tangent function was used as the model equation to reproduce the electron concentration and electron velocity calculated by TCAD simulation. The I-V characteristics in the TCAD simulation could be predicted from the physical parameters used in this model.

In the model, the same physical parameters are used to model the I-V and capacitance characteristics, which are related to the electron concentration. Therefore, the model can deal directly with device/physical parameters, and it can be expressed by a very small number of model parameters (17) for nonlinear components (I_{ds} , C_{gs} , C_{gd}). This model has been easily implemented in LS CAD models, by inserting the physical-based equations into equivalent circuit models.

In the verification, the LS CAD model parameters were extracted for GaN HEMT. The I-V and capacitance characteristics estimated by the model agreed well with the measurement data. Moreover, this model could be used for the accurate estimation of S-parameters and LS RF characteristics. This model is expected to be a powerful tool for improving the performance of GaN HEMTs because of its usefulness in device and circuit design. In device design, the HEMT characteristics can be estimated on the basis of the physical parameters and device structure.

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REFERENCES

- Ohtsuka, H. et al.: Over 57% efficiency C-band GaN HEMT high power amplifier with internal harmonic manipulation circuits, in 2008 IEEE MTT-S Int. Microwave Symp. Dig. WE1E-03, June 2008.
- [2] Mishra, U.K.: GaN-Based RF power devices and amplifiers. Proc. IEEE, 96 (2) (2008), 287-305.
- [3] Okamoto, Y.; Wakejima, A.; Ando, Y.; Nakayama, T.; Matsunaga, K.; Miyamoto, H.: 100W C-band single-chip GaN FET power amplifier. Electron. Lett., 42 (5) (2006), 283–285.
- [4] Shigematsu, H. et al.: C-band-340-W and X-band 100-W GaN power amplifiers with over 50% PAE, in 2009 IEEE MTT-S Int. Microwave Symp. Dig. TH₃A-1, June 2009.
- [5] Takagi, K. et al.: Ku-band, AlGaN/GaN-HEMT with over 30% of PAE, in 2009 IEEE MTT-S Int. Microwave Symp. Dig. WE1C-4, June 2009.
- [6] Curtice, W.R.; Ettenberg, M.: A nonlinear GaAs FET model for use in the design of output circuits for power amplifiers. IEEE Trans. Microw. Theory Tech., MTT-33 (12) (1985), 1383–1394.
- [7] Angelov, I.; Bengtsson, L.; Garcia, M.: Extensions of the Chalmers nonlinear HEMT and MESFET model. IEEE Trans. Microw. Theory Tech., 44 (10) (1996), 1664–1674.
- [8] Angelov, I. et al.: Large-signal modeling and comparison of AlGaN/ GaN HEMTs and SiC MESFETs, in Proc. of Asia-Pacific Microwave Conf. (APMC) 2006, WE4B-1, December 2006.
- [9] Deng, J. et al.: Temperature-dependent RF large-signal model of GaN-based MOSFETs. IEEE Trans. Microw. Theory Tech., MTT-56 (12) (2008), 2709–2716.

- [10] Pedro, J.C.: A physics-based MESFET empirical model, in 1994 IEEE MTT-S Int. Microwave Symp. Dig. WE₃F-14, May 1994.
- [11] Li, M.; Wang, Y.: 2-D analytical model for current-voltage characteristics and transconductance of AlGaN/GaN MODFETs. IEEE Trans. Electron Devices, ED-55 (1) (2008), 261–267.
- [12] Koudymov, A. et al.: Analytical HFET I–V model in presence of current collapse. IEEE Trans. Electron Devices, ED-55 (3) (2008), 712–720.
- [13] Staudinger, J.; Golio, M.; Woodin, C.; de Baca, M.C.: Considerations for improving the accuracy of large-signal GaAs MESFET models to predict power amplifier circuit performance. IEEE J. Solid-state Circuit, SSC-29 (3) (1994), 366–373.
- [14] Suita, M.; Nanjo, T.; Oishi, T.; Abe, Y.; Tokuda, Y.: Ion implantation doping for AlGaN/GaN HEMTs. Phys. Stat. Sol. (c), 3 (6) (2006), 2364–2367.
- [15] Roblin, P.; Kang, S.C.; Morkoc, H.: Analytic solution of the velocitysaturated MOSFET/MODFET wave equation and its application to the prediction of the microwave characteristics of MODFETs. IEEE Trans. Electron Devices, ED-37 (7) (1990), 1608–1622.
- [16] Roblin, P.; Rohdin, H.; Hung, C.J.; Chiu, S-W.: Capacitance–voltage analysis and current modeling of pulse-doped MODFETs. IEEE Trans. Electron Devices, ED-36 (11) (1989), 2394–3404.
- [17] Golio, M.: Microwave MESFETs and HEMTs. Artech, (1991), 152–170.
- [18] Ishihara, N.; Shimizu, T.: A simple equation model for RF MOS FET, Silicon Monolithic Integrated Circuits in RF Systems, 2006, Digest of Papers, 2006 Topical Meeting, 18–20 January San Diego, CA, 2006, 93–96.
- [19] Device Simulator Atlas Ver. 5.16.3.R. Atlas User's Manual, Silvaco Int., Santa Clara, CA, 2010.
- [20] Chavarkar, P.; Mishra, U.: Field effect transistors: FETs and HEMTs. Thin Films, 28 (2001), 71–145.
- [21] Yin, H.: A physics-based large-signal analytical model for AlGaN/ GaN HFETs, Dissertation, North Carolina State University, 2008.
- [22] Dambrine, G.; Cappy, A.; Helidore, F.; Playes, E.: A new method of determining the FET small-signal equivalent circuit. IEEE Trans. Microw. Theory Tech., 36 (7) (1988), 1151–1159.



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