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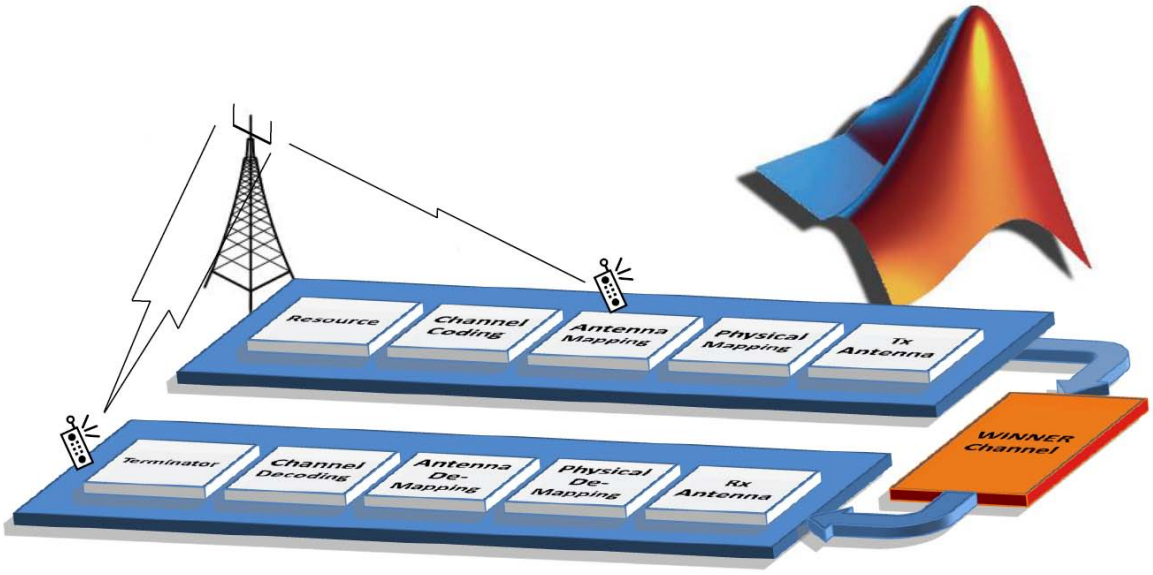

Simulink Based LTE System Simulator

Master of Science Thesis in Communication Engineering

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Department of Signals and Systems
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Cover:

A 3D graph showing the structure of the LTE dowlink simulator, according to Figure 4.1.

Göteborg, Sweden, 2010

Abstract

A MATLAB Simulink-based simulator for an antenna system has been developed at Chalmers, followed by the implementation of a WCDMA system in it. Naturally, 3GPP Long-Term Evolution (LTE) as a new standard for communication system should be considered next. Therefore, a Simulink-based LTE system simulator connected to the existing antenna blocks and WINNER channel is presented in this master thesis. It focuses on the implementation of the LTE downlink in the physical layer. In the transmitter, functionalities like Turbo coding, rate-matching, MIMO (up to 4×4) and OFDM are realized. In the receiver, ideal and non-ideal receiver algorithms are supported, including two types of channel estimation, linear equalizer, Turbo decoding, PMI calculation and HARQ functionality. The results, such as raw bit error rate, bit error rate, block error rate and user throughput are studied in the simulator for different system settings.

Keywords: Simulator, LTE, downlink, Simulink, WINNER, antenna, MIMO, Turbo coding, channel estimation, PMI

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We are very grateful to our supervisors Mats Viberg at Chalmers, Henrik Sahlin and Patrik Persson at Ericsson. We express our special thanks to Henrik Sahlin and Patrik Persson who always give us patient helps whenever we are in trouble. We also have learnt a great deal from them. Ulf Carlberg also provided precious experiences of using Simulink and Lars Pettersson gave us useful explanations of the WINNER channel: many thanks to them too.

Xuan Guo and Pengtao Song

Göteborg, Sweden, 2010

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List of Acronyms

3GPP	Third Generation Partnership Project
ACK	Acknowledgement (in ARQ protocols)
ARQ	Automatic Repeat-reQuest
AWGN	Additive White Gaussian Noise
BCH	Broadcast Channel
BER	Bit-Error Rate
BLER	Block-Error Rate
BPSK	Binary Phase-Shift Keying
CDD	Cyclic-Delay Diversity
CP	Cyclic Prefix
CQI	Channel Quality Indication
CRC	Cyclic Redundancy Check
CSI	Channel State Information
DCI	Downlink Control Information
DFT	Discrete Fourier Transform
DL	Downlink
eNodeB	E-UTRAN NodeB
FDD	Frequency Division Duplex
FFT	Fast Fourier Transform
FIR	Finite Impulse Response
FSTD	Frequency Shift Transmit Diversity
HARQ	Hybrid ARQ
IDFT	Inverse DFT
ISI	Inter-Symbol Interference
LMMSE	Linear Minimum Mean-Square Error
LTE	Long Term Evolution
MAC	Medium Access Control
MIMO	Multiple-Input Multiple-Output
MRC	Maximum Ratio Combining
OFDM	Orthogonal Frequency-Division Multiplexing
PCCC	Parallel Concatenated Convolutional Code
PDCCH	Physical downlink control channel
PDSCH	Physical downlink shared channel
PHY	Physical layer
PMI	Pre-coding Matrix Indicator
PRB	Physical Resource Block
QAM	Quadrature Amplitude Modulation
QPSK	Quadrature Phase-Shift Keying
RB	Resource Block
RI	Rank Indicator
RS	Reference Symbol
RV	Redundancy Version
RRT	Round Trip Time
SFBC	Space Frequency Block Coding
SINR	Signal-to-Interference plus Noise Ratio
SISO	Single-Input Single-Output
SNR	Signal-to-Noise Ratio
TBS	Transport Block Size
TTI	Transmission Time Interval

1 Introduction

1.1 Background

An antenna system simulator based on Simulink has been developed by the Chase and Charmant research centers at Chalmers, followed by the implementation of a WCDMA system as a standard in 3G. LTE (Long Term Evolution), as an important technique of 4G, is naturally the task of the next stage.

LTE can provide high data rates, low latency and flexible bandwidth. To achieve these targets, Orthogonal Frequency Division Multiplexing (OFDM) and Multiple-Input and Multiple-Output (MIMO) are adopted as basic technologies. Besides, other technologies like robust channel coding, scheduling, link adaptation and hybrid ARQ are also important.

1.2 Scope of the thesis

First of all, it is necessary to present the scope of the thesis, including the requirements and some constraints on the implementation of the LTE system.

- The implementation is based on the LTE Release 9 of the 3GPP specification.
- The platform is MATLAB Simulink 7.5.
- Only considered is the LTE downlink built between 1 base station (eNodeB) and 1 user equipment (UE).
- The focus is mainly on the physical layer and partly on the MAC layer.
- Only PDSCH (Physical Downlink Shared Channel) is considered and no control channels are considered.
- Only FDD (Frequency-Division Duplexing) is supported.
- The LTE system needs to be connected to the existing antenna blocks and the WINNER channel.

Thus, this thesis focuses on the implementation of the transmitter and the receiver in LTE downlink, which are also the key content of this report. As for the WINNER channel and the antenna blocks, no more details will be included in this report. See references [1-3].

1.3 Outline of the report

The report is organized as follows:

Chapter 2 gives a brief description of the MATLAB Simulink.

In Chapter 3, the components of the transmitter and the receiver of LTE downlink are discussed in details.

Chapter 4 describes how to set parameters and run the simulator.

The results for different system settings are presented in Chapter 5.

In the end, the work of the thesis is summarized and suggestions for future work are given in Chapter 6.

1.4 Others

Some notations are explained in this Section in order to make the report more readable. Italic letter (x) denotes a variable. Lowercase letter in bold type ($\mathbf{x}$) denotes a vector while uppercase letter in bold type ($\mathbf{X}$) denotes a matrix. The transpose of vector $\mathbf{x}$ (or matrix $\mathbf{X}$) is expressed as $\mathbf{x}^T$ (or $\mathbf{X}^T$) while the conjugate transpose is expressed as $\mathbf{x}^*/\mathbf{x}^H$ (or $\mathbf{X}^*/\mathbf{X}^H$).

2 Platform: MATLAB Simulink

The platform of the simulator is Simulink version 7.5, as one part of Release 2010a from the MathWorks. Simulink is well integrated with the MATLAB environment, which means Simulink has good access to many MATLAB features.

Simulink is a software tool to model, simulate and analyze dynamic systems including signal processing, communication systems and control systems [4]. A feature of Simulink is that it is model-based. For modeling, a graphical user interface (GUI) is provided to hierarchically build models as blocks, which can be selected from existing predefined blocks or created according to the user's wishes. To do a simulation, the Simulink menus or the command line in the MATLAB Command Window can be used. When analyzing, a large number of model analysis tools are available in both Simulink and MATLAB.

In Simulink, the functions are performed by the blocks and the data can be transmitted between the blocks by the connecting lines, which represent the signals. The implementation of the blocks is a key step in the realization of this LTE system simulator. Next we will focus on the usage of pre-defined blocks, as well as building user-defined blocks.

2.1 Pre-defined blocks

After typing '*simulink*' in the command line of MATLAB Command Window, we can find the blocks.

Simulink contains 16 standard block libraries, such as **Commonly Used Blocks**, **Math Operations**, **Ports & Subsystems**, **Sinks**, **Sources**, **User-Defined Functions**, etc. Except the library of **User-Defined Functions**, the libraries provide extensive blocks with different functions, which can be used directly.

Besides, Simulink also includes several useful blocksets like **Communications Blockset** and **Signal Processing Blockset**. Besides, Simulink can also use some MATLAB toolboxes like **Control System Toolbox.**

Mainly used in the simulator are the standard block libraries and the two blocksets. For the user's convenience, some main blocks are listed in Table 2.1, together with their positions in Simulink (not including antenna parts). For more details, see the Product Help of MATLAB.

Pre-defined blocks can be directly used, which is the advantage of Simulink. However, as the practical requirements are far more complicated, it is impossible for these relatively simple blocks to model everything. That is why the embedded MATLAB function is introduced in the next section.

Table 2.1 *Used Simulink blocks in the simulator.*

Blocks	Libraries
Switch	Commonly Used Blocks
For Each Subsystem	Ports & Subsystems
Selector	Signal Processing Blockset
Matrix Concatenate	
Delay	
Buffer	
Delay Line	
Frame Conversion	
General CRC Generator/syndrome detector	Communications Blockset
Interlacer/Deinterlacer	
Convolutional Encoder	
APP Decoder	
Rectangular QAM Modulator/Demodulator Baseband	
AWGN Channel	
Error Rate Calculation	

2.2 Embedded MATLAB function

There is an existing block named **Embedded MATLAB function** in the library of **User-Defined Functions.** Writing a MATLAB function in the block, you can create the model as you wish. In other words, embedded MATLAB function helps building custom models. Therefore, Embedded MATLAB function blocks are more flexible than the pre-defined blocks, despite the programming needed. In the simulator, more than half of the models are implemented using Embedded MATLAB function blocks, especially some key blocks like Precoding, Channel Estimation and Equalizer.

Nevertheless, an Embedded MATLAB function has limitations. Firstly, Embedded MATLAB is just a subset of the MATLAB language, which implies that it cannot support all the MATLAB language features. Table 2.2 shows several main supported and unsupported MATLAB language features in the Embedded MATLAB function (see more features in [5]). Secondly, for some MATLAB functions, there are also limitations. For example, the function *fft* / *ifft* in the Embedded MATLAB is only available when the length of the input vector is a power of 2. For the usage of *randi* , *error* , *plot* or *disp* , an extrinsic function declaration has to be made (see more limitations in Chapter 1 of [5]). Thirdly, before running a simulation, the Embedded MATLAB blocks need to generate efficient embeddable code via updating the Simulink diagram. This might take a significant amount of time for the first updating [6].

Table 2.2 *Supported and Unsupported MATLAB language features in the Embedded MATLAB.*

Supported features	N-dimensional arrays
	Matrix operations
	Variable-sized data
	MATLAB program control statements if, switch, for, and while
	Subfunctions
	Persistent variables
	Global variables
	Structures
Unsupported features	Cell arrays
	Objects
	Nested functions
	Sparse matrices
	Try/catch statements
	Recursion

2.3 Some Simulink tips

In this Section, we briefly talk about some main problems what we met during the work, as well as the solutions we used.

a. Errors from some undefined variables in the Embedded MATLAB function

When the size of an undefined variable in the Embedded MATLAB function changes dynamically, especially in the loops, there will be an error saying "*Undefined function or variable '(name)'. The first assignment to a local variable determines its class*". The solution is to initialize the variable to its full size in the beginning of the Embedded MATLAB function.

b. How to transmit a constant value from workspace to Embedded MATLAB function.

The constant value that has been saved as a variable at MATLAB workspace can be used in an Embedded MATLAB function. At first the variable should be added to the input argument list of the function. Afterwards open the **Tools** menu in an Embedded MATLAB function and select the **Edit Data/Ports**. Find the corresponding constant and set the **Scope** in the **General** menu as **Parameter**. To avoid errors, uncheck the **Tunable** box.

c. Call external MATLAB functions.

As mentioned in Section 2.2, Embedded MATLAB function does not support some MATLAB functions, like *randi* , *error* , *plot* , *disp* , etc. However, they can be used after an extrinsic function declaration at the top of the Embedded MATLAB function using *eml.extrinsic* .

d. Take care of buffer latency problems.

When using **Buffer** blocks, latency may be introduced. The problem of latency is annoying. Zero-tasking latency is only available in some special cases. For most cases, try to avoid this problem or solve it in the following way. Firstly, use the function *rebuffer_delay* to calculate the delay in order to make the schedule of buffer output clear. Then set a persistent variable to assist with the processing of output data in different time.

3 LTE downlink simulator

3.1 Basic structure

The LTE downlink simulator simulates the downlink communication from one E-UTRAN NodeB (eNodeB) to one User Equipment (UE) using a WINNER channel or an AWGN channel. There are four main parts of the simulator: transmitter (eNodeB), channel, receiver (UE) and finally the outputs are calculated in one part. Note that the Chase Antenna blocks which were developed at Chalmers are connected with the simulator.

The LTE downlink simulator is mainly modeled in the physical layer and partly in the Medium Access Control (MAC) layer. In terms of physical channel, the simulator focuses on the Physical Downlink Shared Channel (PDSCH).

Moreover, the simulator operates in terms of sub-frames, i.e. the generation and transmission of signals are in the form of sub-frames.

3.2 Transmitter

The transmitter in the physical layer starts with the grouped resource data which are in the form of transport blocks (see Figure 3.1). In each TTI, one transport block will be transferred first to the channel coding part which consists of two CRC encoders and one Turbo encoder. Then the block of the coded bits is named as a code block. The rate-matching block which cooperates with Hybrid ARQ is a kind of rate coordinator between the channel coding and the following blocks. In addition, since only one eNodeB is simulated, the scrambling process after rate-matching is neglected in the simulator.

As illustrated in Figure 3.1, there are two process lines before the layer mapping block. The two lines correspond to the parallel processing of two codewords[1] in the case of spatial multiplexing transmission. Otherwise only one process line works. The layer mapping and precoding combined with different transmit schemes are two key approaches to achieve the MIMO functionality in LTE. Both of them are contained in the antenna mapping block of the LTE simulator.

[1] A codeword is the block of the complex-valued modulation symbols. One codeword is defined per transport block.

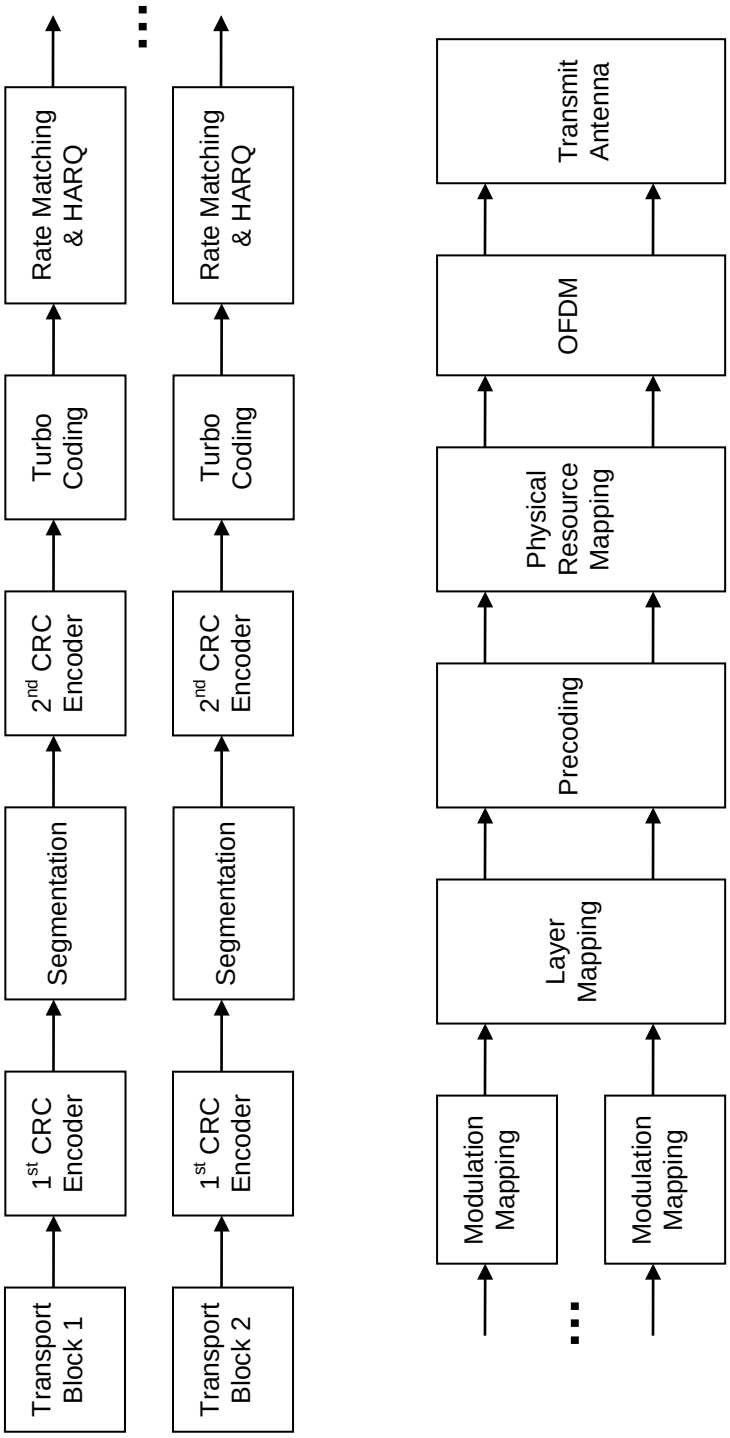


Figure 3.1 *The structure of the LTE simulator transmitter.*

The following physical resource-mapping block and OFDM block are defined at each antenna port. They mainly arrange the transmitted symbols into a certain sub-carrier and time. In the end, the data will be transmitted to the WINNER channel through the transmit antennas.

Moreover, as the transmitter is specified in the 3GPP LTE specifications, it has been mainly implemented as in the specification unless stated otherwise.

3.2.1 Resource

The transport channel is the interface between the physical layer and the MAC layer. As the LTE simulator focuses on the physical layer, the initial data is generated in the form of transport blocks.

In the simulator, the resource generation is combined with the HARQ, which implies there will be two cases of resource generations. One is the generation of bits for a new transport block. The other is the retransmission of bits for the previous transport block, directly extracted from the buffer '*pers_HARQprocessMat*' (see Section 3.4.7).

In the case of single-antenna transmission mode, there is only one transport block to be generated at each TTI. In the spatial multiplexing transmission mode, the simulator can run with up to two transport blocks at the same time. If there are two transport blocks, their transmissions or retransmissions are processed individually.

Another important thing is the transport block size (TBS). It is decided by the number of Physical Resource Blocks (PRB) and the Modulation and Coding Scheme (MCS) from the MAC layer scheduler. The value of TBS is calculated in the initialization of the simulator.

3.2.2 Channel coding

3.2.2.1 Cyclic Redundancy Check

According to [8], an encoder of Cyclic Redundancy Check (CRC) is utilized at the beginning of channel coding.

There are two CRC schemes for PDSCH: '*gCRC24A*' and '*gCRC24B*' (see Figure 3.2). Both of them possess a 24 parity bits length, but work with different cyclic generator polynomials. The '*gCRC24A*' focuses on a transport block, while the '*gCRC24B*' focuses on the code block, which is a segmentation of a transport block when the size of a transport block is larger than the upper limit (6144 bits).

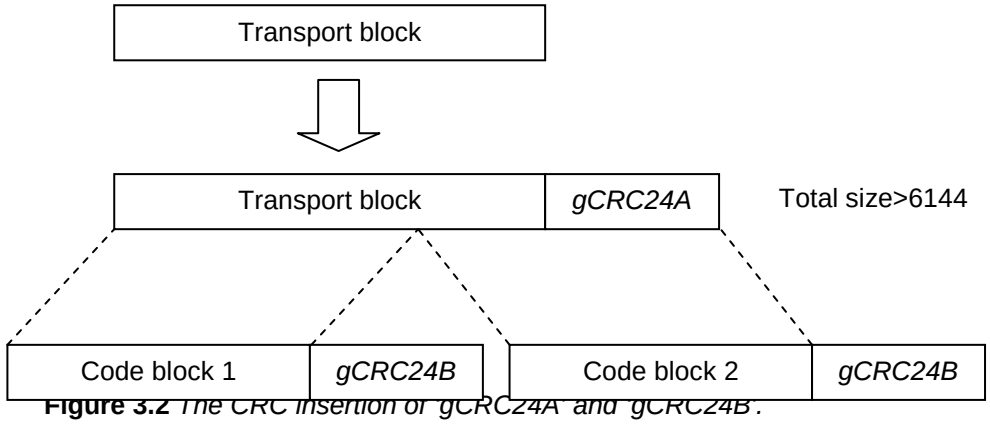


Figure 3.2 *The CRC insertion of 'gCRC24A' and 'gCRC24B'.*

For both schemes, the LTE simulator directly uses the **General CRC Generator** (see Figure 3.3) from the Simulink block library. The main parameters associated to this block are the '*generator polynomial'* and the '*checksums per frame'*.

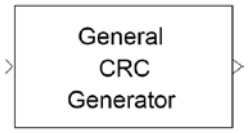


Figure 3.3 *The Simulink block of General CRC Generator.*

The '*generator polynomial'* is specified as an integer row vector, containing the powers of nonzero terms in the polynomial. According to Section 5.1.1 in [8], the '*gCRC24A*' and '*gCRC24B*' work with the row vectors A and B respectively, which are written as

$$A = [24, 23, 18, 17, 14, 11, 10, 7, 6, 5, 4, 3, 1, 0], \quad (3.1)$$

$$B = [24, 23, 6, 5, 1, 0]. \quad (3.2)$$

The default value of '*checksums per frame'* is set to '1' for both '*gCRC24A*' and '*gCRC24B*'. This is due to the fact that, in each TTI, the input units of '*gCRC24A*' and '*gCRC24B*' are defined as 1 transport block and 1 code block respectively.

Moreover, the encoding should be performed in a systematic form (that is, the original data bits appear at the front of the outputs). Such form is automatically generated by the General CRC Generator block.

In Simulink the '**For Each Subsystem**' block can achieve 'for loop' functionality. Thus it is applied to attach a '*gCRC24B*' to each code block within one TTI.

3.2.2.2 Turbo encoder

The channel coding scheme for PDSCH adopts Turbo coding, which is a kind of robust channel coding. When using an AWGN channel, the performance of Turbo codes can be close to the theoretical Shannon capacity limits.

According to Section 5.1.3.2 of [8], the scheme of the Turbo encoder is a Parallel Concatenated Convolutional Code (PCCC) with two 8-state constituent encoders and one Turbo code internal interleaver. The theoretical structure of a Turbo encoder is shown in Figure 3.4.

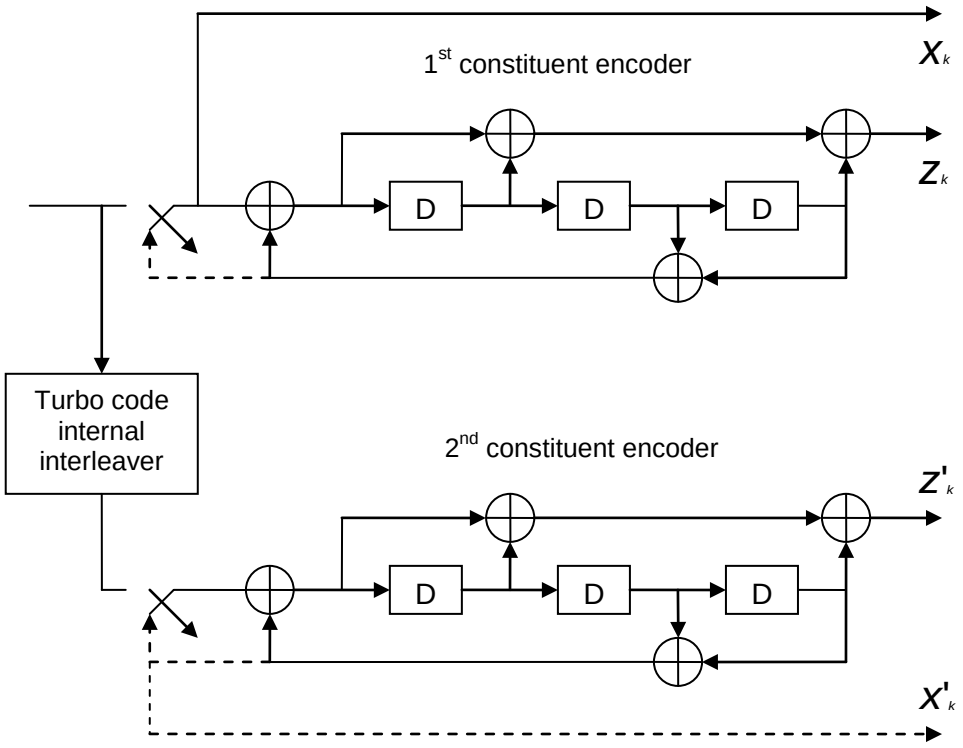


Figure 3.4 *The structure of the Turbo encoder (dotted lines apply for trellis termination only).*

The output from the Turbo encoder consists of three information bit streams with the same length K:

- Systematic bit stream; $(x_k,\ k = 0,1,...,K-1)$
- Parity bit stream; $(z_k,\ k = 0,1,...,K-1)$
- Interleaved parity bit stream. $(z'_k, k = 0,1,...,K-1)$

Since the receiver also has the knowledge of the Turbo internal interleave sequence, the interleaved systematic bit stream X'_k will not be transmitted to the receiver. But the data of X'_k will be partly utilized at the step of trellis termination.

The tail bits are independently appended at the end of each information bit stream to clean up the memory of all registers, i.e. terminate the encoder trellis to a zero state. Generally, the length of the tail bits is equal to the number of registers in one constituent encoder (3 registers are used in one convolutional encoder in LTE). According to Section 5.1.3.2.2 of [8], the sequence of tail bits is rearranged, and 4 tail bits are attached after each information bit stream. Hence, the length of each bit stream becomes $K+4$.

With the three information bit streams, the original Turbo coding rate is 1/3. However, after padding tail bits, the coding rate will decrease a bit. Furthermore, by puncturing or repeating the output of Turbo coding, it can accomplish an alterable channel coding rate under different scenarios. Such process is implemented by the circular buffer at the rate-matching block, and it will be introduced in Section 3.2.3.

In the LTE simulator, the implementation of the Turbo coding can be seen in Figure 3.5. The **Convolutional Encoder Block** (from the Simulink block library) is the main component of Turbo encoder which accepts a polynomial description of a convolutional encoder.

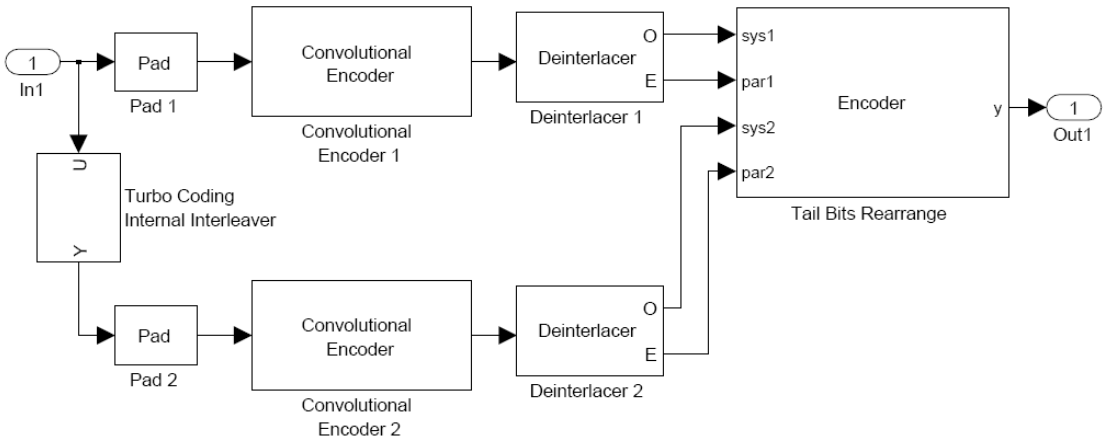


Figure 3.5 *The structure of Turbo coding in the LTE simulator.*

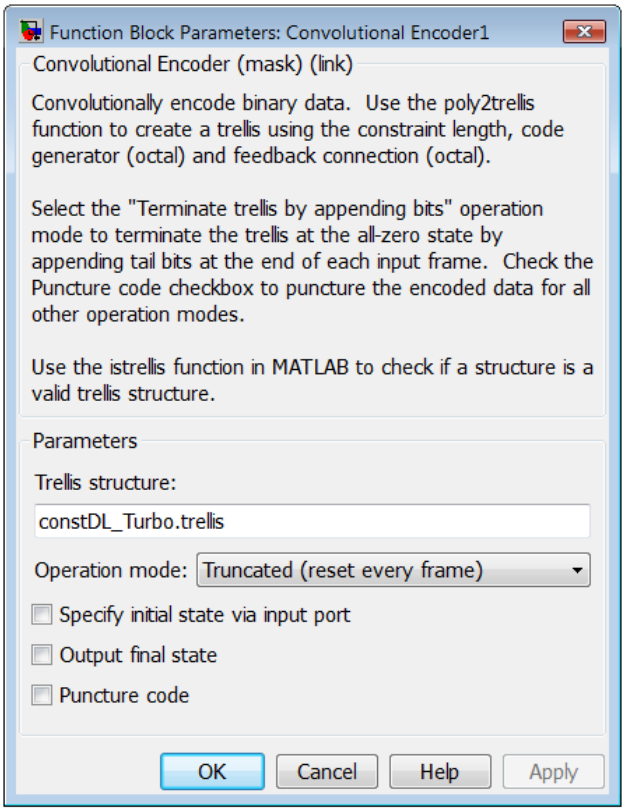


Figure 3.6 *Setting parameters of the Convolutional Encoder Block in the LTE simulator.*

As shown in Figure 3.6, the parameter of '*Trellis structure*' is used to configure the encoder. The 'poly2trellis'[1] is a MATLAB function which can create such trellis structure. To speed up the LTE simulator, the trellis structure is predefined as a MATLAB variable '*constDL_Turbo.trellis*' and it is configured as

$$\text{poly}\,2\,\text{strellis}(4,[13,15],13)\,. \qquad (3.3)$$

The **Pad Block** is utilized to pad 3 zeros at the end of each original information bit stream which will be passed to the following convolutional encoder. With 3 padded zeros, all the three registers of one encoder will be correspondingly set to zeros, namely the trellis will be terminated with zero state. The sequence of the generated tail bits will be rearranged at the **Tail Bits Rearrange** block which is implemented by an **Embedded MATLAB Function** block.

The Turbo code internal interleaver is accomplished by a **Selector Block** (from Simulink block library). According to Section 5.1.3.2.3 in [8], the permutation sequence is predefined as a vector named '*constDL_Turbo.pi*'.

[1] The Matlab function 'poly2strellis' needs the Communications Toolbox at MATLAB.

Note that the Selector Block supports zero-based index mode which is convenient for the permutation.

The **Deinterlacer Block** can separate the systematic and parity bits from the output of the convolutional encoder.

3.2.3 Rate-matching

The main task of the rate-matching is to extract the exact set of bits to be transmitted within a given TTI [15]. In Section 5.1.4 of [8], the rate-matching for Turbo coded transport channels is defined per code block. For each code block, there are three basic steps composing a rate-matching: sub-block interleaver, bit collection and bit selection. Finally, after the rate-matching, each individually processed code block has to be concatenated and transferred to a modulation mapping block.

3.2.3.1 Sub-block interleaver

The sub-block interleaver is defined for each output stream from Turbo coding. The streams include a systematic bit stream, a parity bit stream and an interleaved parity stream.

For the sub-block interleaver in the LTE simulator, it takes advantage of a **Selector Block** (as the Turbo coding internal interleaver) to implement the interleaver functionality. According to Section 5.1.4.1.1 in [8], the permutation sequence is predefined based on the different interleave manners. The systematic and the first parity bit streams work with manner 1, meanwhile the interleaved parity bit steam works with manner 2. The predefined variables of permutation sequence associating to manner 1 and 2 are shown in Table 3.1. Especially for manner 1, the dummy bits of 'NULL' are padded to construct a matrix. The 'NULL' bits will be punctured in the circular buffer block.

Table 3.1 *The variables of permutation sequence for the sub-block interleaver in the LTE simulator.*

Manner	Variable name
manner 1	*constDL_RateM.subbl_interleaver_seq*
manner 2	*constDL_RateM.subbl_interleaver_Pi_k*

3.2.3.2 Bit collection

The bit collection step concatenates the three bit streams (the systematic bit stream, parity bit stream and interleaved parity stream) together according to Section 5.1.4.1.2 of [8].

In the LTE simulator, the bit collection is mainly achieved by an **Interlacer Block** and a **Matrix Concatenate Block**. The Interlacer Block primarily puts the parity bit stream and the interleaved parity bit stream together as a new bit stream. Meanwhile it arranges the bits from the former stream at the odd sequence and the bits from the latter stream at the even sequence. Afterwards the Matrix Concatenate Block concatenates the new bit stream and the former systematic bit stream together to create the input for the following circular buffer block.

3.2.3.3 Bit selection

The bit selection extracts consecutive bits from the circular buffer to the extent that fits into the assigned physical resource [15]. Combined with the Turbo coding, the circular buffer can puncture or repeat the collected coded bits to achieve an alterable channel coding rate under different scenarios.

a. Start point

To enable the operation of Incremental Redundancy (IR) based HARQ in LTE, the rate-matching is expected to provide different subsets of the code block for different transmissions of a packet. Hence, the concept of Redundancy Version (RV) is derived [10]. In the LTE simulator, the value of RV will be assigned from the HARQ scheduler to each code block to specify a start point of extracting bits from a circular buffer. See more details about the HARQ in Section 3.4.7.

In the case of the first transmission of each coded block (RV = 0), puncturing a small amount of systematic bits is proposed in [8] and [10]. Namely, instead of reading out data from the beginning of systematic bit stream, the output of the circular buffer starts from a specified point (see in Figure 3.7).

In the case of HARQ retransmission, the starting point of extracting bits will be configured according to a specified RV (RV = 1, 2, 3).

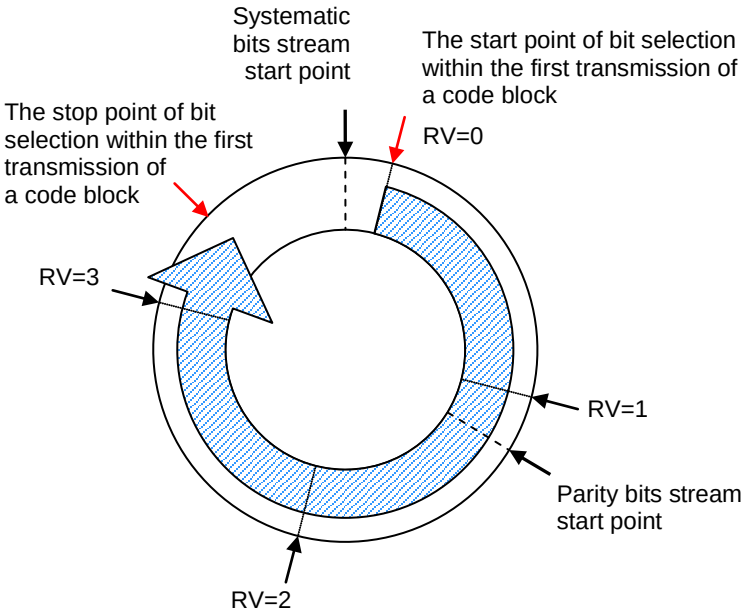


Figure 3.7 *Circular buffer work with puncturing scheme.*

b. Circular buffer length

In Section 5.1.4.1.2 of [8], the circular buffer length depends on two factors:

- the length of bit stream K_w reading out from the bit collection;
- the length of total number of soft channel bits which associates to the UE category defined in Section 4.1 of [11].

The LTE simulator assumes the UE always possesses a minimum capacity to receive the maximum number of DL-SCH transport block bits within a TTI, so the circular buffer length is always equal to the K_w. The length is predefined as that of a variable named '*constDL_RateM_embed.N_cb*' in the LTE simulator. The variable associates to the parameter of N_{cb} in Section 5.1.4.1.2 of [8].

c. Stop point

The bit selection stop point E [1] is calculated based on the total number of bits G [2] that available for transmission of one transport block in PDSCH.

[1] For the definition of 'E', see Section 5.1.4.1.2 of [8].
[2] For the definition of 'G', see Section 5.1.4.1.2 of [8].

The different code blocks which are segmented from one transport block may have a different stop point E in the circular buffer. In the case that E is less than N_{cb}, a puncturing scheme is applied to the circular buffer (as the blue arrow shown in Figure 3.7); whereas in the case when E is larger than N_{cb}, a repeating bit selection happens and starts from the specific start point again (see Figure 3.8).

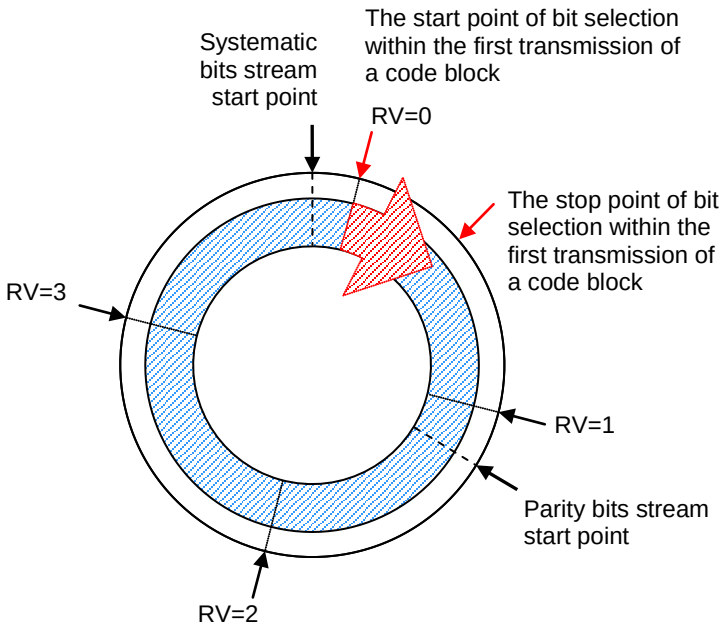


Figure 3.8 *Circular buffer work with repeating scheme.*

Moreover, the circular buffer of the LTE simulator is implemented by an **Embedded MATLAB Function Block**. Particularly, when puncturing the dummy bits 'NULL', which are generated by the sub-block interleaver, the locations of 'NULL' will be directly passed to the **Inverse Rate-Matching Block** at the receiver side. In practice, it should be well predefined at a receiver to accomplish a sub-block deinterleaver.

The main associated variables of the circular buffer are listed in Table 3.2.

Table 3.2 *The variables of the circular buffer*.

Name	Associated variables
Number of code blocks	*constDL_RateM_embed.C*
Length of circular buffer	*constDL_RateM_embed.N_cb*
Start point of bit selection	*Start*
Stop point of bit selection	*constDL_RateM_embed.E*
Note: the '*constDL_RateM_embed.E*' is defined as a row vector, and the number of elements is equal to '*constDL_RateM_embed.C*'. Each element defines the stop point of bit selection on each code block.	

3.2.4 Modulation

There are three modulation schemes for the PDSCH: QPSK, 16QAM and 64QAM.

In the LTE simulator, the Simulink block of **Rectangular QAM Modulator Baseband** is utilized to achieve the whole QAM modulation schemes above and also the QPSK scheme. This is based on the fact that the QPSK possesses the same constellation mapping as 4QAM.

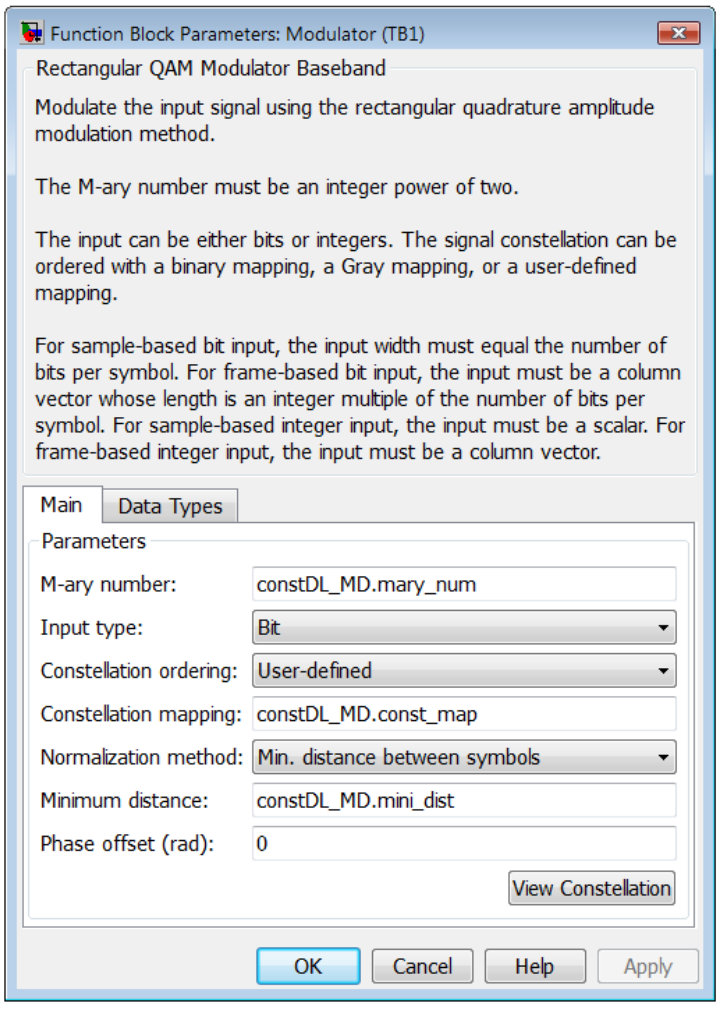


Figure 3.9 *The configuration of the Rectangular QAM Modulator Baseband block.*

Figure 3.9 shows the configuration of the Rectangular QAM Modulator Baseband Block, and the corresponding predefined basic parameters are listed in Table 3.3.

Table 3.3 *Parameters for Rectangular QAM Modulator Baseband.*

	Parameters	QPSK	16QAM	64QAM

Modulation order	*constDL_MD. mary_num*	2	4	6
Constellation mapping	*constDL_MD. const_map*	[2,3,0,1]	[11, 10, 14, 15, 9, 8, 12, 13, 1, 0, 4, 5, 3, 2, 6, 7]	[47,46,42,43,59,58,62,63, 45,44,40,41,57,56,60,61, 37,36,32,33,49,48,52,53, 39,38,34,35,51,50,54,55, 7, 6, 2, 3,19,18,22,23, 5, 4, 0, 1,17,16,20,21, 13,12, 8, 9,25,24,28,29, 15,14,10,11,27,26,30,31]
Minimum distance	*constDL_MD. mini_dist*	$\sqrt{2}$	$2/\sqrt{10}$	$2/\sqrt{42}$

3.2.5 Antenna mapping

3.2.5.1 Overview

Antenna mapping is the combination of layer mapping and pre-coding, which jointly process the modulation symbols for one or two codewords to make them transmit on different antenna ports. The structure of antenna mapping is illustrated in Figure 3.10.

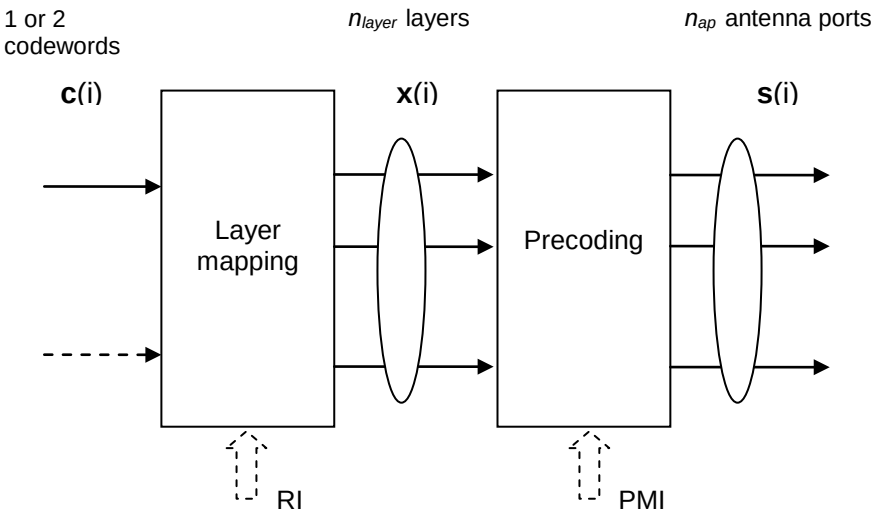


Figure 3.10 *The structure of antenna mapping.*

In Table 3.4, some necessary parameters are listed. The symbols for codewords, layers and antenna ports can be individually expressed as

$$\mathbf{c}(\mathrm{i}) = [c_0(i),...,c_{n_{codeword}-1}(i)]^T,\ i = 0,1,..,M_{symb}^{codeword}-1,$$

$$\mathbf{x}(\mathrm{i}) = [x_0(i),...,x_{n_{layer}-1}(i)]^T,\ i = 0,1,..,M_{symb}^{layer}-1,$$

$$\mathbf{s}(\mathrm{i}) = [s_0(i),...,s_{n_{ap}-1}(i)]^T,\ i = 0,1,..,M_{symb}^{ap}-1.$$

Table 3.4 *The parameters of the antenna mapping.*

Name	Description	Name in simulator
TxMode	Transmission mode	*MAC.TxMode*
$n_{codeword}$	The number of codewords	*MAC.num_codewords*
n_{layer}	The number of layers	*MAC.num_layers*
n_{ap}	The number of antenna ports	(not used)
nTx	The number of transmit antennas	*MAC.Tx_ANT.num*
nRx	The number of receive antennas	*MAC.Rx_ANT.num*
$M_{symb}^{codword}$	The number of symbols per codeword	*constDL_PRECOD. M_codeword_symb*
M_{symb}^{layer}	The number of symbols per layer	*constDL_PRECOD. M_layer_symb*
M_{symb}^{ap}	The number of symbols per antenna port	*constDL_PRECOD. M_ap_symb*
RI	Rank Indicator, i.e. a recommended number of layers by UE	*RI*
PMI	Pre-coding Matrix Indicator, i.e. a recommended pre-coder matrix by UE	*PMI*

Note:
1. Because only cell-specific reference signals are temporarily taken into account, which means that no ports other than ports {0, 1, 2, 3} are considered, in the simulator n_{ap} is equal to *nTx,* i.e. the number of transmit antennas (see more in page 326-327 of [15]).
2. In the simulator, the case of two codewords separately with different sizes is not included.

3.2.5.2 Layer mapping

In the layer mapping, the modulation symbols for one or two codewords will be mapped onto one or several layers.

According to Section 6.3.3 in [7], except the transmission on a single antenna port (in this case, the symbols for one codeword is directly mapped onto one layer), there are mainly two kinds of layer mapping: one for spatial multiplexing and the other for transmit diversity.

In case of spatial multiplexing, there may be one or two codewords. But the number of layers is restricted. On one hand, it should be equal to or more than the number of codewords. On the other hand, the number of layers cannot exceed the number of antenna ports. The most important thing is the concept of 'layer'. The layers in spatial multiplexing have the same meaning as 'streams'. They are used to transmit multiple data streams in parallel, so the number of layers here is often referred to as the transmission rank [15]. In spatial multiplexing, the number of layers may be adapted to the transmission rank, by means of the feedback of a Rank Indicator (RI) to the layer mapping, as Figure 3.10 shows. The implementation of layer mapping for spatial multiplexing is depicted in Figure 3.11.

Note:

1. In this simulator, only a fixed number of layers is initialized and used. A RI is calculated in the receiver. However, it is not sent back. This calculation is only available for closed-loop multiplexing.

2. Mapping two codewords onto three layers is not available, as the transmission of two transport blocks with different sizes are currently not supported by this simulator.

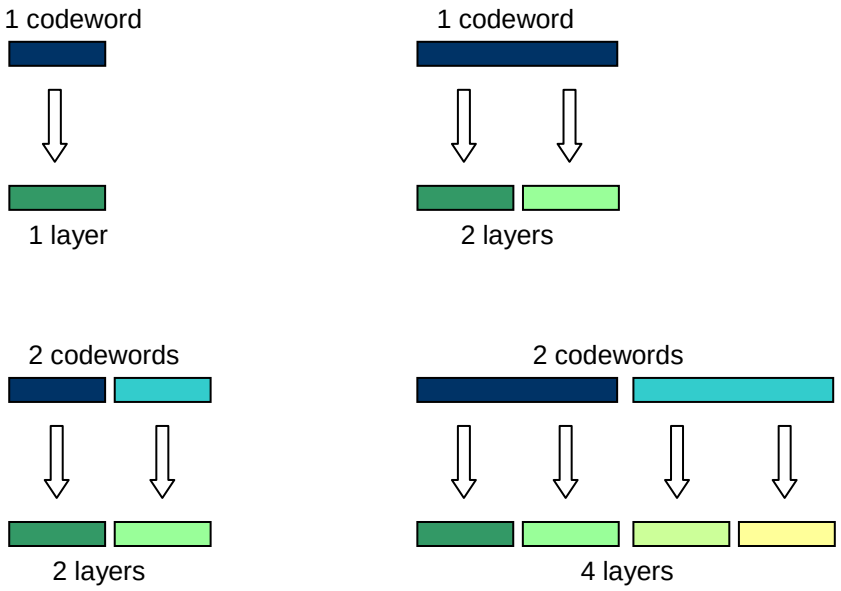


Figure 3.11 *Layer mapping for spatial multiplexing.*

In case of transmit diversity, there is only one codeword and the number of layers is equal to the number of antenna ports. Moreover, the number of layers in this case is not related to the transmission rank, because transmit-diversity schemes are always single-rank transmission schemes [15]. The layers in transmit diversity are used to conveniently carry out the following pre-coding by some pre-defined matrices. Figure 3.12 shows the implementation of layer mapping for transmit diversity.

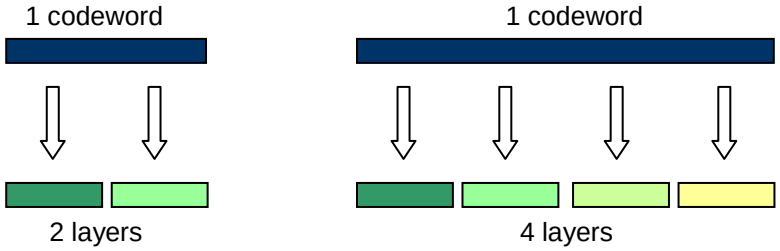


Figure 3.12 *Layer mapping for transmit diversity.*

3.2.5.3 Pre-coding

According to different transmission modes, symbols on each layer will be pre-coded for transmission on the antenna ports. Table 3.5 shows transmission modes for PDSCH [9], as well as the corresponding status in the simulator.

Table 3.5 *Transmission mode of PDSCH.*

Transmission mode	Transmission scheme of PDSCH	Status
Mode 1	Single-antenna port; port 0	Supported
Mode 2	Transmit diversity	Supported
Mode 3	Large delay CDD	Supported
Mode 4	Closed-loop spatial multiplexing	Supported
Mode 5	Multi-user MIMO	Not supported
Mode 6	Closed-loop spatial multiplexing using a single transmission layer	Supported
Mode 7	Single-antenna port; port 5	Not supported
Mode 8	Dual layer transmission; port 7 and 8 or single-antenna port; port 7 or 8	Not supported
Note: Only one user and only transmission using cell-specific reference signals are considered, so modes 5, 7, 8 are not supported.		

3.2.5.3.1 Single-antenna port; port 0

This mode uses only one transmit antenna. In this case, there may be two or more receive antennas, which can configure receiver diversity. The pre-coding of this mode is defined by $s_0(i) = x_0(i)$ and $M_{symb}^{ap} = M_{symb}^{layer}$ [7].

3.2.5.3.2 Closed-loop spatial multiplexing

The pre-coding for closed-loop spatial multiplexing has a structure shown in Figure 3.10, with a feedback of PMI. It is defined by [7]

$$\mathbf{s}(\mathrm{i}) = \mathbf{W}(\mathrm{i})\mathbf{x}(\mathrm{i}), \tag{3.4}$$

where $\mathbf{W}(\mathrm{i})$ is the pre-coding matrix of size $n_{ap} \times n_{layer}$ and $i = 0,1,..,M_{symb}^{ap} - 1$, $M_{symb}^{ap} = M_{symb}^{layer}$.

In this mode, the decision of $\mathbf{W}(\mathrm{i})$ should take into account the feedback of PMI. The relation between $\mathbf{W}(\mathrm{i})$ and PMI will be discussed in Section 3.4.3. In practice, the eNodeB may, or may not, use the recommended $\mathbf{W}(\mathrm{i})$. For simplification, the eNodeB of the simulator directly follows the recommendation of the UE and all the symbols in a sub-frame are using the same $\mathbf{W}(\mathrm{i})$, which implies that $\mathbf{W}(\mathrm{i})$ changes with sub-frame.

According to the specification [7], there are two codebooks of the pre-coder matrices. One codebook is for two antenna ports and one and two layers, as illustrated in Table 3.6. The other codebook is for four antenna ports and one, two, three and four layers, as listed in Table 3.7. Both closed-loop spatial multiplexing and open-loop spatial multiplexing use these codebooks. Note that for all the transmission modes, the total power of the signals does not change after the implementation of pre-coding.

Table 3.6 *Codebook for pre-coder matrices on two antenna ports.*

Index	n_{layer}	
	1	2
0	$\frac{1}{\sqrt{2}}\begin{bmatrix}1\\1\end{bmatrix}$	$\frac{1}{\sqrt{2}}\begin{bmatrix}1 & 0\\0 & 1\end{bmatrix}$
1	$\frac{1}{\sqrt{2}}\begin{bmatrix}1\\-1\end{bmatrix}$	$\frac{1}{2}\begin{bmatrix}1 & 1\\1 & -1\end{bmatrix}$
2	$\frac{1}{\sqrt{2}}\begin{bmatrix}1\\j\end{bmatrix}$	$\frac{1}{2}\begin{bmatrix}1 & 1\\j & -j\end{bmatrix}$
3	$\frac{1}{\sqrt{2}}\begin{bmatrix}1\\-j\end{bmatrix}$	

Note: In case of closed-loop spatial multiplexing, the codebook index 0 is not used when the number of layers is two.

Table 3.7 *Codebook for pre-coder matrices on four antenna ports.*

Index	$\mathbf{u}_n$	n_{layer}			
		1	2	3	4
0	$\mathbf{u}_0 = [1\ -1\ -1\ -1]^T$	$\mathbf{W}_0^{\{1\}}$	$\mathbf{W}_0^{\{14\}}/\sqrt{2}$	$\mathbf{W}_0^{\{124\}}/\sqrt{3}$	$\mathbf{W}_0^{\{1234\}}/2$
1	$\mathbf{u}_1 = [1\ -j\ -1\ j]^T$	$\mathbf{W}_1^{\{1\}}$	$\mathbf{W}_1^{\{12\}}/\sqrt{2}$	$\mathbf{W}_1^{\{123\}}/\sqrt{3}$	$\mathbf{W}_1^{\{1234\}}/2$
2	$\mathbf{u}_2 = [1\ 1\ -1\ 1]^T$	$\mathbf{W}_2^{\{1\}}$	$\mathbf{W}_2^{\{12\}}/\sqrt{2}$	$\mathbf{W}_2^{\{123\}}/\sqrt{3}$	$\mathbf{W}_2^{\{3214\}}/2$
3	$\mathbf{u}_3 = [1\ j\ 1\ -j]^T$	$\mathbf{W}_3^{\{1\}}$	$\mathbf{W}_3^{\{12\}}/\sqrt{2}$	$\mathbf{W}_3^{\{123\}}/\sqrt{3}$	$\mathbf{W}_3^{\{3214\}}/2$
4	$\mathbf{u}_4 = \begin{bmatrix} 1 \\ (-1-j)/\sqrt{2} \\ -j \\ (1-j)/\sqrt{2} \end{bmatrix}$	$\mathbf{W}_4^{\{1\}}$	$\mathbf{W}_4^{\{14\}}/\sqrt{2}$	$\mathbf{W}_4^{\{124\}}/\sqrt{3}$	$\mathbf{W}_4^{\{1234\}}/2$
5	$\mathbf{u}_5 = \begin{bmatrix} 1 \\ (1-j)/\sqrt{2} \\ j \\ (-1-j)/\sqrt{2} \end{bmatrix}$	$\mathbf{W}_5^{\{1\}}$	$\mathbf{W}_5^{\{14\}}/\sqrt{2}$	$\mathbf{W}_5^{\{124\}}/\sqrt{3}$	$\mathbf{W}_5^{\{1234\}}/2$
6	$\mathbf{u}_6 = \begin{bmatrix} 1 \\ (1+j)/\sqrt{2} \\ -j \\ (-1+j)/\sqrt{2} \end{bmatrix}$	$\mathbf{W}_6^{\{1\}}$	$\mathbf{W}_6^{\{13\}}/\sqrt{2}$	$\mathbf{W}_6^{\{134\}}/\sqrt{3}$	$\mathbf{W}_6^{\{1324\}}/2$
7	$\mathbf{u}_7 = \begin{bmatrix} 1 \\ (-1+j)/\sqrt{2} \\ j \\ (1+j)/\sqrt{2} \end{bmatrix}$	$\mathbf{W}_7^{\{1\}}$	$\mathbf{W}_7^{\{13\}}/\sqrt{2}$	$\mathbf{W}_7^{\{134\}}/\sqrt{3}$	$\mathbf{W}_7^{\{1324\}}/2$
8	$\mathbf{u}_8 = [1\ -1\ 1\ 1]^T$	$\mathbf{W}_8^{\{1\}}$	$\mathbf{W}_8^{\{12\}}/\sqrt{2}$	$\mathbf{W}_8^{\{124\}}/\sqrt{3}$	$\mathbf{W}_8^{\{1234\}}/2$
9	$\mathbf{u}_9 = [1\ -j\ -1\ -j]^T$	$\mathbf{W}_9^{\{1\}}$	$\mathbf{W}_9^{\{14\}}/\sqrt{2}$	$\mathbf{W}_9^{\{134\}}/\sqrt{3}$	$\mathbf{W}_9^{\{1234\}}/2$
10	$\mathbf{u}_{10} = [1\ 1\ 1\ -1]^T$	$\mathbf{W}_{10}^{\{1\}}$	$\mathbf{W}_{10}^{\{13\}}/\sqrt{2}$	$\mathbf{W}_{10}^{\{123\}}/\sqrt{3}$	$\mathbf{W}_{10}^{\{1324\}}/2$
11	$\mathbf{u}_{11} = [1\ j\ -1\ j]^T$	$\mathbf{W}_{11}^{\{1\}}$	$\mathbf{W}_{11}^{\{13\}}/\sqrt{2}$	$\mathbf{W}_{11}^{\{134\}}/\sqrt{3}$	$\mathbf{W}_{11}^{\{1324\}}/2$
12	$\mathbf{u}_{12} = [1\ -1\ -1\ 1]^T$	$\mathbf{W}_{12}^{\{1\}}$	$\mathbf{W}_{12}^{\{12\}}/\sqrt{2}$	$\mathbf{W}_{12}^{\{123\}}/\sqrt{3}$	$\mathbf{W}_{12}^{\{1234\}}/2$
13	$\mathbf{u}_{13} = [1\ -1\ 1\ -1]^T$	$\mathbf{W}_{13}^{\{1\}}$	$\mathbf{W}_{13}^{\{13\}}/\sqrt{2}$	$\mathbf{W}_{13}^{\{123\}}/\sqrt{3}$	$\mathbf{W}_{13}^{\{1324\}}/2$
14	$\mathbf{u}_{14} = [1\ 1\ -1\ -1]^T$	$\mathbf{W}_{14}^{\{1\}}$	$\mathbf{W}_{14}^{\{13\}}/\sqrt{2}$	$\mathbf{W}_{14}^{\{123\}}/\sqrt{3}$	$\mathbf{W}_{14}^{\{3214\}}/2$
15	$\mathbf{u}_{15} = [1\ 1\ 1\ 1]^T$	$\mathbf{W}_{15}^{\{1\}}$	$\mathbf{W}_{15}^{\{12\}}/\sqrt{2}$	$\mathbf{W}_{15}^{\{123\}}/\sqrt{3}$	$\mathbf{W}_{15}^{\{1234\}}/2$
Note: $\mathbf{W}_n^{\{s\}}$ consists of the columns $\{s\}$ of the matrix $\mathbf{W}_n$, $\mathbf{W}_n = \mathbf{I} - 2\mathbf{u}_n\mathbf{u}_n^H/\mathbf{u}_n^H\mathbf{u}_n$, where $\mathbf{I}$ is the 4×4 identity matrix.					

3.2.5.3.3 Large delay CDD

There is a delay for the feedback of PMI in closed loop spatial multiplexing. When the UE moves at a high speed, the channel changes fast and the PMI delay may be larger than the coherence time of the channel. So the feedback of PMI is not reasonable any more. In this case, large delay CDD, another name of open-loop spatial multiplexing, is adopted as a replacement [15]. It does not require feedback of the pre-coding matrix by the UE. Instead, it cyclically applies some predetermined matrices.

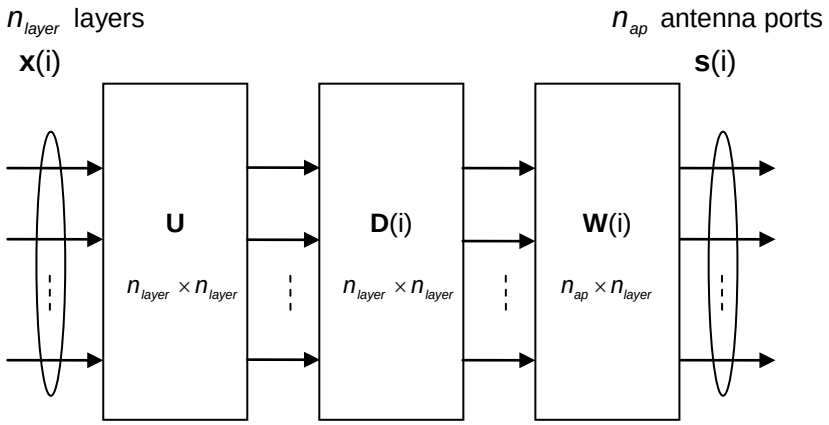


Figure 3.13 *The pre-coding of larger delay CDD.*

As seen in Figure 3.13, the pre-coding of larger delay CDD is defined by [15]

$$\mathbf{s}(i) = \mathbf{W}(i)\mathbf{D}(i)\mathbf{U}\mathbf{x}(i), \tag{3.5}$$

Where $i = 0,1,..,M_{symb}^{ap} - 1$ and $M_{symb}^{ap} = M_{symb}^{layer}$. $\mathbf{U}$ is the DFT pre-coding matrix [20] of size $n_{layer} \times n_{layer}$ and $\mathbf{D}(i)$ is a diagonal matrix of size $n_{layer} \times n_{layer}$ supporting cyclic delay diversity, listed in Table 3.8. The principle of CDD is explained in detail in [15-16]. Note that there is no matrix for a single layer, as larger delay CDD is only defined for the case of two or more layers [15].

The matrix $\mathbf{W}(i)$ is selected from the codebook in Table 3.6 or Table 3.7. For two antenna ports, $\mathbf{W}(i)$ is assigned as

$$\mathbf{W}(i) = \frac{1}{\sqrt{2}}\begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}. \tag{3.6}$$

For four antenna ports, $\mathbf{W}(i)$ changes cyclically according to the index i, assigned as

$$\mathbf{W}(i) = \mathbf{C}_k, \tag{3.7}$$

where k is the index given by $k = \left(\lfloor i / n_{layer} \rfloor \bmod 4 \right) + 1$ and $\mathbf{C}_1, \mathbf{C}_2, \mathbf{C}_3, \mathbf{C}_4$ separately correspond to the matrices $\mathbf{W}_{12}^{\{s\}}$, $\mathbf{W}_{13}^{\{s\}}$, $\mathbf{W}_{14}^{\{s\}}$, $\mathbf{W}_{15}^{\{s\}}$ in Table 3.7 (the set $\{s\}$ is decided by the number of layers).

Table 3.8 *Matrices of Large delay CDD.*

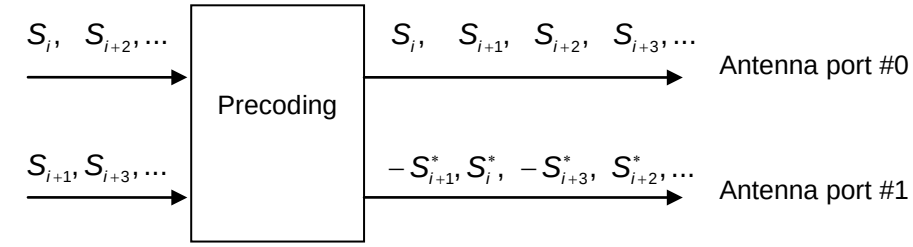
n_{layer}	**U**	**D**(i)
2	$\frac{1}{\sqrt{2}}\begin{bmatrix} 1 & 1 \\ 1 & e^{-j2\pi/2} \end{bmatrix}$	$\begin{bmatrix} 1 & 0 \\ 0 & e^{-j2\pi i/2} \end{bmatrix}$
3	$\frac{1}{\sqrt{3}}\begin{bmatrix} 1 & 1 & 1 \\ 1 & e^{-j2\pi/3} & e^{-j4\pi/3} \\ 1 & e^{-j4\pi/3} & e^{-j8\pi/3} \end{bmatrix}$	$\begin{bmatrix} 1 & 0 & 0 \\ 0 & e^{-j2\pi i/3} & 0 \\ 0 & 0 & e^{-j4\pi i/3} \end{bmatrix}$
4	$\frac{1}{2}\begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & e^{-j2\pi/4} & e^{-j4\pi/4} & e^{-j6\pi/4} \\ 1 & e^{-j4\pi/4} & e^{-j8\pi/4} & e^{-j12\pi/4} \\ 1 & e^{-j6\pi/4} & e^{-j12\pi/4} & e^{-j18\pi/4} \end{bmatrix}$	$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & e^{-j2\pi i/4} & 0 & 0 \\ 0 & 0 & e^{-j4\pi i/4} & 0 \\ 0 & 0 & 0 & e^{-j6\pi i/4} \end{bmatrix}$

3.2.5.3.4 Closed-loop spatial multiplexing using a single transmission layer

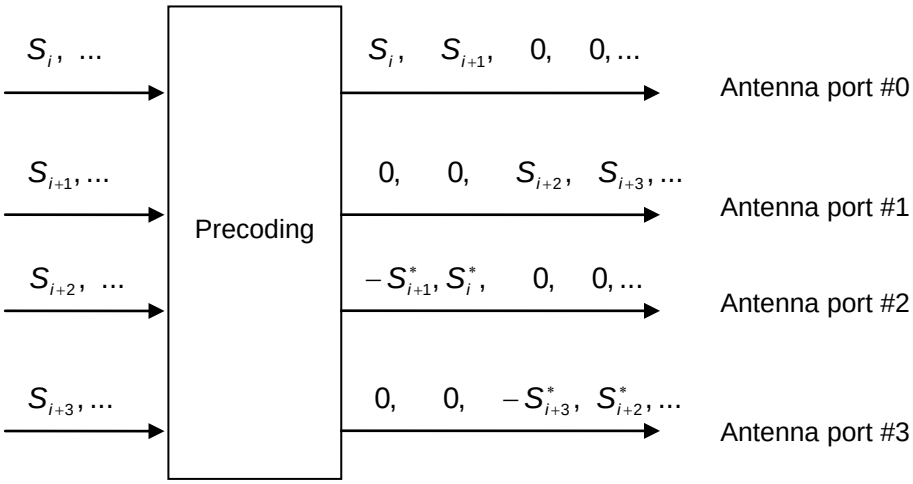
This mode is a special case of closed-loop spatial multiplexing when the number of layers is one. It can be regarded as codebook-based beam-forming. In this case, a recommendation of pre-coding vector should also be reported by the UE. Similarly, the recommended vector is directly used by the eBodeB. The vector is selected from the codebook in Table 3.6 or Table 3.7, only for a single layer.

3.2.5.3.5 Transmit diversity

Transmit diversity for two antenna ports is based on Space Frequency Block Coding (SFBC), and transmit diversity for four antenna ports is based on a combination of SFBC and Frequency Shift Transmit Diversity (FSTD) [15]. According to the specifications [7], transmit diversity is implemented by a pre-defined matrix. The functionalities for both two and four antenna ports are illustrated in Figure 3.14. It can be seen that compared with the case of two antenna ports, the four-antenna-port transmission has a reduced bandwidth utilization. Note that unlike spatial multiplexing, M_{symb}^{ap} in transmit diversity equals $M_{symb}^{codeword}$ rather than M_{symb}^{layer}, which just implies that the concept of layers for the two cases are basically different.



a. 2-antenna-port transmit diversity.



b. 4-antenna-port transmit diversity.

Figure 3.14 *The Pre-coding of LTE transmit diversity.*

3.2.6 Physical mapping

3.2.6.1 Resource mapping

As defined in Section 6.2 of [7], the transmitted signal in each slot time is described by a resource grid of $N_{RB}^{DL}N_{sc}^{RB}$ sub-carriers and N_{symb}^{DL} OFDM symbols. In case of multi-antenna transmission, there is one resource grid defined per antenna port. Furthermore, each antenna port is defined by its associated reference signal. Each element in the resource grid for a specified antenna port is called a resource element, and is uniquely identified by the index pair (*k, n*) in a slot, where *k* and *n* are the indices in the frequency and time domains respectively.

The resource-mapping block maps data symbols, reference signal symbols and control information symbols into a certain resource element at different antenna ports.

In the LTE simulator, users can configure the properties of the resource grid with the parameters listed in Table 3.9. Since only a cell-specific reference signal is considered, the antenna ports associating to the LTE simulator contain antenna port 0-3 (defined in Section 6.10.1.2 of [7]).

Table 3.9 *Configuration of resource grid in the LTE simulator.*

Name	Associated parameters	Options
Number of resource blocks (N_{RB})	*MAC.N_RB*	6, 15, 25, 50, 100
Number of physical resource blocks (n_{PRB})	*MAC.N_PRB*	$1-98$
Cyclic Prefix type	*MAC.CP.type*	1 for normal CP 2 for extend CP
Number of OFDM symbols for L1/L2 control signals	*MAC.control_region.size*	0, 1, 2, 3, 4

Note:
1. The number of resource blocks is defined for the LTE system, and it associates to the system bandwidth and sampling frequency.
2. The number of physical resource blocks is defined for one UE, and is associated to the bandwidth of the UE.
3. Although there is no PDCCH in the LTE simulator now, the resource elements for the L1/L2 control signals are preserved and set to 0.

On each antenna port, resource mapping is implemented separately. Particularly, a certain resource element used for reference signal transmission on one antenna port in a slot shall not be used for any transmission on other antenna ports in the same slot and is therefore set to 0.

The implementation of resource mapping in the LTE simulator is done by Embedded MATLAB Function blocks. The reference signals are generated by a pseudo-random sequence generator. In the LTE simulator, to simplify the initialization of the generator, the slot number is defined from 0 to 1 (originally, 0-19 defined in the Section 6.10.1.1 of [7]), and the cell ID is defined as 0 for only one cell in the LTE simulator.

3.2.6.2 OFDM

Orthogonal Frequency Division Multiplexing (OFDM) is a basic technology of LTE, which is used in the downlink transmission scheme. OFDM can be seen as a kind of multi-carrier modulation, which divides a large system bandwidth into multiple narrowband sub-carriers. This makes each sub-carrier nearly flat fading. The use of cyclic-prefix mitigates inter-symbol interference in a time-dispersive channel.

The OFDM modulator can be implemented by a scaled inverse fast Fourier transform (IFFT), following the formula (3.1) in [18]

$$x(n) = \frac{1}{\sqrt{N}} \sum_{k=0}^{N-1} X(k) e^{j 2\pi k n / N}, \; n = 1,2,...,N-1, \qquad (3.8)$$

where $X(k)$ is the discrete signal in the frequency domain, $x(n)$ is the discrete signal in the time domain and N is the FFT size.

Correspondingly, the OFDM demodulator can be expressed as a scaled FFT, following the formula (3.2) in [18]

$$X(k) = \sqrt{N} \sum_{n=0}^{N-1} x(n) e^{-j 2\pi k n / N}, \; k = 1,2,...,N-1. \qquad (3.9)$$

There are several basic parameters of OFDM in this simulator, such as the sub-carrier spacing (Δf), the number of sub-carriers (N_{sc}), the cyclic prefix length (N_{cp}) and the FFT size (N). As shown in Table 3.10, a sub-carrier spacing of 15 kHz is used and the latter three parameters are decided by the system bandwidth (BW). There are also other related parameters in this table, like the sampling frequency (f_s) and the number of OFDM symbols in a slot (N_{symb}). The cyclic prefix length is specified in terms of samples, while in the specification [7], it is in terms of unit time.

Note that a system bandwidth of 15 MHz is not available in this simulator, as embedded MATLAB function only supports radix-2 FFT. All other bandwidths in Table 3.10 are supported.

Table 3.10 *Downlink OFDM parameters [7] [12].*

BW (MHz)		1.4	3	5	10	15	20
Δf		15 kHz					
N_{sc}		72	180	300	600	900	1200
N		128	256	512	1024	1536	2048
Sampling Rate		1.92	3.84	7.68	15.36	23.04	30.72
N_{symb}		7/6 (normal/extended CP)					
N_{cp}	Normal	9 (10)	18 (20)	36 (40)	72 (80)	108 (120)	144 (160)
	Extended	32	64	128	256	384	512

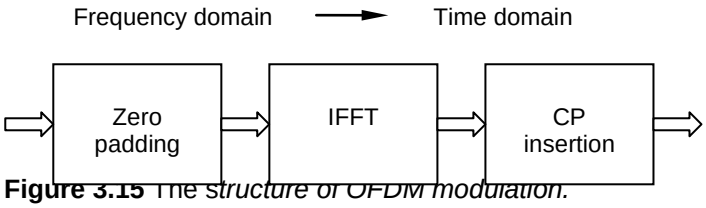


Figure 3.15 The *structure of OFDM modulation.*

The structure of OFDM generation for each antenna port for is illustrated in Figure 3.15. The zero padding process is shown in Figure 3.16 (the first value after zero padding is an unused DC-sub-carrier and the blank part in the figure is padded with zeros). An IFFT transforms frequency-domain signals into time-domain signals, i.e. OFDM symbols. The CP insertion copies the last N_{cp} samples of the OFDM symbol and appends them at the beginning of the symbol.

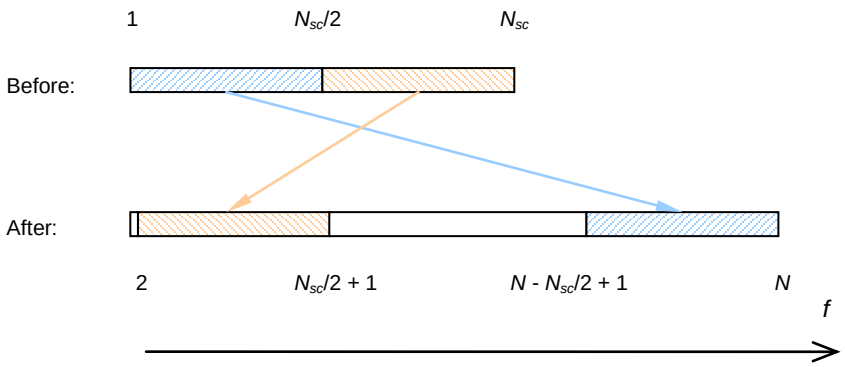


Figure 3.16 *Zero padding of OFDM modulation.*

In the implementation of OFDM, **Embedded MATLAB function** is used. The OFDM parameters have corresponding names to those stated in Table 3.10. Another parameter '*MAC.CP.type*' is also set to specify a case of normal CP or extended CP (see Table 3.9).

3.3 Channel Model

In the LTE simulator, two channel models are applied: a WINNER channel mode and an AWGN channel mode.

The WINNER channel can provide a multi-path fading channel under different scenarios, and it can support the 2×3 MIMO functionality currently.

In the LTE downlink simulator, the channel estimator will focus on each single channel path separately in the frequency domain. For convenience, we describe the channel from one transmit antenna to one receive antenna as a column vector

$$\mathbf{h} = \begin{bmatrix} H_0 & H_1 & \cdots & H_{N-1} \end{bmatrix}^T \tag{3.10}$$

in the frequency domain. Here, H_j ($j = 0,1,\cdots N-1$) is the channel gain on each sub-carrier in the frequency domain; *N* is equal to the number of sub-carriers within all RBs. The path loss and channel fading are assumed to be included in $\mathbf{h}$. In the case of MIMO transmission, each channel path $\mathbf{h}$ will be estimated at the receiver side.

The transmitted signals can be expressed as a diagonal matrix

$$\mathbf{X} = \begin{bmatrix} X_0 & 0 & \cdots & 0 \\ 0 & X_1 & & 0 \\ \vdots & & \ddots & \vdots \\ 0 & 0 & \cdots & X_{N-1} \end{bmatrix} \tag{3.11}$$

in the frequency domain.

After the processing of inverse-OFDM on each receive antenna port, a received signal can be described as a column vector $\mathbf{y}$ with *N* elements, and

$$\mathbf{y} = \mathbf{Xh} + \mathbf{v}\,, \tag{3.12}$$ [1]

$$\mathbf{h} = \mathbf{Fg}\,, \tag{3.13}$$

where $\mathbf{v}$ is a vector of the noise in the frequency domain, $\mathbf{g}$ is a vector of the channel impulse response in the time domain, and

$$\mathbf{F} = \begin{bmatrix} W_N^{00} & \cdots & W_N^{0(N-1)} \\ \vdots & \ddots & \vdots \\ W_N^{(N-1)0} & \cdots & W_N^{(N-1)(N-1)} \end{bmatrix} \tag{3.14}$$

is the DFT-matrix with

$$W_N^{nk} = \frac{1}{\sqrt{N}} e^{-j2\pi\frac{nk}{N}}. \tag{3.15}$$

In case of the AWGN channel mode, the $\mathbf{h}$ in Equation (3.12) is a column vector with N elements of 1.

[1] As a matter of convenience, Equation (3.12) is written in a matrix notation.

In the LTE simulator, as a matter of convenience, the white noise $\mathbf{v}$ is directly added in the frequency domain (see the Note in Section 3.4.1.2). In addition, the Chase Antenna Blocks are not connected when using the AWGN channel mode.

3.4 Receiver

In the LTE downlink simulator, the receiver side simulates the operation of one user equipment that processes the received signals and interacts with the eNodeB. Figure 3.17 shows the basic flow. After the steps of inverse-OFDM and inverse resource mapping, the receiver has to do the channel estimation to provide the necessary channel information to the following blocks of equalizer and PMI calculation. Note that after the layer extracting, up to two processing lines operates in parallel in case of spatial multiplexing. Otherwise there is only one process line in action.

In each TTI, the following blocks of demodulation, inverse rate-matching, channel decoding, and HARQ will detect each transmitted bits and ask for a retransmission if any errors remain after the Turbo decoding.

Finally, the PMI will be fed back to the eNodeB, and the decoded bits are transferred to the output calculation block to evaluate the simulator performance in each TTI.

Because the receiver is not specified in the 3GPP specifications [7-9], this part is mainly implemented based on the books and articles from [19] to [27].

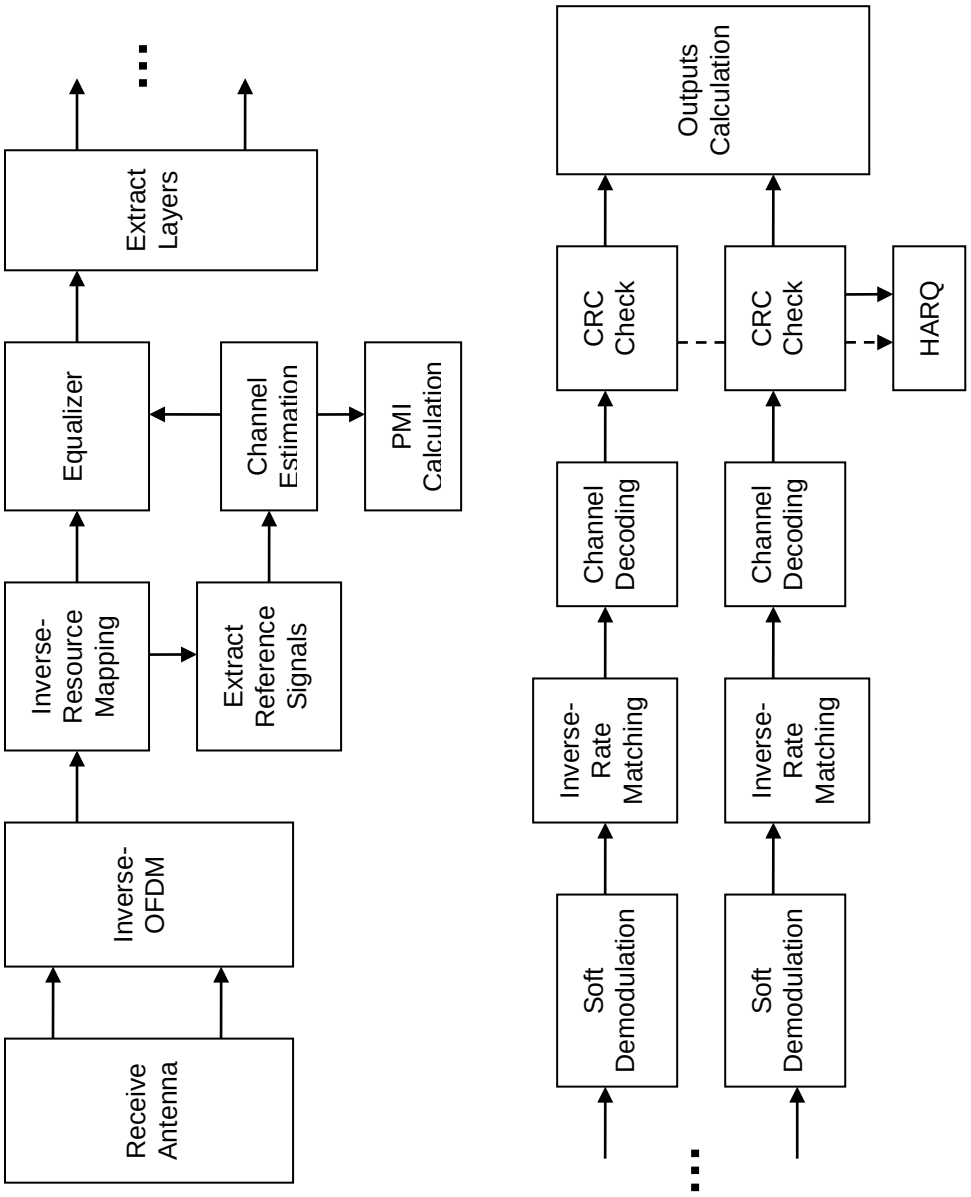


Figure 3.17 *The structure of the LTE simulator receiver.*

3.4.1 Channel estimation

3.4.1.1 Overview

The main functionality of the channel estimation is to provide an estimated channel $\hat{\mathbf{h}}$ to the equalizer and the calculators of the RI and PMI in the frequency domain. A noise variance estimate is also provided from the channel estimation. For brevity, the description of the calculation for this noise variance is omitted.

In an LTE downlink system, the symbols of the reference signal are inserted within fixed sub-carriers per antenna port as defined in Section 6.10 of [7]. Thus, the preliminary step is to estimate a physical channel at the reference signals' sub-carrier frequency in the frequency domain and within each sub-frame in the time domain. After the preliminary channel estimate, based on the available channel information, an interpolation is applied to the other data symbols both in the frequency and time domain.

In the LTE simulator, three channel estimation models are accomplished:

- Ideal channel estimation with Least Square (LS) estimator;
- Non-ideal channel estimation with LS estimator;
- Non-ideal channel estimation with modified LS estimator.

Section 3.4.1.2 describes the implementation of ideal channel estimation in the LTE simulator. In Sections 3.4.1.3 and 3.4.1.4, two alternative algorithms for the preliminary channel estimation are considered. One is the LS estimator; the other is the modified LS estimator. In the end, the interpolation is introduced in Section 3.4.1.5.

3.4.1.2 Ideal channel estimation

Taking the WINNER channel into account, the channel information $\mathbf{h}$ in Equation (3.12) cannot be directly obtained from the channel model. So a compromise way of ideal channel estimation is developed in the simulator. Namely, the channel estimator extracts channel information before the **AWGN Block,** which will provide a white noise $\mathbf{v}$ (see Figure 3.18). Otherwise, in case of a non-ideal channel estimation model, the channel estimator works after the noise is added by the AWGN Block.

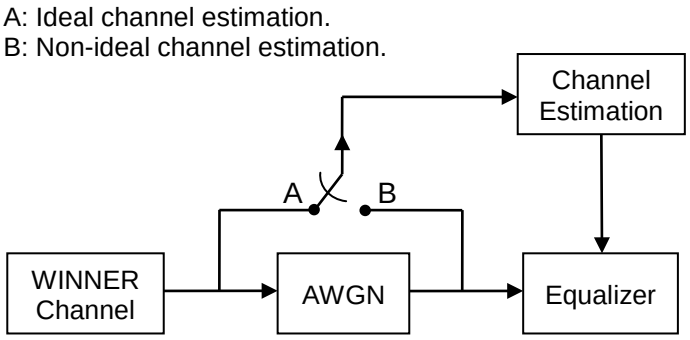


Figure 3.18 *The structure of the channel estimation.*

Note:

To simplify the LTE simulator, the noise will be directly added in the frequency domain for both ideal and non-ideal channel estimations, i.e. the AWGN Block is applied after the inverse-OFDM process.

3.4.1.3 Least Square estimator

Since the reference signals are not inserted in each sub-carrier in the LTE downlink, the number of elements N in Equation (3.11) will be reduced to N_{RS}^{DL}, which is the number of sub-carriers used for reference signals within all RBs. In the LTE downlink, it is calculated as

$$N_{RS}^{DL} = N_{RB}^{DL} \times 2, \tag{3.16}$$

where N_{RB}^{DL} is the number of RBs in downlink.

Correspondingly for the preliminary channel estimation, with the reduction of elements to N_{RS}^{DL}, the received reference signals are described as

$$\mathbf{y}_{RS} = \mathbf{X}_{RS}\mathbf{h}_{RS} + \mathbf{v}_{RS}, \tag{3.17}$$

$$\mathbf{h}_{RS} = \mathbf{F}_{RS}\mathbf{g}_{RS}. \tag{3.18}$$

The LS estimator for channel impulse response $\mathbf{g}_{RS}$ minimizes $(\mathbf{y}_{RS} - \mathbf{X}_{RS}\mathbf{h}_{RS})^H(\mathbf{y}_{RS} - \mathbf{X}_{RS}\mathbf{h}_{RS})$ and generates the estimated channel as

$$\hat{\mathbf{h}}_{RS\_LS} = \mathbf{F}_{RS}\mathbf{Q}_{RS\_LS}{\mathbf{F}_{RS}}^H{\mathbf{X}_{RS}}^H\mathbf{y}_{RS}, \tag{3.19}$$

where

$$\mathbf{Q}_{RS\_LS} = ({\mathbf{F}_{RS}}^H{\mathbf{X}_{RS}}^H\mathbf{X}_{RS}\mathbf{F}_{RS})^{-1}. \tag{3.20}$$

The theoretical structure of the LS estimator is shown in Figure 3.19. Since ${\mathbf{X}_{RS}}^H\mathbf{X}_{RS} = \mathbf{I}$, Equation (3.19) can be reduced to

$$\hat{\mathbf{h}}_{RS\_LS} = {\mathbf{X}_{RS}}^H\mathbf{y}_{RS} = {\mathbf{X}_{RS}}^{-1}\mathbf{y}_{RS}. \tag{3.21}$$

Thus, the structure of LS estimator can be simplified.

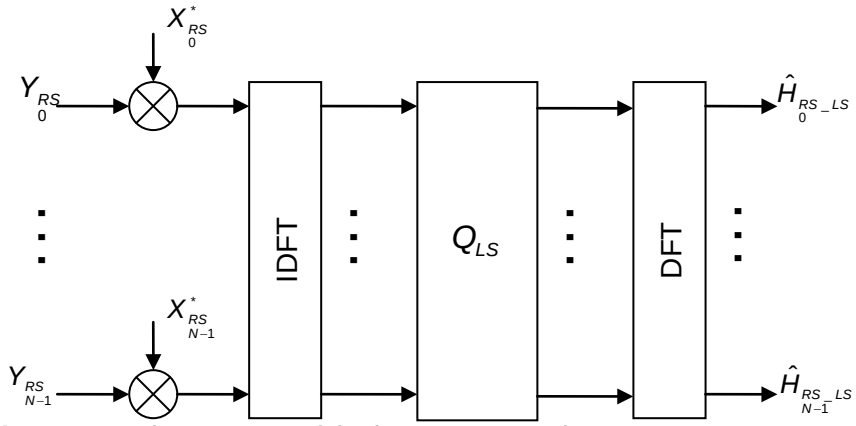


Figure 3.19 *The structure of the least square estimator.*

In the LTE simulator, a 4-dimensional matrix of $\mathbf{X}_{RS}^{-1}$ is pre-defined as a variable '*constDL_Estimate.LS_invX* ' at the m-file 'LTE_Initialize.m '. The 1st dimension associates to the number of transmit antennas; the 2nd and 3rd dimensions associate to N_{RS}^{DL}; and the 4th dimension associates to the OFDM symbol number within one slot.

3.4.1.4 Modified least square estimator

The disadvantage of the LS estimator is a high mean-square error. Thus, a modified LS estimator has been derived in [19]. The basic structure of the modified LS estimator is shown in Figure 3.20.

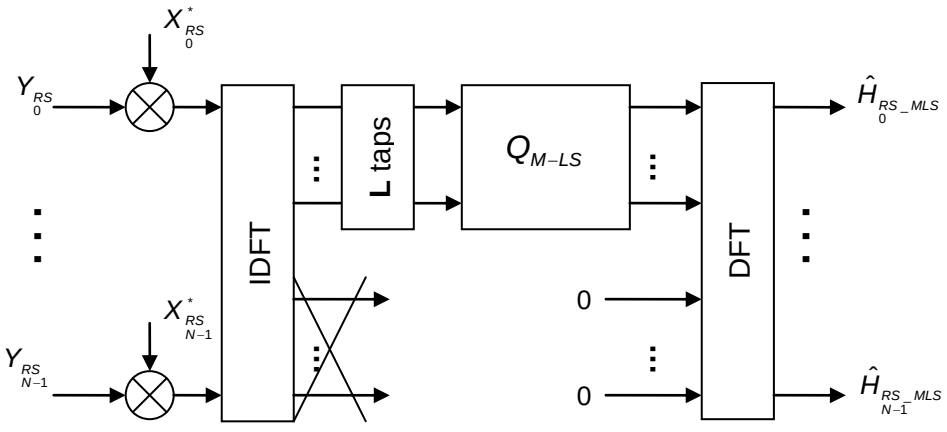


Figure 3.20 *The structure of the modified least square estimator.*

After an IDFT processing, most of the energy in $\mathbf{g}_{RS}$ focuses on the first L taps in the time domain, as shown in the second image of Figure 3.21. By assuming the noise possesses a uniform distribution over the entire range, the residual $N-L$ taps can be cut off to decrease the mean-square error.

The number of the first L taps is defined as

$$L = r \times N_{RS}^{DL}. \tag{3.22}$$

In an OFDM system, under the assumption of channel length less than or equal to the CP length, the ratio r in Equation (3.22) is calculated as the ratio between the length of one CP and one OFDM symbol.

With normal CP mode, the ratio is set to 144/2048; and with extend CP mode, the ratio is set to 512/2048. See Section 6.12 in [7] for CP and OFDM symbol lengths.

Define the matrix $\mathbf{T}_{RS}$ as the first L columns of the DFT-matrix $\mathbf{F}_{RS}$. Then the estimated channel $\hat{\mathbf{h}}_{RS\_MLS}$ can be generated by a modified LS estimator as

$$\hat{\mathbf{h}}_{RS\_MLS} = \mathbf{T}_{RS}\mathbf{Q}_{RS\_MLS}\mathbf{T}_{RS}{}^{H}\mathbf{X}_{RS}{}^{H}\mathbf{y}_{RS}, \tag{3.23}$$

where

$$\mathbf{Q}_{RS\_MLS} = (\mathbf{T}_{RS}{}^{H}\mathbf{X}_{RS}{}^{H}\mathbf{X}_{RS}\mathbf{T}_{RS})^{-1}. \tag{3.24}$$

To speed up the LTE simulator, a 4-dimensional matrix of $\mathbf{T}_{RS}\mathbf{Q}_{RS\_MLS}\mathbf{T}_{RS}{}^{H}\mathbf{X}_{RS}{}^{H}$ is predefined as a variable '*constDL_Estimate.M_LS_invX*'. The structure of the variable is the same as that of '*constDL_Estimate. LS_invX*' defined in Section 3.4.1.3.

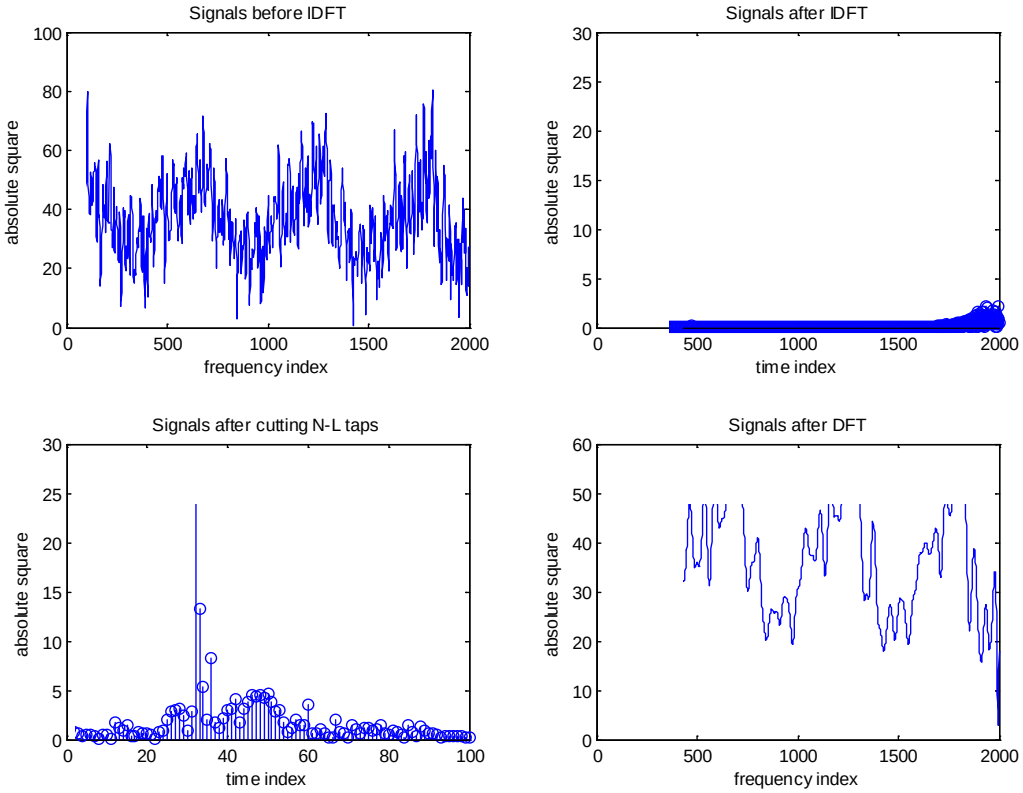


Figure 3.21 *The signals at the different steps of a modified least square estimator.*

3.4.1.5 Interpolation

In the LTE simulator, based on the available channel information generated by the preliminary estimation, a linear interpolation is applied to produce the whole estimated channel information on each sub-carrier frequency (except on reference signals') per sub-frame time (1ms).

The first linear interpolation is applied between two reference symbols along the frequency dimension (see the vertical arrows in Figure 3.22). Particularly, for the data symbols at the band edge, one UE can utilize the reference signals from the neighbors to accomplish the linear interpolation.

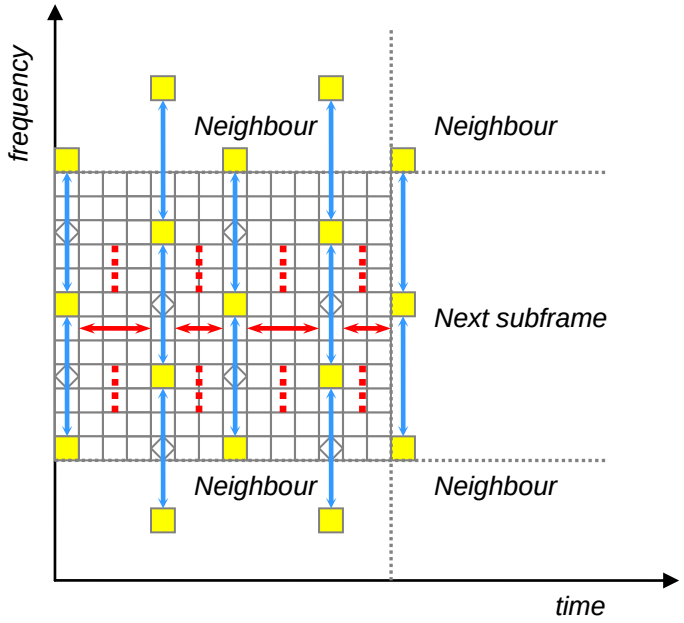


Figure 3.22 *The channel estimation on the edge symbols.*

The second linear interpolation is applied on each sub-carrier along the time dimension (as the horizontal arrows shown in Figure 3.22). For the interpolation of the last two OFDM symbols in time, the channel information from the next sub-frame will be used.

In the LTE simulator, a **Buffer Block** (from the Simulink block library) is utilized to buffer 2 sub-frames in one TTI. The configurations of the Buffer Block are illustrated in Figure 3.23. The parameter '*Output buffer size*' affects the number of rows of the output. In the simulator, this parameter is associated to the number of OFDM symbols within two sub-frames. The value of '*MAC.N_symb*' is equal to 7 for the normal CP mode; otherwise it is equal to 6. Multiplying with 4 implies that there are 4 slots within 2 sub-frames. Another parameter called 'Buffer overlap' specifies the number of rows from the current output to be repeated in the next output. Such a parameter is configured as the number of OFDM symbols within one sub-frame.

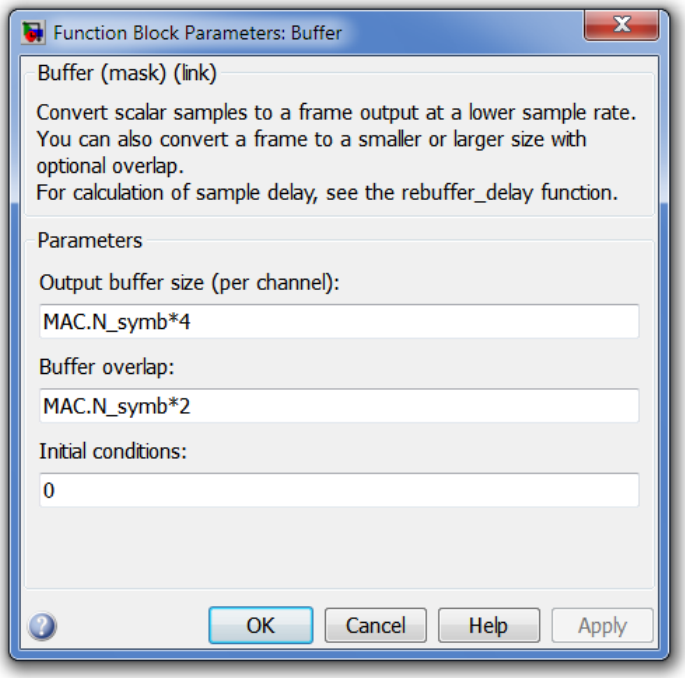


Figure 3.23 *The dialog block of the Buffer.*

3.4.2 Equalizer

The equalizer is used to reduce the inter-symbol interference (ISI) as a result of the channel frequency selectivity and recover the original signals. In this simulator it is implemented in the frequency domain and consists of two parts: one is a Linear Minimum Mean-Square Error (LMMSE) receiver for single-antenna transmission and spatial multiplexing, and the other is decoding of Space-Frequency Block Codes for transmit diversity. The equalizer requires receiver CSI (channel state information), which can be obtained from channel estimation. The performance of the equalizer is related to the performance of the channel estimation. In other words, how well the channel information is estimated.

As this part is implemented by an **Embedded MATLAB function**, the following parts will focus on the model and the algorithms.

3.4.2.1 Model

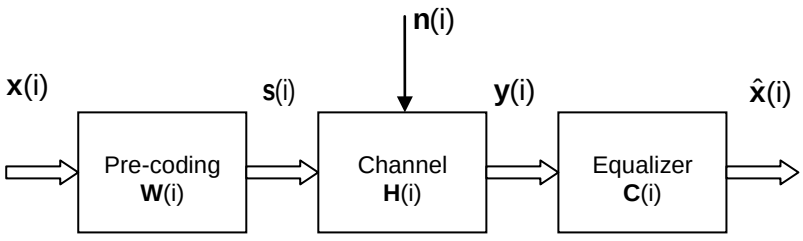


Figure 3.24 *The model of the equalizer.*

Figure 3.24 shows the frequency-domain model of the equalizer in the simulator for sub-carrier number i, where **W**(i) is the whole pre-coding matrix (for open-loop spatial multiplexing, this matrix is a product as **W**(i)**D**(i)**U**), **H**(i) is the channel matrix[1] and **C**(i) is the equalizer matrix. Besides, the signals at the different stages are expressed as

$\mathbf{x}(\mathrm{i}) = [x_0(i),...,x_{n_{layer}-1}(i)]^T,\ i = 0,1,..,M_{symb}^{layer} - 1.$

$\mathbf{s}(\mathrm{i}) = [s_0(i),...,s_{nTx-1}(i)]^T,\ i = 0,1,..,M_{symb}^{ap} - 1.$

$\mathbf{y}(\mathrm{i}) = [y_0(i),...,y_{nRx-1}(i)]^T,\ i = 0,1,..,M_{symb}^{ap} - 1.$

$\hat{\mathbf{x}}(\mathrm{i}) = [\hat{x}_0(i),...,\hat{x}_{n_{layer}-1}(i)]^T,\ i = 0,1,..,M_{symb}^{layer} - 1$ (for spatial multiplexing)

$\hat{\mathbf{x}}(\mathrm{i}) = \hat{x}(i),\ i = 0,1,..,M_{symb}^{ap} - 1$ (for transmit diversity)[2]

Assume that the channel matrix **H**(i) and additive white Gaussian noise **n**(i) are separately given by

$$\mathbf{H}(\mathrm{i}) = \begin{bmatrix} h_{11}(i) & h_{12}(i) & \cdots & h_{1nTx}(i) \\ h_{21}(i) & h_{22}(i) & \cdots & h_{2nTx}(i) \\ \vdots & \vdots & \ddots & \vdots \\ h_{nRx1}(i) & h_{nRx2}(i) & \cdots & h_{nRxnTx}(i) \end{bmatrix}, \qquad (3.25)$$

$\mathbf{n}(\mathrm{i}) = [n_0(i),...,n_{nRx-1}(i)]^T,$

where $h_{jk}(i)$ denotes the frequency-domain complex channel gain between transmit antenna k and receive antenna j, and $i = 0,1,..,\ M_{symb}^{ap} - 1$. Note that the simulator has an assumption that $n_j(i)$ is independently distributed with a variance of σ_j^2 at each receive antenna j, which means the cross-covariance between noises from different receive antennas is zero.

Thus, the signal model before the equalization can be expressed as

$$\mathbf{y}(\mathrm{i}) = \mathbf{H}(\mathrm{i})\mathbf{W}(\mathrm{i})\mathbf{x}(\mathrm{i}) + \mathbf{n}(\mathrm{i}). \qquad (3.26)$$

[1] The channel matrix **H**(i) in this section has the different definition from the channel vector **h** in Section 3.3 and Section 3.4.1.

[2] For transmit diversity, there is no layer de-mapping in this simulator and the output is just the input in this case.

Furthermore, the signal model after the equalization can be expressed as

$$\hat{\mathbf{x}}(\mathrm{i}) = \mathbf{C}(\mathrm{i})\mathbf{H}(\mathrm{i})\mathbf{W}(\mathrm{i})\mathbf{x}(\mathrm{i}) + \mathbf{C}(\mathrm{i})\mathbf{n}(\mathrm{i}) . \quad (3.27)$$

It is clear that the noise may be enhanced after the equalization and the equalized noise can be calculated as

$$\mathbf{n}_{enh}(\mathrm{i}) = \mathbf{C}(\mathrm{i})\mathbf{n}(\mathrm{i}) . \quad (3.28)$$

3.4.2.2 LMMSE equalizer

The LMMSE equalizer minimizes the mean square error in estimating the original signal. According to the estimated channel matrices $\hat{\mathbf{H}}(\mathrm{i})$ and the estimated noise covariance matrix $\hat{\mathbf{N}} = diag([\hat{\sigma}_1^2 \ ... \ \hat{\sigma}_{nRx}^2])$, the equalizer matrix for each sub-carrier is given by [17]

$$\mathbf{C} = (\hat{\mathbf{H}}\mathbf{W})^* (\hat{\mathbf{H}}\mathbf{W}\mathbf{W}^*\hat{\mathbf{H}}^* + \hat{\mathbf{N}})^{-1}, \quad (3.29)$$

where the sub-carrier index i is omitted for brevity. For single-antenna transmission, $\mathbf{W}$ should be replaced by the constant 1 and for open-loop spatial multiplexing, $\mathbf{W}$ should be substituted by $\mathbf{W} \times \mathbf{D} \times \mathbf{U}$.

3.4.2.3 The decoding and combining of Space-Frequency Block Codes

The equalization for transmit diversity follows two steps. The first is the decoding of Space-Frequency Block Codes at each receive antenna, which is similar to the Alamouti decoding. The second is the combining of decoded values for all the receive antennas.

Firstly, according to Alamouti decoding, it is easy to find a way to decode Space-Frequency Block Codes [21]. For example, in case of two transmit antennas, the receive signal at antenna j can be expressed by

$$\begin{pmatrix} y_j(i) \\ y_j^*(i+1) \end{pmatrix} = \frac{1}{\sqrt{2}} \mathbf{H}_j \begin{pmatrix} x_0(i) \\ x_1^*(i) \end{pmatrix} + \begin{pmatrix} n_j(i) \\ n_j^*(i+1) \end{pmatrix}, \quad (3.30)$$

where the scaling factor $1/\sqrt{2}$ assures that the total power of the signals are fixed in the processing of precoding. Here,

$$\mathbf{H}_j = \begin{pmatrix} h_{j1} & -h_{j2} \\ h_{j2}^* & h_{j1}^* \end{pmatrix}. \quad (3.31)$$

Note that this expression is based on an assumption that the channel gains h_{j1} and h_{j2} are constant over the two consecutive sub-carriers. Then the equalizer is written as [21]

$$\begin{pmatrix} \hat{x}_0(i) \\ \hat{x}_1^*(i) \end{pmatrix} = \mathbf{C}_j \begin{pmatrix} y_j(i) \\ y_j^*(i+1) \end{pmatrix}, \tag{3.32}$$

where

$$\mathbf{C}_j = \frac{\sqrt{2}}{|h_{j1}|^2 + |h_{j2}|^2} \mathbf{H}_j^*. \tag{3.33}$$

The scaling factor $\sqrt{2}$ is used to make the whole channel unity. It is easily noticed that

$$\mathbf{H}_j \mathbf{H}_j^* = (|h_{j1}|^2 + |h_{j2}|^2) \times \mathbf{I}, \tag{3.34}$$

where $\mathbf{I}$ is the identity matrix. Alternatively, the equalizer in Equation (3.33) can be expressed as:

$$\mathbf{C}_j = \sqrt{2}\mathbf{H}_j^* (\mathbf{H}_j \mathbf{H}_j^*)^{-1}. \tag{3.35}$$

Not considering the scaling factor, this expression has the same form as a zero-forcing equalizer. That is why the decoding of Space-Frequency Block Codes is set as a part of the 'equalizer' in this simulator.

Secondly, the maximum ratio combining (MRC) is done on the decoded results at all the receive antennas. Take two receive antennas for example. Assume that $\hat{x}_{ANT0}(i)$ and $\hat{x}_{ANT1}(i)$ are the decoded results at two antennas. The combination is given by [22]

$$\hat{x}(i) = \frac{\sqrt{SNR_{ANT0}}\hat{x}_{ANT0}(i) + \sqrt{SNR_{ANT1}}\hat{x}_{ANT1}(i)}{\sqrt{SNR_{ANT0}} + \sqrt{SNR_{ANT1}}}. \tag{3.36}$$

The weight of the branch j is the square root of the corresponding signal-to-noise ratio SNR_{ANTj} after decoding. In the end, the scaling factor $1/(\sqrt{SNR_{ANT0}} + \sqrt{SNR_{ANT1}})$ is added to make the whole channel unity. The signal-to-noise ratio at antenna j is calculated as (The power of the transmitted signal is equal to 1)

$$SNR_{ANTj} = \frac{|h_{j1}|^2 + |h_{j2}|^2}{2\sigma_j^2}. \tag{3.37}$$

3.4.3 Pre-coding Matrix Indicator calculation

3.4.3.1 Overview

The PMI calculation is only implemented in case of closed-loop spatial multiplexing.

The methods used in the simulator were presented by Stefan Schwarz et al. in [23-24]. The decision of PMI is based on calculation of the mutual information between the signals of a certain model. The mutual information varies with the choice of the pre-coding matrix, so the task of this part is to find the value of PMI corresponding to the maximum mutual information.

In this simulator, the PMI calculation is realized using an **Embedded MATLAB function**. The models and algorithms of two mutual information based methods will be considered next.

3.4.3.2 Pre-equalization mutual information based method [23]

The signal model in this method is similar to that in Equation (3.26). It is given by

$$\mathbf{y}_{k,n} = \mathbf{H}_{k,n}\mathbf{W}_i\mathbf{x}_{k,n} + \mathbf{n}_{k,n}. \qquad (3.38)$$

Here k is a frequency index and n is a time index, because this method is based on the sub-frame structure (a sub-frame consists of resource elements, each of which has a frequency index and a time index). Each vector or matrix in Equation (3.38) has the same size as that in Equation (3.26), and it is assumed that the noise at the receiving antennas have the same variance σ_n^2. Moreover, the same pre-coding matrix is used in a whole sub-frame and it changes with the subframe.

The mutual information on resource element (k, n) can be expressed as [23]

$$I_{k,n}(\mathbf{W}_i) = \log_2 \det(\mathbf{I}_{n_{layer}} + \frac{1}{\sigma_n^2}\mathbf{W}_i^*\hat{\mathbf{H}}_{k,n}^*\hat{\mathbf{H}}_{k,n}\mathbf{W}_i). \qquad (3.39)$$

After that, the mutual information is summed up on all the elements in a sub-frame and the sum rate is given as

$$R(\mathbf{W}_i) = \sum_n \sum_k I_{k,n}(\mathbf{W}_i). \qquad (3.40)$$

It can be regarded as a function of $\mathbf{W}_i$, which is chosen from the codebook. Then the selected pre-coding matrix can be decided according to

$$\mathbf{W}_k = \arg \max_{\mathbf{W}_i} R(\mathbf{W}_i) . \quad (3.41)$$

Finally, according to the definition of PMI [9], the receiver sends back the value of PMI corresponding to the index of selected pre-coder matrix.

One drawback of this method is the computational complexity. A modified method, which is mean mutual information based, is a compromise between complexity and performance (see more details in [23]). In the simulator, the modified method is adopted.

3.4.3.3 Post-equalization mutual information based method [24]

The post-equalization mutual information based method may have a better performance for the linear receiver. The signal model, which can be compared with the model in Equation (3.27), is given by

$$\hat{\mathbf{x}}_{k,n} = \mathbf{C}_{k,n}\mathbf{H}_{k,n}\mathbf{W}_i\mathbf{x}_{k,n} + \mathbf{C}_{k,n}\mathbf{n}_{k,n} . \quad (3.42)$$

The mutual information can be calculated according to the signal-to-interference-plus-noise ratio (SINR) as

$$I_{k,n}(\mathbf{W}_i) = \sum_{l=1}^{n_{layer}} \log_2(1 + SINR_{k,n,l}) , \quad (3.43)$$

where $SINR_{k,n,l}$ is written as ($\mathbf{K}_{k,n} = \mathbf{C}_{k,n}\mathbf{H}_{k,n}\mathbf{W}_i$)

$$SINR_{k,n,l} = \frac{\left|\mathbf{K}_{k,n}(l,l)\right|^2}{\sum_{j\neq l}\left|\mathbf{K}_{k,n}(l,j)\right|^2 + \sigma_n^2 \sum_j \left|\mathbf{C}_{k,n}(l,j)\right|^2} . \quad (3.44)$$

The remaining of steps are similar to those in the pre-equalization mutual information based method.

3.4.3.4 The strategies of the implementation

Because this simulator uses a pre-determined number of layers, this simulator only maximizes the mutual information for the fixed number of layers in the calculation of PMI. While in the calculation of RI, the simulator tries all possible number of layers and pre-coder matrices to find the RI corresponding to the maximum mutual information. Note that PMI is calculated and used in the simulator, while RI is calculated but not used (just included as a reference).

3.4.3.5 Others

A delay of 8 TTI for the feedback of PMI (i.e. *constDL_PRECOD.PMI.delay* $= 8$) is used in the LTE downlink simulator. Another parameter '*constDL_PRECOD.PMI.calcMode'* in the simulator is used to assign the different PMI calculation methods (1 implies the pre-equalization method, and 2 means the post-equalization method).

What is sent back is the value of the index rather than pre-coder matrix, so it is necessary to know the definition of PMI, which is described in Table 3.11.

Table 3.11 *Precoding Matrix indicator (PMI) definition [9].*

n_{ap}	n_{layer}	PMI value	Pre-coder Matrix
2	1	$PMI \in \{0, 1, 2, 3\}$	Matrix in Table 3.6, with index PMI and n_{layer} equal to 1.
	2	$PMI \in \{0, 1\}$	Matrix in Table 3.6, with index $PMI + 1$ and n_{layer} equal to 2.
4	1, 2, 3 or 4	$PMI \in \{0, 1, ..., 15\}$	Matrix in Table 3.7, with index PMI and corresponding n_{layer} .

3.4.4 Demodulation

To provide a soft bit stream to the soft combining block and the Turbo decoder, the demodulation part has to work in the soft decision mode within the form of log-likelihood ratio (LLR) which reflects the reliability on each received bit as

$$LLR(b_k) = log\left[\frac{Pr(b_k = 0|x_k)}{Pr(b_k = 1|x_k)}\right]. \qquad (3.45)$$

The LLR is the ratio of probabilities of a 0 bit being transmitted versus a 1 bit being transmitted based on the knowledge of received signal x_k .

In the LTE simulator, the demodulator is achieved by the block of **Rectangular QAM Demodulator Baseband** from the Simulink library. The configuration of demodulation mapping should be the same as the modulator in Section 3.2.4, and it is illustrated in Figure 3.25.

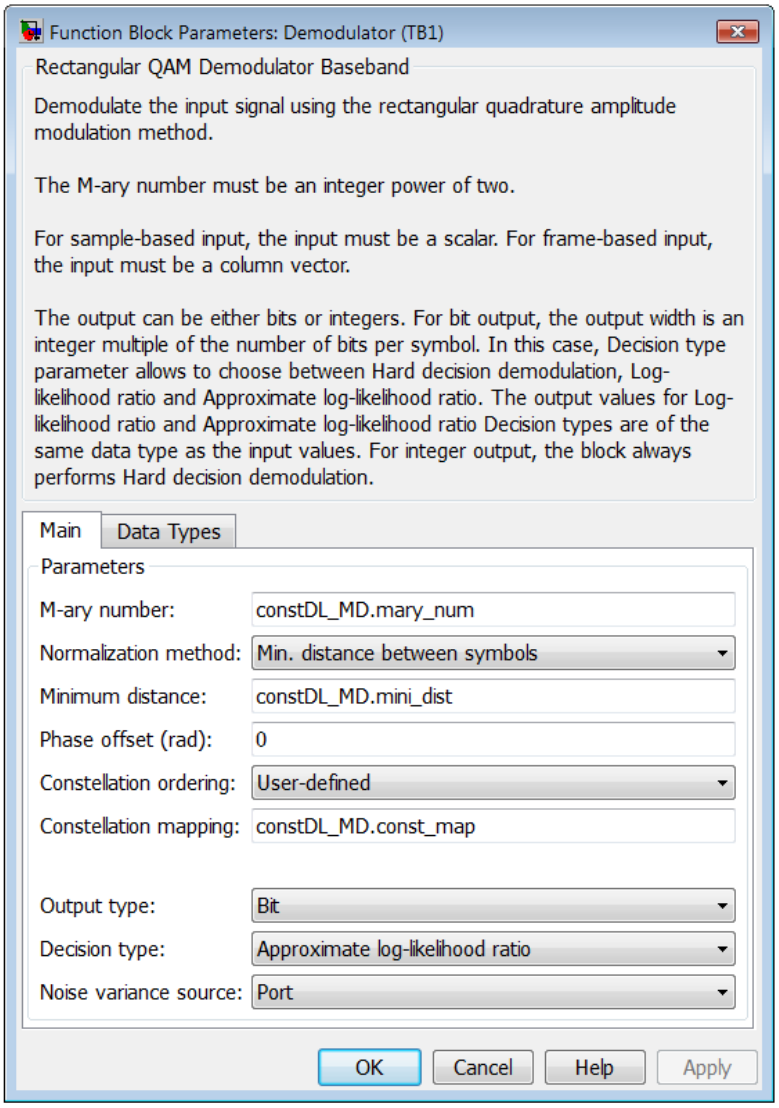


Figure 3.25 *The configuration of the Rectangular QAM Demodulator Baseband block.*

In addition, the decision type of 'Approximate log-likelihood ratio' is another option available for the soft decision of the Rectangular QAM Demodulator Baseband block. The approximate LLR [25] is calculated by the nearest constellation point to the received signal rather than all the constellation points as done in LLR.

The parameter 'Noise variance source' of the demodulator block is provided by an extra port that receives an estimated noise variance from the equalizer block.

Note, the Simulink block **APP Decoder**, which is utilized in the Turbo decoding part (see Section 3.4.6), works with a sign reversed formulation of the LLR algorithm as

$$LLR(b_k) = log\left[\frac{Pr(b_k = 1|x_k)}{Pr(b_k = 0|x_k)}\right]. \qquad (3.46)$$

Hence, the output from the **Rectangular QAM Demodulator Baseband** block working with Equation (3.45) should be inversed. It is implemented in the LTE simulator with a **Gain** block whose relative parameter is configured as '-1'.

3.4.5 Inverse rate-matching

By segmenting and permuting the output from the demodulation block, the inverse rate-matching step reconstructs the data within the form of a code block, which will be fed to a Turbo decoder. The basic flow of the inverse rate-matching includes: segmentation, zero padding (or soft combining) and sub-block deinterleaver.

Corresponding to the transmitter, an inverse processing of the circular buffer is applied in the inverse rate-matching step of the receiver. Similarly, the segmentation, zero-padding and soft combining have to cooperate with the HARQ block, which will provide a Redundancy Version (RV) to decide the start point of a circular buffer.

In the LTE simulator, the functions of segmentation, zero-padding and soft combining are implemented with an **Embedded MATLAB Function** block. For the variables of the inverse circular buffer possessing, see Table 3.2. For the variables associated to the HARQ, see Table 3.12.

3.4.5.1 Segmentation

In each TTI, the output from the demodulator is a bit stream corresponding to one transport block. The inverse rate-matching is defined per code block, so the segmentation of the output from the demodulator is performed to reconstruct the complete code blocks with soft values associated to one transport block.

Before the segmentation, a preparative step is required. The dummy bits of 'NULL' which are punctured at the transmitter side have to be restored according to their original positions. Then the segmentation is accomplished based on the stop point (denote by E in Section 3.2.3.3) of a circular buffer at the transmitter side.

However to exactly reconstruct the whole code blocks, an extra step of zero padding or soft combining is required, which is opposite to the puncturing or repeating bits at the transmitter side.

3.4.5.2 Zero padding

Corresponding to the puncturing process in the circular buffer of the transmitter, the zero padding is applied at the punctured bit positions to reconstruct the original code block. The length of the reconstructed code block is equal to N_{cb} (the circular buffer length).

Since the LLR reflects the reliability of a received bit, the padded zeros within the LLR form implies

$$Pr(b_k = 0|x_k) = Pr(b_k = 1|x_k). \tag{3.47}$$

3.4.5.3 Soft combining

The soft combing is applied when a repeating process is applied at the circular buffer of the transmitter, or in the case that a HARQ retransmission of one code block occurs. Based on Equation (3.48), the retransmitted bits within the LLR form will be combined with the overlapped bits together to increase the accumulated received E_b / N_0.

$$LLR(b_k) = \sum_{i=1}^{M} log\left[\frac{Pr(b_{ki} = 1|x_k)}{Pr(b_{ki} = 0|x_k)}\right], \tag{3.48}$$

where *M* is the number of transmissions.

3.4.6 Channel decoding

3.4.6.1 Turbo decoding

The Turbo decoder possesses an iterative structure which updates Maximum A Posteriori (MAP) probabilities of coded bits. Corresponding to the parallel concatenated convolutional code (PCCC) at the transmitter, the Turbo decoder consists of two parallel concatenated convolutional decoders which exchange the information of posteriori probabilities.

In the LTE simulator, the Turbo decoder is implemented with two Soft-Input Soft-Output (SISO) modules [26] which are based on the MAP algorithm to decode each constituent. According to Equations (1, 2) of [27], within each iterative processing, the two SISO decoders yield the extrinsic information which would be applied as the a-priori information to each other.

The structure of the SISO based Turbo decoder is shown in Figure 3.26. The input (C; I) of both SISO 1 and SISO 2 are the coded bit stream within LLR form generated by the soft demodulator. However, the input (U; I) working as the a-priori information of one SISO module comes from the output (U; O) of another SISO module. The output of updated coded bits (C; O) is not used in Turbo decoder.

After a maximum of 8 decoding iterations[1], the final decision is calculated by adding the two (U; O) from SISO 1 and SISO 2 together within the LLR form. Afterwards, the result will be processed with a hard decision, namely positive values are assigned with 1, and negative values are assigned with 0.

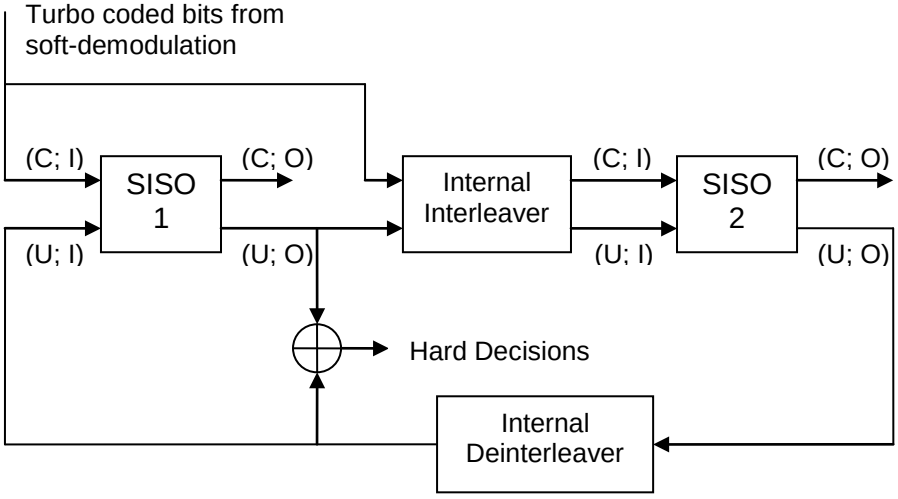


Figure 3.26 *The structure of the SISO based Turbo decoder [26].*

[1] Generally the iteration stops when a criterion is fulfilled, for example, there is no error indicated by the CRC check. To simplify the LTE simulator, each turbo code decoding will always be iterated 8 times

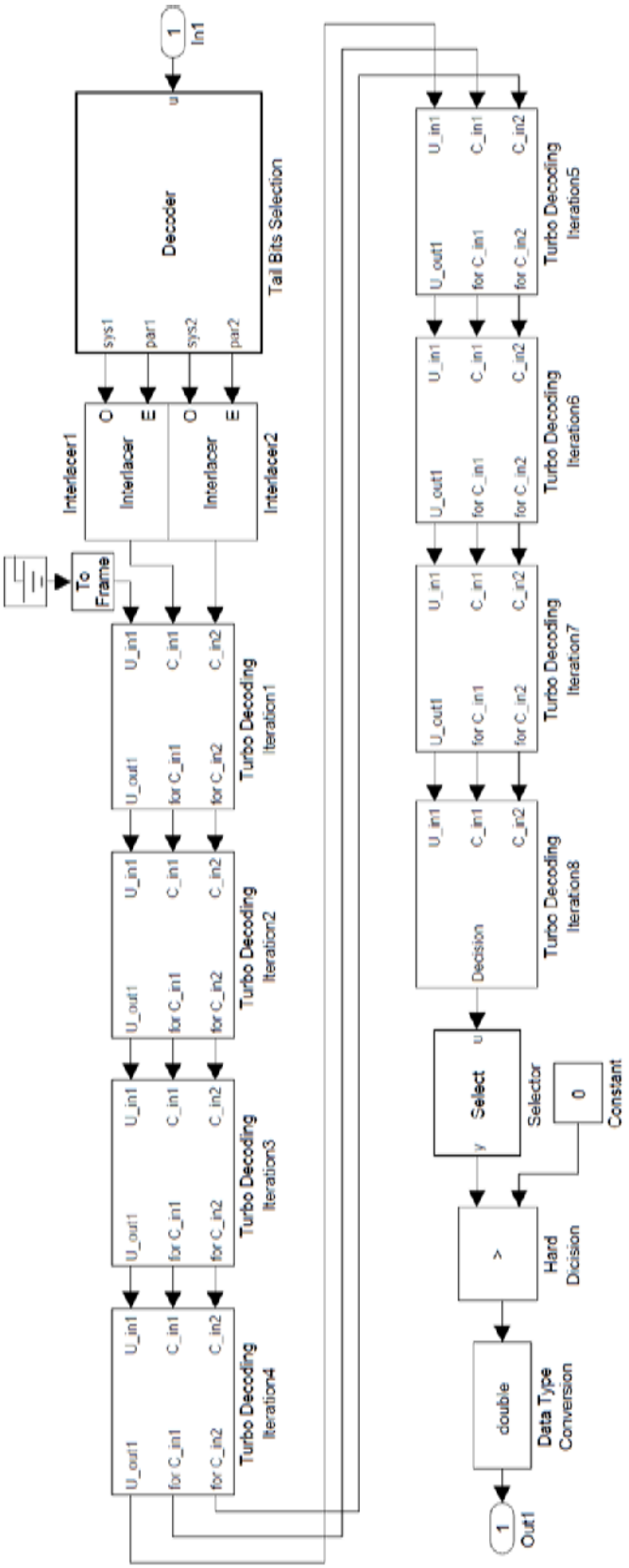


Figure 3.27 *The structure of the Turbo decoding in LTE simulator.*

In the LTE simulator, as illustrated in Figure 3.27, the Turbo decoding block mainly consists of 8 iteration decoders. Within each decoder, the **APP Decoder Block**[1] (from the Simulink block library) is directly utilized as a SISO module (see Figure 3.28). In addition, within the first iteration, the a-priori information for SISO 1 is provided with a zero-bit stream by the **Ground** block. After the hard decision block, an extra **Data Type Conversion** block is applied to convert the data type of final decision from Boolean to Double.

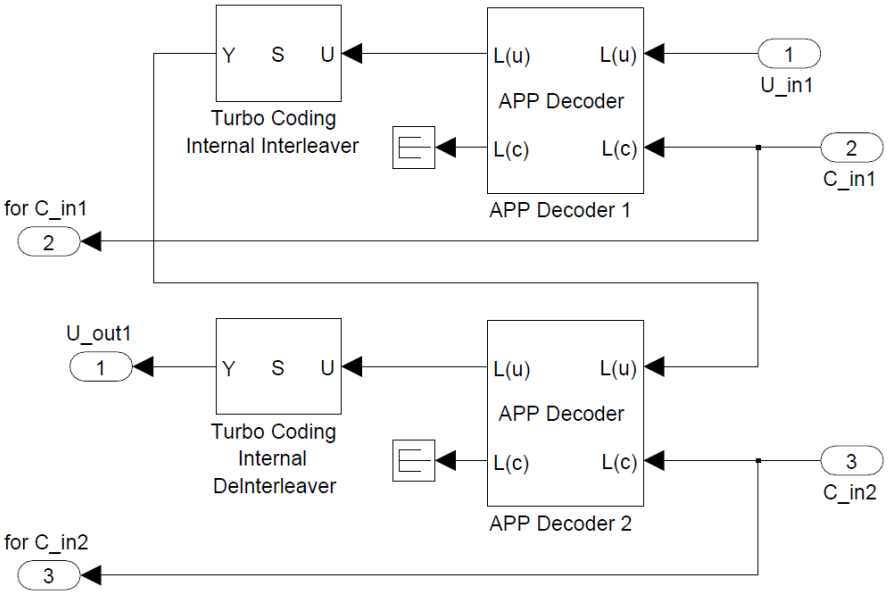


Figure 3.28 *The structure of one Turbo decoding iteration unit block.*

3.4.6.2 CRC check

For the CRC check, the block of **General CRC Syndrome Detector** (see Figure 3.29) is applied. The specified parameters of the CRC detector should be the same as the CRC generator for '*gCRC24A*' and '*gCRC24B*' respectively.

As shown in Figure 3.29, there is an **Err** port available at the General CRC Syndrome Detector block. The Err port can generate a vector which concerns the correctness of each input frame and is provided with a size equal to the number of checksums. It generates '0' if the whole frame is correct and '1' otherwise. The output from the Err port will be transferred to the HARQ to decide whether a retransmission is required.

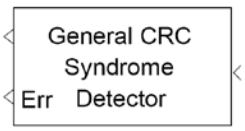


Figure 3.29 *The General CRC Syndrome Detector block in Simulink*.

[1] Decode convolutional code using A Posteriori Probability (APP) method.

3.4.7 Hybrid ARQ

3.4.7.1 Overview

The LTE employs Hybrid ARQ, which is a combination of forward error-control coding and Automatic Repeat Request (ARQ), to realize the fast retransmission of erroneous packets. The forward error-control coding uses Turbo coding to correct errors and the ARQ uses CRC, which provides ACK/NACK information, to detect errors.

For LTE, HARQ uses the strategy of Incremental Redundancy (IR), where each retransmission contains different bits from the original transmission. Then the receiver combines the retransmitted bits and the original bits. As a result, IR provides an additional coding gain at the cost of a lower code rate [15].

However, this simulator only focuses on the functionality of HARQ in the physical layer, so some simplifications are applied. For example, though the LTE downlink utilizes asynchronous adaptive HARQ [13], the issues of synchronization and adaptation for the simulator are not considered at this stage, so they are not considered for HARQ either. Instead, the time interval between the retransmission and the previous transmission is fixed, and the modulation and coding scheme does not change during the retransmission. The detailed implementation of HARQ for this simulator is specified in the following section.

3.4.7.2 Implementation of Hybrid ARQ

Hybrid ARQ is implemented via an **Embedded MATLAB function** in this simulator. The transmissions and retransmissions occur in the form of transport blocks, and the combining of received data is done in the form of code blocks.

The TTI of this simulator is set to 1 ms, equal to the time interval of a subframe. There may be one or two transport blocks (in spatial multiplexing) transmitted in a TTI. The two transport blocks are processed separately in the latter case. So next only the case of one transport block is considered.

Furthermore, the simulator only considers the downlink FDD case, when there will be a maximum of 8 HARQ processes [9]. Assume that the first transmission of a transport block takes place in sub-frame n. For simplification, if retransmission is needed, it always occurs in sub-frame $n+8$. The round-trip time (RRT) is set to 8 TTIs, equal to the maximum number of HARQ processes. This means ACK/NACK which will reach the transmitter in sub-frame $n+8$.

Except for the normal parameters like new-data indicator, redundancy version and the transport block size, some other parameters are set in this simulator for the convenience of implementation. The parameters are shown in Table 3.12.

Table 3.12 *Parameters in the implementation of HARQ.*

Type	Name	Description	Note
Constant	*TBsize*	Transport block size	
	N_{cb}	Soft buffer size for code blocks	
	C	Number of code blocks	
	NoHARQprocess	Number of HARQ processes	The value is 8
	MaxNoHARQ	Maximum number of HARQ transmission	The value is 4
	RVseq	Redundancy version coding sequence	The default value is $\{0, 1, 2, 3\}$ for QPSK and 16 QAM, $\{0, 0, 1, 2\}$ for 64 QAM (see Section 8 in [12])
Variable	*HI*	HARQ indicator	0: ACK; 1: NACK.
	NDI	New-data indicator	The value is 0 or 1
	RV	Redundancy version	The value is taken from *RVseq*
	NoReTx	Number of the total retransmissions	The value is 0, 1, 2 or 3 and 0 denotes a new transmission
Persistent Variable	*pers_HARQprocessMat*	A 2-D matrix as the buffer of transport blocks for all HARQ processes	The size is $TBsize \times NoHARQprocess$. It is used to record transport blocks of previous trans-missions for all HARQ processes.
	pers_HARQID	HARQ ID of current subframe	The value is one of $\{0, 1, \ldots, 7\}$
	pers_HARQbuffer	A 3-D matrix as soft buffer of code blocks for soft combining	The size is $N_{cb} \times C \times NoHARQprocess$. It is used to record reconstructed code blocks of previous transmissions for all HARQ processes.

In the simulator, implementation of HARQ involves four parts: resource generation, circle buffer, soft combining and CRC (of course the functionality of HARQ need more blocks), as illustrated in Figure 3.30. The resource generation can be regarded as the scheduler of HARQ, which is responsible for the new transmission and retransmission of transport blocks, and sending control information to other parts.

a. Resource generation

A new transmission is considered in case [14]:

1) It is the first transmission (for the first 8 sub-frames after the simulator runs);

2) ACK is received, i.e. $HI = 0$;

3) NACK is received but the maximum number of retransmission has been reached, i.e. $HI = 1$ and $NoReTx = 3$;

Otherwise, retransmission is considered.

The status of the parameters in HARQ processes (take QPSK for example) is described in Figure 3.31. In this example, the initial conditions of NDI are set as zeros. It can be seen that the TB with ID 1 retransmits two times, and TBs with ID 3 and 11 retransmit one time.

b. Circle Buffer

In Figure 3.30, a circle buffer is provided by the resource generation with the value of RV, which is used to decide the starting point of the selected bits. See more details in Section 3.2.3.3.

c. Soft combining

As seen in Figure 3.30, soft combining receives the values of RV and NDI. The value of RV is used to decide the position of the corresponding soft bits in the reconstructed code block. When NDI is toggled, it means a new transmission and requires clearing the soft buffer and if not, it means retransmission and does soft combining. Soft combining is specified in Section 3.4.5.

d. CRC

CRC generates ACK/NACK which will be sent back to the transmitter.

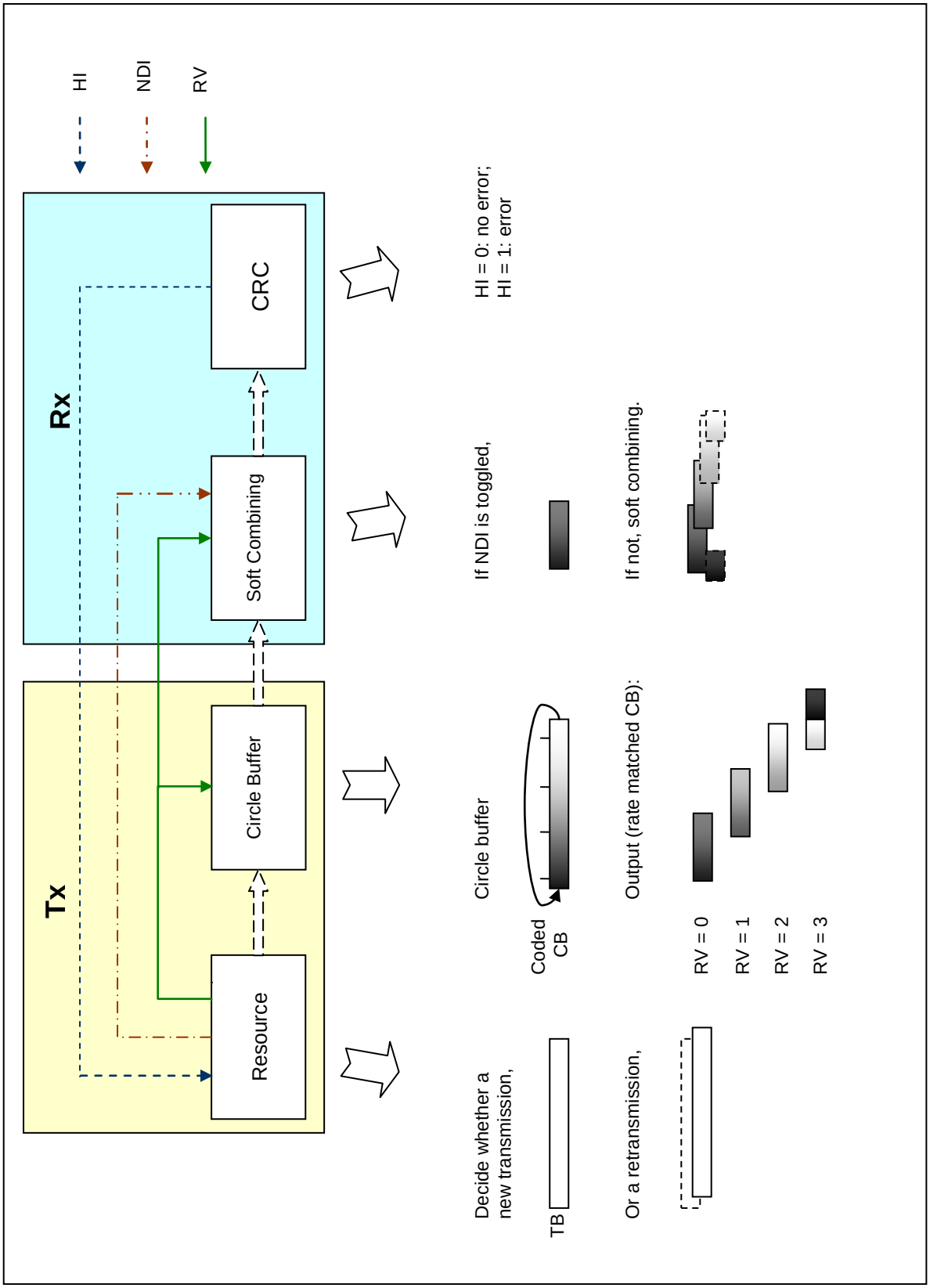


Figure 3.30 *The structure of HARQ in the simulator.*

Figure 3.31 *The HARQ processes of the simulator (QPSK).*

4 Running the LTE Simulator

4.1 Name and functionality

The LTE downlink simulator is shown in Figure 4.1. There are five main functional areas contained in the LTE downlink simulator: configuration area, eNodeB area, channel area, UE area and output area. The name and the functionality of the main components within these areas are listed in Table 4.1.

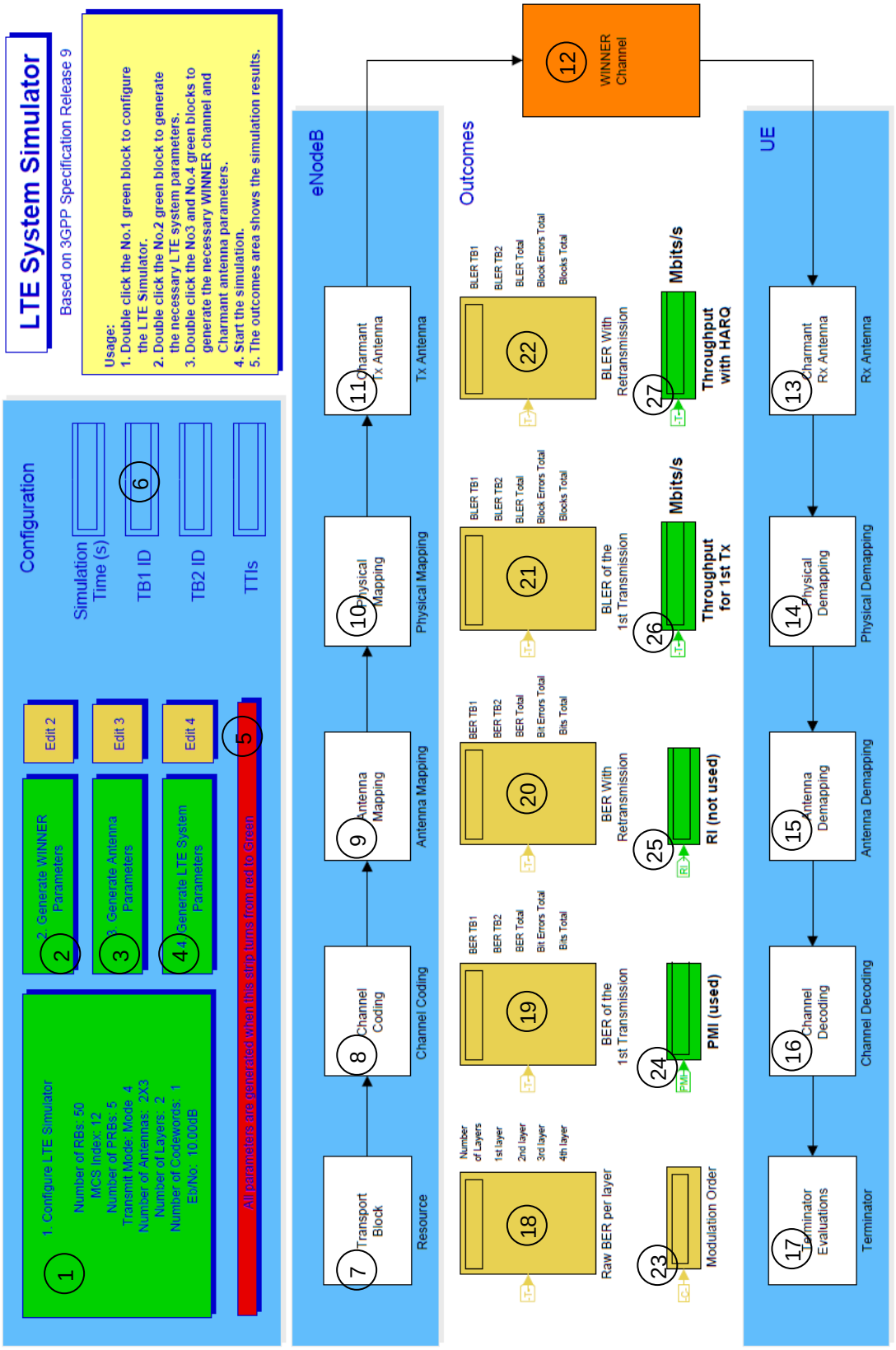


Figure 4.1 *The LTE downlink simulator.*

Table 4.1 *The name and the functionality of main components.*

Index	Name	Main functionality
1	Configuration LTE Simulator	Add the necessary folders to search path. Configure the main parameters of the LTE simulator.
2	Generate WINNER Parameters	Generate the parameters of the WINNER channel.
3	Generate Antenna Parameters	Generate the parameters of the Chase transmit and receive antenna blocks.
4	Generate LTE System Parameters	Generate the parameters of the LTE system.
5	Parameter Status Bar	Show the status of parameter generation. If all the necessary parameters are generated, the strip will turn from red to green.
6	Simulation Time	Show the simulation time, the 1st transport block identity, the 2nd transport block identity and the number of TTIs.
7	Resource	Generate the resource data in the form of transport blocks. Cooperate with HARQ to regenerate the resource data for retransmissions.
8	Channel Coding	CRC attachment, Turbo encoding and rate-matching.
9	Antenna Mapping	Modulation mapping, layer mapping and precoding.
10	Physical Mapping	Physical resource mapping and OFDM implementation.
11	Transmit Antenna	Chase transmit antenna block.
12	Channel Mode	WINNER channel or AWGN channel
13	Receive Antenna	Chase receive antenna block.
14	Physical Demapping	Inverse-OFDM, channel estimator and inverse resource-mapping.
15	Antenna Demapping	Equalizer, PMI calculation, layer de-mapping and demodulation.
16	Channel Decoding	Inverse rate-matching, Turbo decoding, CRC check.
17	Terminator	Calculate the output.

18	Raw BER	Display the number of layers and the raw BER on each layer.
19	BER of the 1st Transmission	Display the BER results of the first transmission. Contain the BER per transport block, the average BER, the number of total error bits and the number of total transmitted bits
20	BER with HARQ	Display the BER results with retransmission. Contains the BER per transport block, the average BER, the number of total error bits and the number of total transmitted bits
21	BLER of the 1st Transmission	Display the BLER results of the first transmission. Contains the BLER per transport block, the average BLER, the number of total error blocks and the number of total transmitted blocks
22	BLER with HARQ	Display the BLER results with retransmission. Contains the BLER per transport block, the average BLER, the number of total error blocks and the number of total transmitted blocks
23	Modulation Order	Display the modulation order.
24	PMI	Display the feedback PMI value from UE to eNodeB.
25	RI	Display the recommended RI from UE.
26	Throughput without HARQ	Display the throughput of the first transmission in Mbits/s.
27	Throughput with HARQ	Display the throughput with retransmissions in Mbits/s.
Note: The AWGN block is added after inverse-OFDM (see the Note in Section 3.4.1.2). Hence, the AWGN channel is implemented within the 'Physical Demapping' block.		

4.2 Usage

1. Set up the simulation stop time and simulation mode of MATLAB Simulink. The recommend simulation stop time is at least larger than 0.032s (32 TTIs); the simulation mode is recommended to the normal mode.

2. Set up the solver type of MATLAB Simulink. The default solver type is set as discrete (no continuous states). If there is no change of solver type, skip this step.

3. In the configuration area of LTE downlink simulator, double click the No.1 green block of 'Configure LTE Simulator' to open the dialog block and set the main parameters of LTE system (see Section 4.3).

4. In the configuration area, in turn double click the No.2-No.4 green blocks to generate the necessary parameters of WINNER channel, antenna and LTE system respectively. The strip of 'Parameter Status Bar' will turn from red to green when all the necessary parameters are generated.

5. Start the simulation.

6. The relative results will be displayed at the output area.

Note:

1. To test the LTE simulator working under different settings of E_b / N_0, once all the necessary parameters are already generated at the first simulation time, only modify the E_b / N_0 parameter in Step 3, and then directly start the simulation to shorten the Simulink initialization time.

2. To test the LTE system under a certain channel scenario, modify the LTE system parameters in Step 3, and then only double click the No.4 green block to regenerate the new LTE system parameters. Do not double click the No.2 and No.3 green blocks to keep a certain channel for each test.

4.3 Set up LTE downlink simulator

The users can configure and modify the LTE system with several parameters at the dialog block (see Figure 4.2 and Figure 4.3 or at the MATLAB m-file of '*LTE_Initialize.m*'.

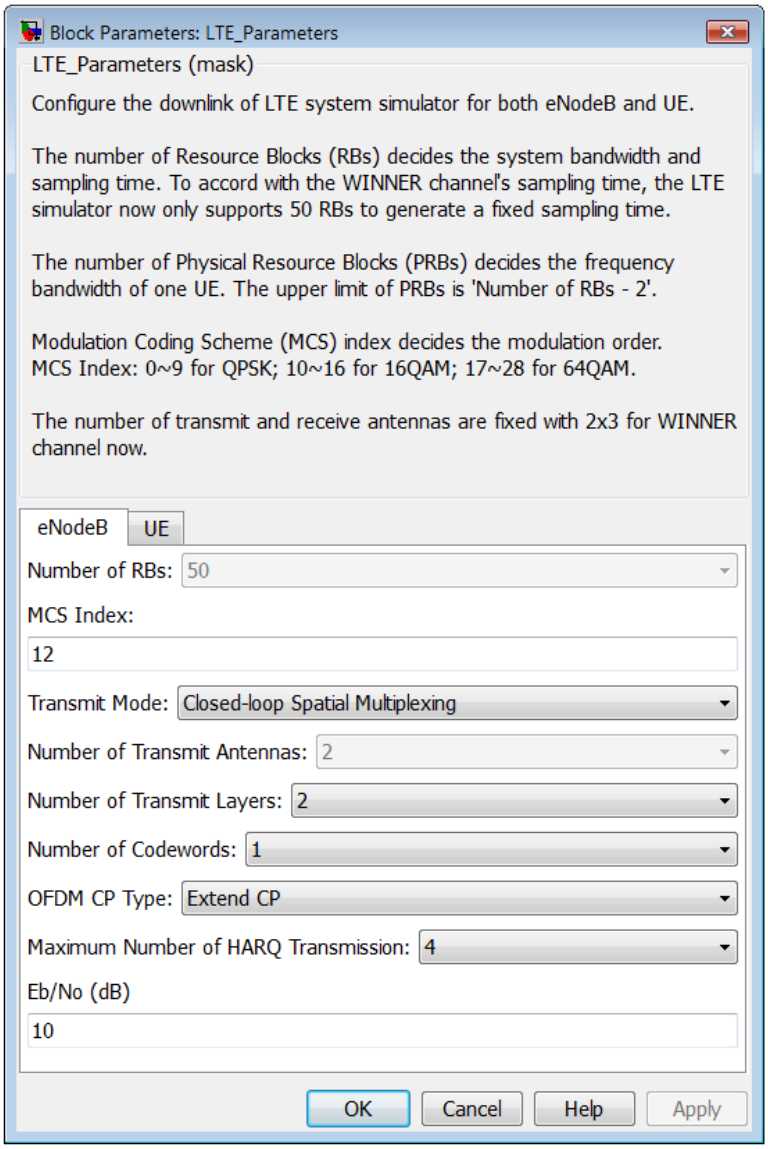


Figure 4.2 *The dialog block of the eNodeB configuration.*

The parameters associated with the eNodeB are listed in Table 4.2.

Table 4.2 *The modifiable parameters of eNodeB.*

Index	Name	Options
1	Number of RBs	50
2	MCS Index	0 - 9 for QPSK modulation mapping; 10 - 16 for 16QAM modulation mapping; 17 - 28 for 64QAM modulation mapping.

3	Transmit Mode	Single antenna Transmit diversity Open-loop Spatial Multiplexing Closed-loop Spatial Multiplexing Multi-MIMO (not supported now) Closed-loop Spatial Multiplexing with a single layer
4	Number of Transmit Antennas	2
5	Number of Transmit Layers	1, 2, 3, 4
6	Number of Codewords	1, 2
7	OFDM CP Type	Normal CP, Extended CP
8	Maximum Number of HARQ Transmission	1, 2, 3, 4
9	Eb/No	Input a value within dB

Note:
1. The number of Resource Blocks (RBs) is fixed to 50 for the WINNER channel. This is because the sampling time of WINNER channel is currently fixed.
2. The Modulation and Coding Scheme (MCS) Index decides the modulation order here.
3. For the transmit mode, the scheme of 'Multi-MIMO' is not being supported now. Since the number of transmit and receive antennas of the WINNER channel is fixed to 2×3, the scheme of 'Single antenna' is not supported for the WINNER channel currently.
4. For the WINNER channel, the number of transmit antenna is currently fixed to 2.
5. The case of 3 layers is not supported now. Moreover, for the WINNER channel, the maximum number of layers is 2 associating to 2 transmit antennas.
6. The number of codewords equals the number of transport blocks that will be transmitted in each TTI.
7. The OFDM Cyclic Prefix (CP) type implies the number of OFDM symbols in one slot. Normal CP implies 7 OFDM symbols in one slot; extended CP implies 6 OFDM symbols in one slot.

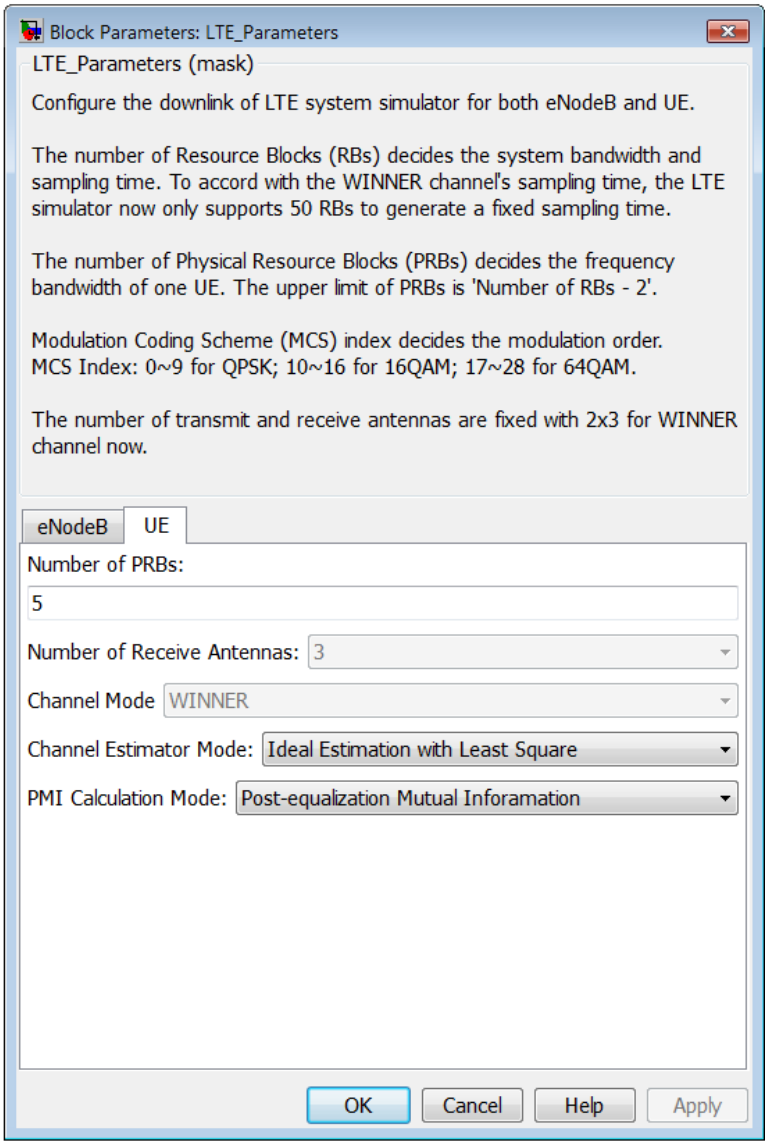


Figure 4.3 *The dialog block of the UE configuration.*

The parameters associating to the UE are listed in Table 4.3.

Table 4.3 *The modifiable parameters of the UE.*

Name	Options
Number of PRBs	1 - 48
Number of Receive Antennas	3
Channel Mode	WINNER channel AWGN channel
Channel Estimator Mode	Ideal Estimation with Least Square Non-ideal Estimation with Least Square Non-ideal Estimation with Modified Least Square

PMI Calculation Mode	Pre-equalization Mutual Information Post-equalization Mutual Information

In addition, the users also can set up the relevant parameters at the MATLAB m-file of '*LTE_Initialize.m*' to modify the LTE system property. The modifiable parameters are listed in Table 4.4.

Table 4.4 *The modifiable parameters at the m-file of 'LTE_Initialize.m'.*

Variable Name	Options	Property
MAC.control _region. .size	$0-4$	Define the number of OFDM symbols in one slot used for transmitting control information.
constDL_RM. start _RB	2 to $N_{RB}^{DL}-n_{PRB}$	Define the start point of resource mapping along the frequency dimension.
constDL_HARQ. RVseq	$\{a,b,c,d\}$	Define the redundancy version coding sequence. Default value: [0 1 2 3] for QPSK or 16QAM and [0 0 1 2] for 64QAM.
constDL_PRECOD. PMI.width_f	1 to $n_{PRB}\times N_{sc}^{RB}$	Define the width of the subsets in frequency domain for calculation of PMI.
constDL_PRECOD. PMI.width_t	1 to $N_{symb}\times 2$	Define the width of the subsets in time domain for calculation of PMI.
constDL_PRECOD. PMI.delay	Integer	Define the delay of PMI feedback in terms of subframe. The default value is 8.

4.4 Connect with new antenna blocks

To connect a new transmit or receive antenna block with the LTE downlink simulator, the input and output of the antenna block should possess the same data structure, and the interface is predefined in Table 4.5. Furthermore, the antenna blocks also have to provide the values of antenna gain (or other necessary information) to the MATLAB m-file named '*LTE_Channel_Setup.m*' to configure the noise variance of the channel.

Table 4.5 *The input and output data structure of antenna blocks.*

	Input and output data structure
Transmit antenna	2 dimensional data, defined as [m_{Tr} , n_{Tr}]. Here m_{Tr} is the number of transmitted symbols on each transmit antenna port, and n_{Tr} is the number of transmit antennas.
Receive antenna	2 dimensional data, defined as [m_{Re} , n_{Re}]. Here m_{Re} is the number of received symbols on each receive antenna port, and n_{Re} is the number of receive antennas.

5 Simulation Results

To validate the LTE downlink simulator, some simulation results with an AWGN channel are presented in this section. The validations under different scenarios include:

- The raw BER and coded BER for different modulation schemes.
- The throughput before and after HARQ for different modulation schemes.
- The raw BER for different channel estimation modes.
- The raw BER for different transmission modes.
- The throughput before and after HARQ for different transmission modes.

The basic settings of LTE system for all scenarios are configured in Table 5.1.

Table 5.1 *The Basic configurations of LTE system for all scenarios.*

Simulation time	1 second (1000 TTIs)
Number of RBs	50
Number of PRBs	10
Modulation schemes	QPSK (MCS = 6, TBS = 1032); 16QAM (MCS = 12, TBS = 2024); 64QAM (MCS = 28, TBS = 3240).
OFDM CP mode	Normal CP
PMI calculation mode	Post-equalization mutual information
Maximum HARQ transmissions	4

Figure 5.1 shows the raw BER and coded BER for different modulation schemes. The simulator works with the SISO transmission mode and the ideal channel estimation mode. As illustrated, the channel coding can provide a larger coding gain for the modulation scheme with higher modulation order.

In Figure 5.2, under -7dB, the throughput of the QPSK is larger than the 16QAM and 64QAM. Hence, the scheduling and the link adaptation are two very important techniques under such scenarios.

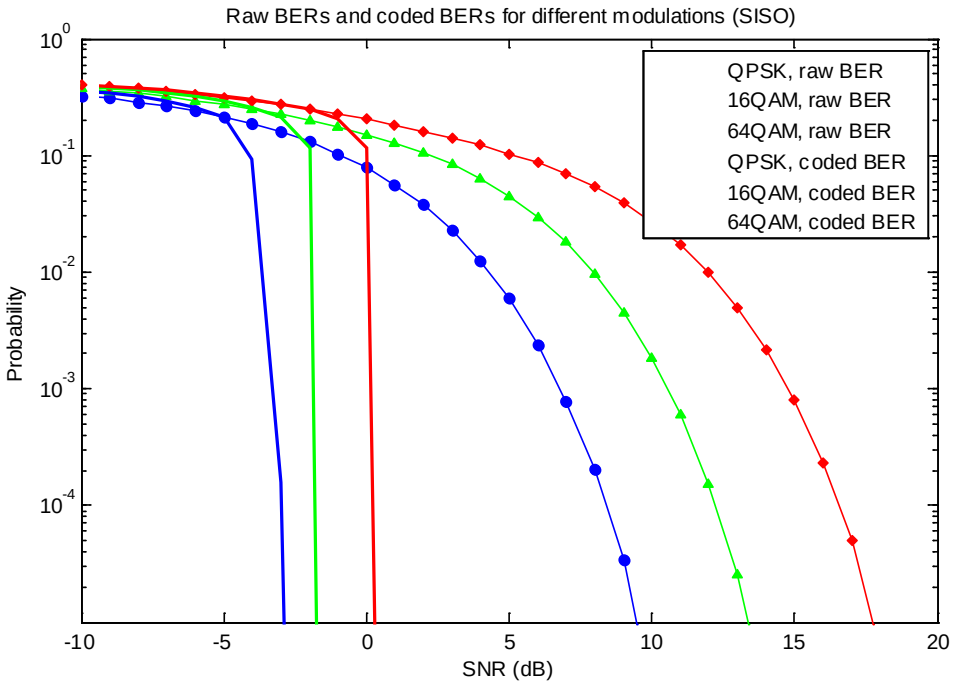


Figure 5.1 *The raw BER and coded BER for different modulations, with SISO transmission mode and ideal channel estimation.*

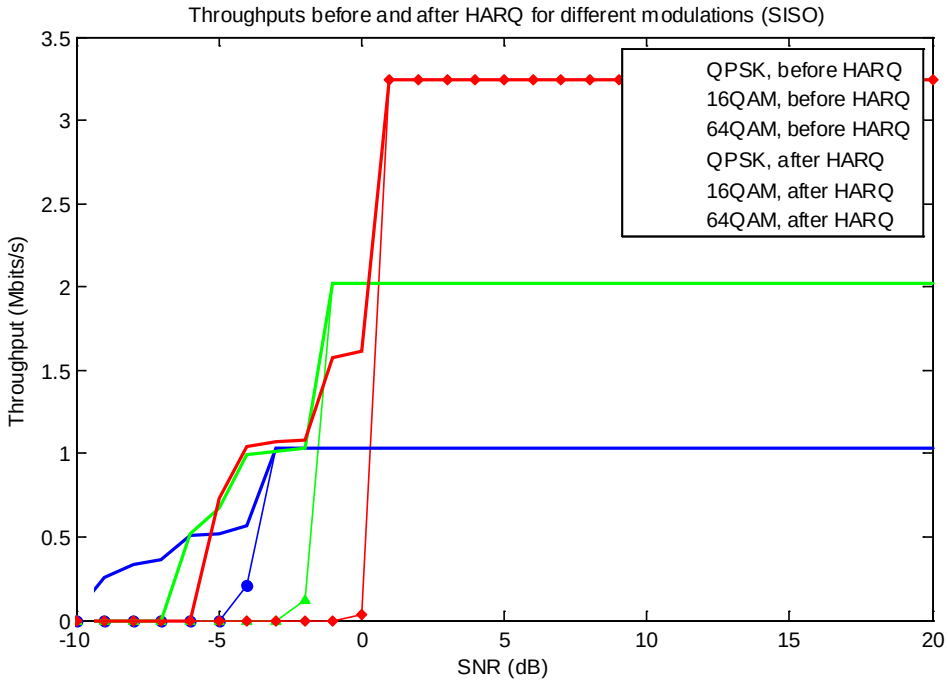


Figure 5.2 *The throughput before and after HARQ, with SISO transmission mode and ideal channel estimation mode.*

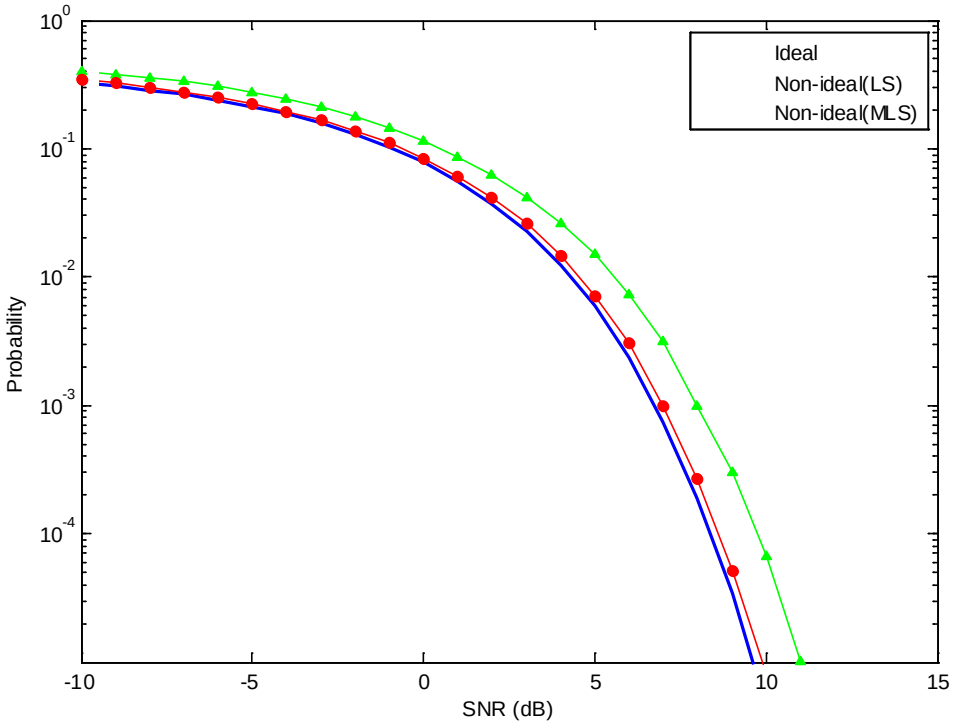


Figure 5.3 *The raw BER for different channel estimation modes.*

Figure 5.3 shows the performance of the ideal and non-ideal channel estimation. Here, the modified LS estimator is closer to the ideal channel estimation than the LS estimator.

The raw BER for different transmission modes can be seen in Figure 5.4. Obviously, the transmission with transmit diversity mode can win around 3dB gains to the closed-loop spatial multiplexing mode and the SISO mode. Due to the fact that the channel is an AWGN channel, there is not much difference of gain between the 2×2 and the 4×4 antenna modes.

Figure 5.5 correspondingly shows the throughput for different transmit modes. The two transmit diversity modes possess the same maximum throughput as the SISO mode under such a scenario, whereas the necessary SNR is lower for the transmit diversity mode. The closed-loop spatial multiplexing modes can achieve a higher throughput, but they need a similar SNR as SISO mode to keep a stable throughput.

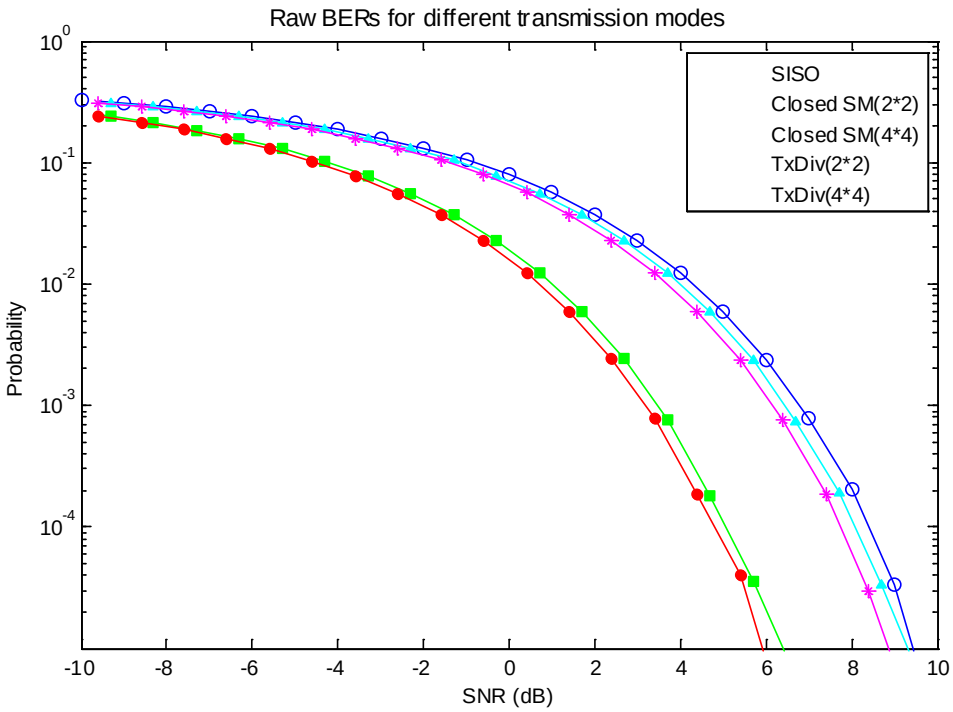


Figure 5.4 *The raw BER with different transmission modes.*

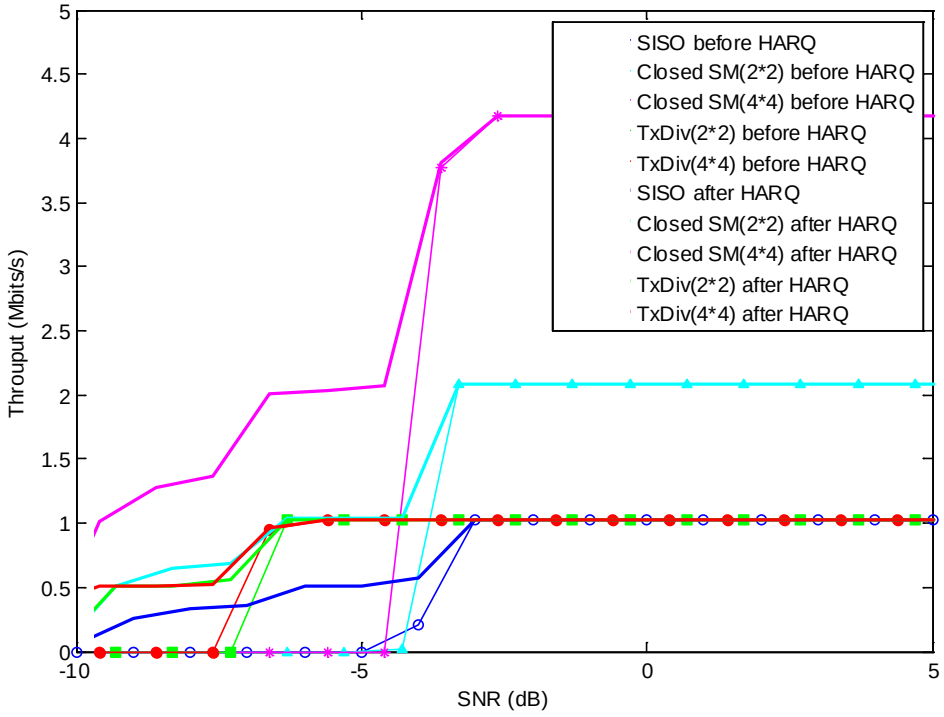


Figure 5.5 *The throughput before and after HARQ with different transmission modes.*

6 Conclusions and Future work

In this thesis a Simulink-based LTE downlink simulator has been developed, which is mainly used to simulate the Physical Downlink Shared Channel.

Both the transmitter and the receiver of the LTE downlink are implemented. The transmitter is implemented according to the specifications. It supports turbo coding, rate matching, 4×4 MIMO and OFDM. The available MIMO schemes include closed loop spatial multiplexing, open loop spatial multiplexing, transmit diversity and codebook-based beam forming. A basic receiver is also presented, which supports three channel estimation modes and linear equalizers. Moreover, a simplified HARQ functionality is available. A performance evaluation of the simulator in an AWGN channel, in terms of raw BER, BER, BLER and throughput, is presented in the current report. Existing antenna blocks and the WINNER channel are also connected to the system.

Though the simulator successfully supports most basic functionalities of the LTE system in the physical layer, it can be improved in a future work. For example, the code in the Embedded MATLAB functions can be optimized to speed up the simulation. As the use of the Simulink-based WINNER channel is limited (it only supports 2×3 antennas), the test of the simulator in fading channel is yet not done. This test is an important task in the next stage.

Finally, regarding functionalities of the LTE system, the following work items are suggested:

- LTE uplink,
- Feedback of CQI and RI from the UE,
- Multi-user MIMO,
- Scheduling,
- Link adaptation,
- Consider the issues of synchronization and adaptation to improve the HARQ functionality in LTE downlink.

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